# Jasper Rühl

24.07.1998

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### **EXPERIENCE**

#### **Engineer Cyber Analytics & Defense**

Nov 2024 - present

fernao magellan

part of Cyber Analytics & Defense Team Python, Splunk, Elastic, Terraform

+ maintain and develop platform operation service

Working Student May 2023 - Oct 2023

Guardsquare

prototyped dependency detection feature for iOS AppSweep Python, C++

- + statically linked libraries are successfully detected
- + each symbol of the iOS app is matched to its origin (native or external library)
- + repository of libraries is automatically built and necessary information is extracted

Working Student Mar 2022 - April 2023

Controlware

DevOps for SOC team's TheHive Infrastructure, creating various tools Python

- + Incident importer for several XDR platforms (MS Defender, SentinelOne, Cortex XDR)
- + IntelCaching that periodically parses info Wikipage for the AutoSOC/MailTextGenerator
- + MailTextGenerator that uses easily editable text templates and fills them with incident info
- + AutoSOC that automatically resolves trivial MS Defender incidents
- + introduced function decorator to make our python programs significantly more fault resilient

Research Assistant Jan 2021 - Dec 2021

Fraunhofer AISEC

aided in development of an LLVM based MemSafety tool  $c_{++}$ ,  $p_{\text{ython}}$ 

developed PoC's for anti-ControlFlowIntegrity exploitation techniques (DOP, COOP, LOP)

Student Tutor WS21-22 / WS22-23

Chair of IT Security

hosted weekly tutorials on the IT Security lecture

- $\boldsymbol{+}$  presented and taught students about the fundamentals of IT Security
- + classes ranged from 5-25 students

### **PROJECTS**

#### Congestion Control for Application Flows on Shared QUIC connections Masther Thesis

SS24

explore methods to enable lower latency for realtime communication  $_{\mbox{\scriptsize Go}}$ 

+ add a stream prioritization mechanism to quic-go

#### Risotto: A DBT for Weak Memory Models Guided Research

published at ACM ASPLOS 2023

- improved emulation of x86 cmpxchg instruction on ARM architectures c
- + introduced a new CAS instruction for QEMUs TCG
- $+ \ \mathsf{appropriate} \ \mathsf{ARM} \ \mathsf{instruction} \ \mathsf{is} \ \mathsf{generated}$
- + dl.acm.org/doi/10.1145/3567955.3567962

#### Raspberry Pi VPN Endpoint Interdisciplinary Project

SS23

Gürtler & Roach Cybersecurity

developed program to setup Raspberry Pi microcomputers

- + Pi's are a tailscale exit node
- + Access Control List ensures no outgoing connections from the Pi
- + Ansible is used to automatically setup the Pi

Forkever Bachelor Thesis SS20

GDB-like debugger for binary exploitation Python, C

- + create copies of program-state by injecting fork system calls
- + memory can be visualised and manipulated with a hexeditor
- + Forkever is used by the students of the binary exploitation lab course
- + github.com/haxkor/forkever

# **EDUCATION**

2021 - 2024
2016 - 2020
2018 - 2019

# **SKILLS**

Programming Languages
Python, Go, C, C++, Bash
Technologies
Splunk, Elastic, Docker, QEMU, Ansible,
Terraform, git, Linux,
Languages
German, English, Malay (basic)

# **PERSONAL**

MINGA	2017 - 2020
Mentor for two international students at TUM	
English tutor Assisted in teaching pupils	2013 - 2015