Don Bosco Institute of Technology, Kurla

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EXPERIMENT NO:3

Title: A program to simulate Booth's multiplication

Class:S.E Lecturer:Sejal.Ch

Subject: PA

EXPERIMENT NO: 3 Simulate Booth's Algorithm

Simulate Booth's Algorithm							
AIM	Write a program to simulate Booth's multiplication						
LEARNING	To implement the operation of the arithmetic unit including						
OBJECTIV	the implementation of fixedpoint multiplication for signed						
E	numbers.						
LEARNI	Students will be able to write a higher level language code						
NG	for simulating hardware operation for Booth's						
OUTCO	multiplication process.						
ME							
	CSL 403.1: Ability to compile a code for computer operations.						
LAB	del restriction of compare a code for compared operations.						
OUTCOME							
PROGRAM	PO11,						
OUTCOME	PO52,						
00100112	PO83,						
	PO93,						
	PO122,						
	PSO12						
	Remembe						
BLOOM'S	r,						
TAXONOMY	Understa						
LEVEL	nd						
LLVLL							
	Booth's multiplication algorithm is a multiplication algorithm						
	that multiplies two signed binary numbers in two's						
	complement notation.						
THEORY							
	Booth algorithm gives a procedure for multiplying binary						
	integers in signed 2's complement representation in efficient						
	way, i.e., less number of additions/subtractions required. It						
	operates on the fact that strings of 0's in the multiplier						
	require no addition but just shifting and a string of 1's in the						
	multiplier from bit weight 2 ^k to weight 2 ^m can be treated						
	as $2^{(k+1)}$ to 2^m .						
	As in all multiplication schemes, booth algorithm requires						
	examination of the multiplier bits and shifting of the partial						
	product. Prior to the shifting, the multiplicand may be added to						
	the partial product, subtracted from the partial product, or left						
	unchanged according to following rules:						
	algorithm:						
	1. Take two decimal numbers from the user in the range of 0 to						
	15,both positive and negative(M=Multiplicand & Q=Multiplier)						

and convert it to 4- bit binary numbers.

2. Negative numbers are represented in two's complement form.

3. Intialise the counter with the count of number of bits.

4.Intialise the one bit register Q-1 as 0.

5.Intialise A to zero ,where A is accumulator which stores the MSB of the result . Multiplier Q stores the LSB of the result.

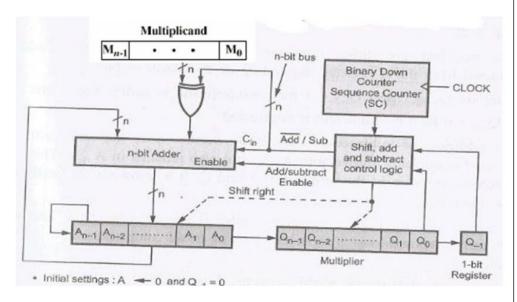
6.Check LSB bit of Q and Q-1:

a.If they are 11 or 00 arithmetic shift is done for A,Q and Q-1 and decrement the counter.

b.If they are 01 add A to M and then arithmetic shift is done for A,Q and Q-1 and decrement the counter.

c.If they are 10 sub M from A and then arithmetic shift is done for A,Q and Q-1 and decrement the counter.

7. Check the counter, if it is not 0, move to step 6, otherwise store the result in A and Q.



SOFTWARE USED

SOFTWARE C/C++/Java

STEPS TO
EXECUTE
THE
PROGRAM

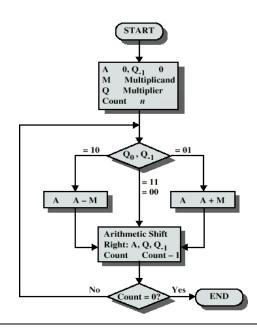
- 1. Take two decimal numbers from the user in the range of 0 to 15,both positive and negative(M=Multiplicand & Q=Multiplier) and convert it to 4 bit binary numbers.
- 2. Negative numbers are represented in two's complement form. 3.Intialise the counter with the count of number of bits .
- 4. Intialise the one bit register Q1 as 0.
- 5. Intialise A to zero ,where A is accumulator which stores the MSB of the result . Multiplier Q stores the LSB of the result.
- 6. Check LSB bit of Q and Q1 : a.If they are 11 or 00 arithmetic shift is done for A,Q and Q1 and $\frac{1}{2}$

decrement the counter.

- b. If they are 01 add A to M and then arithmetic shift is done for A,Q and Q1 and decrement the counter.
- c. If they are 10 sub M from A and then arithmetic shift is done for A,Q and Q1 and decrement the counter.
- 7. Check the counter, if it is not 0, move to step 6, otherwise store the $\,$

result in A and Q.

FLOWCHART



EXAMPLE

Counter	A	Q	Q_{-1}	Operations	
4	0000	0011	0	Initial Values	
3	1001	0011	0	A A-M}	First
	1100	1001	1	A A - M } Shift	First Cycle
2	1110	0100	1	Shift }	Second Cycle
1	0101	0100	1	A A + M }	Third Cycle
	0010	1010	0	A A + M }	Cycle
0	0001	0101	0	Shift }	Fourth Cycle

CODE

3A:

```
m=int(input())
md=int(input())
a='00000'
qm1='0'
l=1
d=bin(int(1))[2:].zfill(5)
counter=4
if(m<0):
     print("m is lesser theab 0")
     m^* = -1
     c=bin(m)[2:].zfill(5)
     c = c.replace('0', '.')
     c = c.replace('1', '0')
c = c.replace('.', '1')
     m=str(bin(int(c,2) + int(d,2))[2:]).zfill(5)
else:
     m=bin(m)[2:].zfill(5)
if(md<0):
     l=1
     md*=-1
     c=bin(md)[2:].zfill(5)
     c = c.replace('0', '.')
     c = c.replace('1', '0')
     c = c.replace('.', '1')
     md=str(bin(int(c,2) + int(d,2))[2:]).zfill(5)
else:
     md=bin(md)[2:].zfill(5)
c=m
c = c.replace('0', '.')
c = c.replace('1', '0')
c = c.replace('.', '1')
print("c { } ".format(c))
twos=str(bin(int(c,2) + int(d,2))[2:]).zfill(5)
counter=len(md)-1
print("a\t md \t q-1 \t counter\toperartion")
print("{} {}
                {}
initial".format(a,md,qm1,counter+1))
op1=md[-1]
```

```
op2=qm1
while(counter!=-1):
    if( op1=='0' and op2=='0'):
          qm1=md[-1]
          temp=md;
          md=a[-1]+temp
          md=md[0:len(md)-1]
          a = a[0] + a
          a=a[0:len(a)-1]
          print("{} {}
                        {}
                                  {}
                                        ARITHMETIC
RIGHT SHIFT".format(a,md,qm1,counter))
    if( op1=='1' and op2=='1'):
          qm1=md[-1]
          temp=md;
          md=a[-1]+temp
          md=md[0:len(md)-1]
          a = a[0] + a
          a=a[0:len(a)-1]
          print("{} {}
                       {}
                                  {}
                                        ARITHMETIC
RIGHT SHIFT".format(a,md,qm1,counter))
    if( op1=='1' and op2=='0'):
          a=str(bin(int(a,2) + int(twos,2))[2:])
          a=a.zfill(5)
          a=a[-5:]
          print("{} {}
                                  {}
                                        A-
                         {}
B".format(a,md,qm1,counter))
          qm1 = md[-1]
          temp=md;
          md=a[-1]+temp
          md=md[0:len(md)-1]
          a = a[0] + a
          a=a[0:len(a)-1]
          print("{} {}
                                        ARITHMETIC
                                  {}
                         {}
RIGHT SHIFT".format(a,md,gm1,counter))
    if(op1=='0' and op2=='1'):
          a=str(bin(int(a,2) + int(m,2))[2:])
```

```
a=a.zfill(5)
     #
           a=a[1:6]
           a = a[-5:]
           print("{} {}
                                     {}
                           {}
A+B".format(a,md,gm1,counter))
           qm1=md[-1]
           temp=md;
           md=a[-1]+temp
           md=md[0:len(md)-1]
           a=a[0]+a
           a=a[0:len(a)-1]
           print("{} {}
                         {}
                                     {}
                                           ARITHMETIC
RIGHT SHIFT".format(a,md,qm1,counter))
     counter=1
     op1=md[4]
     op2=qm1
cases:
. 2
c 00110
                 q-1
                                        operartion
         md
                         counter
00000
       11110
                 0
                              5
                                     initial
00000
      01111
                 0
                              4
                                     ARITHMETIC RIGHT SHIFT
00111
                 0
                              3
00011
                                     ARITHMETIC RIGHT SHIFT
                 1
                              3
00001
      11011
                 1
                              2
                                     ARITHMETIC RIGHT SHIFT
00000 11101
                 1
                              1
                                     ARITHMETIC RIGHT SHIFT
00000 01110
                              0
                                     ARITHMETIC RIGHT SHIFT
 11000
                          counter
                                         operartion
         md
                 q-1
00000
       00010
                 0
                               5
                                      initial
00000
       00001
                 0
                               4
                                      ARITHMETIC RIGHT SHIFT
                 0
                               3
11001
       00001
11100
       10000
                 1
                               3
                                      ARITHMETIC RIGHT SHIFT
00011
       10000
                 1
                               2
                                      A+B
                               2
00001
                 0
                                      ARITHMETIC RIGHT SHIFT
       11000
00000
       11100
                 0
                               1
                                      ARITHMETIC RIGHT SHIFT
00000 01110
                 0
                                      ARITHMETIC RIGHT SHIFT
```

```
c 00110
                   q-1
a
          md
                           counter
                                            operartion
00000
        00010
                                         initial
                   0
                                 5
00000
                                 4
                                         ARITHMETIC RIGHT SHIFT
        00001
                   0
00111
        00001
                   0
                                 3
                                 3
                                         ARITHMETIC RIGHT SHIFT
00011
        10000
                   1
11100
       10000
                   1
                                 2
                                         A+B
11110 01000
                   0
                                 2
                                         ARITHMETIC RIGHT SHIFT
                                 1
                                         ARITHMETIC RIGHT SHIFT
11111
       00100
                   0
       10010
                   0
                                 0
                                         ARITHMETIC RIGHT SHIFT
 11101
          md
                           counter
                                            operartion
                  q-1
00000
       11001
                  0
                                 5
                                         initial
                                 4
11110
       11001
                  0
                                         A-B
11111
                  1
                                 4
                                         ARITHMETIC RIGHT SHIFT
       01100
00001
       01100
                  1
                                 3
                                         A+B
00000
                  0
                                 3
                                         ARITHMETIC RIGHT SHIFT
       10110
                                 2
00000
                  0
                                         ARITHMETIC RIGHT SHIFT
       01011
11110
       01011
                  0
                                 1
                                         A-B
11111
                                 1
                                         ARITHMETIC RIGHT SHIFT
       00101
                   1
11111 10010
                                         ARITHMETIC RIGHT SHIFT
3B:
a=int(input())
lis=[]
if(a<0):
      a^* = -1
      a=bin(a)[2:]
      a="1"+a+"0"
      print(a)
else:
      a=bin(a)[2:]
      a="0"+a+"0"
def compare(a,b):
      if(a=='0' and b=='1'):
            return 1
      elif(a=='1' \text{ and } b=='1'):
            return 0
      elif(a=='1' \text{ and } b=='0'):
            return -1
```

```
for i in range(1,len(a)):
                   lis.append(int(compare(a[i-1],a[i])))
              print(*lis)
             for i in range(0, len(a)-1, 2):
                   print(2*lis[i]+lis[i+1],end="")
               1110110
                 0 -1 1 0 -1
                  -1 -1 (base)
                    0 -1 1 0 -1
                   -1 -1 (base)
              We have successfully implemented booths algorithm and
CONCLUSIO
              carried out signed multiplication . Also several test cases were
N
              taken and verified. Extra scope of bit pair re encoding was
              also successfully implemented
                   William Stallings, "Computer Organization and
REFERENCE
              Architecture: Designing for Performance", Pearson
S
              Publication, 10 th Edition, 2013
              2. B. Govindarajulu, "Computer Architecture and
                Organization: Design
              Principles and Applications", Second Edition, McGrawHill
              (India)
```