| N | am | Δ. |
|----|----|----|
| ΤJ | am | v. |

Note: (Late: -15% Penalty).

1) (15 pts) Write a brief essay paragraph explaining one breakthrough in the history of computing and the significance of the breakthrough to advancing Computer Science.

Acceptable answers include explanations of vacuum tubes, transistors, integrated circuits, VLSI, binary arithmetic, quantum computing, and parallel computing.

Ans. See bockside for Work

2) (15 pts) Using the CRC polynomial 1101, compute the CRC code for the information word, 1 0110 1101

Ans:

see back side for Work

3) (10 pts) Convert the following decimal fractions to binary with a maximum of six places to the right of the binary point:

a. 26.78125

See backside for Work

ь. 194.03125

See backside for Work

4) (15 pts) Define the following

a. Combination Logic:

Combination Logic takes inputs and runs them through logical gates.

b. Sequential Logic:

Sequential Logic takes inputs and loops some of Homin a second or whore times. So there is like a state which effects the output of the next round of inputs.



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Name:

c. How are sequential circuits different than combinational circuits?

Sequential circuits depend on the last in put. sort

of like a state. It holds on to previous inputs and they affect cure

5) (10 pts) Add the following 8-bit two's complement numbers (i.e. one sign bit and Input.

seven data bits) AND indicate "Overflow" if it occurs.

Answer: No overflow answer = 11001010

b. 0110 1011
+ 0101 0101
$$1100^{0000}$$
 $32 + 8 + 2 + 1$
 $010 + 0 + 1 = 43$

Answer: There is overflow because we added two postike numbers and got a negative

6) (15 pts) Given the boolean equation: $x^{\dagger}yz + x(yz)' + x'(y+z) + (xyz)'$ a. Construct the truth table for the Boolean equation:

| X | у | Z | F(xyz) |
|----|---|---|--------|
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 1 |
| 0 | l | 0 | 1 |
| 0 | 1 | ١ | 1 . |
| | 6 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1: | ı | 0 | 0 |
| 1 | 1 | ı | 0 |

Name:

b. Construct the K-map for this circuit.

| xy 00 01 11 10 z 0 1 0 0 | | | | | 10 |
|---------------------------|----|----|----|----|----------|
| z_0 0 0 | VV | 00 | 01 | 11 | 10 |
| | آ | | | 0 | |
| | 1 | ı | 1) | 0 | Ø |

c. Write a reduced but equivalent Boolean equation. Use either the K-map or apply the Boolean postulates/theorem's to do your reduction.

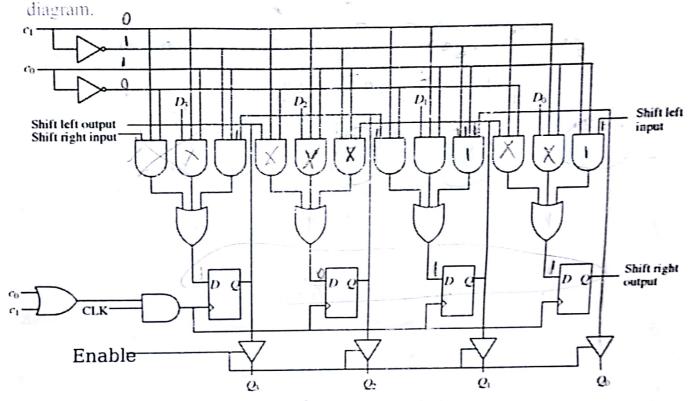
7) (15 pts) Assume a 2 byte memory:

b. What are the lowest and highest addresses if memory is word-addressable, assuming a 16-bit word?

c. What are the lowest and highest addresses if memory is word-addressable, assuming a 32-bit word?

8) (5 pts) Given the left-right shift register shown below answer the following questions. Place an 'x' for don't care conditions on the non-relevant outputs.

Note: Each Control input 'e0 and e1' as listed in the function table is shown twice in the



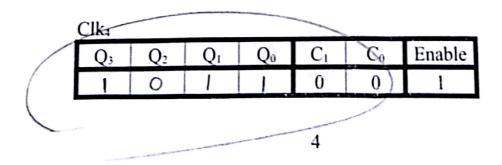
| Control | Function | |
|------------------------------|---|---|
| 0 0 0 1 1 0 1 1 | No change Shift left Shift right Parallel load | Shift right input D_3 D_2 D_1 D_0 Shift Shift left output D_3 D_2 D_1 D_0 D_0 Shift D_1 D_2 D_1 D_0 D_1 D_0 Shift D_2 D_1 D_0 D_1 D_0 D_1 D_0 D_1 D_0 Shift D_1 D_1 D_0 D_1 D_1 D_0 D_1 D_1 D_0 D_1 |

a. Fill in the state output of Q₀-to-Q₃ on clock Clk₄ given the input 'Enable=1' AND the following Input serial load of the shift-register in clocks Clk₀-to-Clk₃

right output left input

Clko-to-Clk3

| | Clk_0 | Clk_1 | Clk ₂ | Clk ₃ | C_1 | C_0 |
|-------------------|---------|---------|------------------|------------------|-------|-------|
| Shift Right Input | 0 | | 0 | 0 | 0 | 1 |
| Shift Left Input | 1_ | 0 | 1 | 1 | 0 | 1 |





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9) (15 pts) Using the MarieSim emulator - Write working assembly code that executes the logical programming construct:

Turing machines and Finite State Machines will continue until told to 'halt'. Please halt your code upon completion of the assigned task. Include comments to your code!

Initialize A=5; B=9;

UNTIL $(A \ge B)$! Until A is greater than or equal to B

DO

C = A + B! Add A to B STORE C ! store in C

! Subtract 1 from A A=A-1! Subtract 2 from B B=B-2

DONE

Answer:

See Backside For Wark

10) (15 pts) Using the MarieSim emulator - Write a working assembly code that executes the logical programming construct:

Turing machines and Finite State Machines will continue until told to 'halt'. Please halt your code upon completion of the assigned task. Include comments to your code!

See Bockside For Work

IF(A = B)

THEN

C = A + B

ELSE C = A - B

Answer:

11) (15 pts) Using the MarieSim emulator - Write working assembly code that executes the logical programming construct:

Include comments to your code!



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Write a working assembly code that uses <u>stack parameter</u> passing linkage to call a subroutine labeled "sub_1" to subtract two numbers and return the results to the main routine. t = (r - s)

Your main routine must:

- a. load your two parameters (r,s) from memory and push them onto your stack.
- b. call sub_1
- c. pop the result from your stack and store it into memory label 't: '.

Your sub-routine must:

- a. pop your two parameters from your stack
- b. subtract the parameters as shown in the problems formula
- c. push the result onto the stack
- d. return to your 'main' routine

Please initialize 't: 0' to start your code. t = (r - s)Use care not to let your memory stack step on your code.

Answer:

12) (15 pts) Using the MarieSim emulator - Write working assembly code that executes the array initialization C code shown below:

Include comments to your code!

```
#include <stdio.h>
int main () {
  int n[ 10 ]; /* n is an array of 10 integers */
  int i;

/* initialize elements of array n to 0 */
  for ( i = 0; i < 10; i++ ) {
     n[ i ] = i + 100; /* set element at location i to i + 100 */
  }

return 0;
}</pre>
```

See Backside For Work

#1.) Timeline of Mechinial Relays, Vacuum Tubes and Transistors

Michinical celays were used to transmit data in pre 1940's era. They were implemented by sending electric current through a coil which in return closed a circuit wire switch via attraction by a magnetic field. Open circuit meant I and closed meant 0. The gate was an actual tiny metal rod with mass so that made it difficult for it to swing back and fourth at high rotes. The engineers needed it to swing fact inorder for it to send bits at faster rotes. Michineal relays had a frequency of about 50Hz which was not fast enough.

Vacuom Tubes were a huge improvement over mechnical relays because they didn't have any swinging parts and were much faster. Vacuum tubes could reach +5000 tz frequencies. They were used in the first general purpose computer the ENIAC in 1945 designed by John Mauchly and colleague J. fresper Eckert. There was also konrad Zuse an engineer with the same idea but he couldn't use vacoum tubes because his too expensive and hard to assemble in comparison to their predecesor the Transister.

Transisters were much smaller (sometimes a million times smaller) and nuch more durable. They are solid components which means no complex limer assembly, could be smaller and also much faster than vacuum tubes. They are good enough to be still in use today and their small size is what made it possible for todays computers to be so compact and small.

#2.)

#3.) a.) 26,78125

$$26 | 78125$$

$$21/2 = |3| | |0| | |0.78125 \times 2| = |1| |5625| |$$

$$13/2 = 6 | |1| |0| |0.1250 \times 2| = |1| |1250| |$$

$$0.1250 \times 2| = |0| |2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500| |0.2500|$$

b.)

11000010,000010

#9.)

```
ORG 100 //
                     //beginning of loop and load A in the accumulator
loop, LOAD
            Α
                     //Add A + B (A is inside accumulator)
     ADD
            В
                     //Store the sum in C
            C
     STORE
                     //Load A back in to the accumulator
     LOAD
            Α
                     //A - 1 is now in the accumulator
     SUBT
           one
                     //A = accumulator value
     STORE
             Α
                     //Load B back in to the accumulator
     LOAD
            В
                     //B - 2 is now in the accumulator
     SUBT
            two
                     //B = accumulator vaue
     STORE
            В
                     //LOAD A back into the accumulator
     LOAD
             Α
                     //Subtract acc from B and put result in accu
     SUBT
            В
                 800 //if A - B > 0 that means \dot{A} >= B so stop(skip) loop
     SKIPCOND
                 loop //restart from the beginning of the loop
     JUMP
                      //Stop the Program
     HALT
               DEC 5 // A = 5
     Α,
                      // B = 9
               DEC 9
     В,
                      // C = 0
               DEC 0
     С,
                     // one = 1
               DEC 1
     one,
               DEC 2 // two = 2
     two,
```

#10.)

```
//start from origin
    ORG 100
                    //load value A into accumulator
    LOAD A
                    //subtract B from the accumulator
    SUBT B
                    //If A - B = 0 skip the else jump
    SKIPCOND 400
     JUMP else
     //THEN CLAUSE STARTS HERE
                    //A goes into the accumulator
                    //B is added to A
     ADD B
                    //Store the result in B
     STORE C
                    //Stop the program
     HALT
     //THEN CLAUSE ENDS HERE
                     //load A into the accumulator
else,LOAD A
                    //subtract B from the accumulator
     SUBT B
                    //Store the result in c
     STORE C
                     //Stop the Program
     HALT
          DEC 5//A = 5
     Α,
     В,
          DEC 9//B = 5
          DEC 0 // C = 0
     С,
```

#12)

| | | ORG 1 | 100 | | |
|---|------------------------------------|--|-----|--|---|
| f | orloop, | LOAD ADD STOREI LOAD ADD STORE CLEAR LOAD SUBT STORE SKIPCON JUMP HALT | ID | count oneHun index index one index size one size 400 forloop | <pre>//count starts at 0 //Add 100 to the count //STORE it in Array[index] //LOAD index //index + 1 //index = index + 1 //clear the accumulator //load the size(10) in accumulator //size - 1 //size = size -1 //if size - 1 = 0 break for loop</pre> |
| | oneHun, one, size, index, | DEC DEC DEC HEX | | 100 1 10 112 | <pre>//oneHun = 100 //one = 1 //initial size = 10 //start of array @index=112 in hex</pre> |