

Haytham Fayek

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Education

RMIT University , Australia Ph.D., Electrical and Computer Engineering	Mar 2015 to Feb 2019
Petronas University of Technology , Malaysia M.Sc. (Research), Electrical and Electronics Engineering	May 2012 to Jul 2014
B.Eng. (Hons), Electrical and Electronics Engineering	Jan 2007 to Jan 2012

Experience

Facebook , United States Postdoctoral Research Scientist	Aug 2018 to Present
RMIT University , Australia Casual Academic	Mar 2016 to Aug 2018
Facebook , United States Research Intern	Oct 2016 to Mar 2017
Approved Forensics , Malaysia Assistant Electrical Engineer	Apr 2012 to Mar 2014
Ranhill WorleyParsons , Malaysia Electrical Trainee Engineer	Jun 2010 to Jan 2011

Selected Honours

- NVIDIA Hardware Grant, 2015.
- RMIT Vice Chancellor's PhD Scholarship (VCPS), 2015 to 2018.
- Gold Medal, 24th International Invention, Innovation & Technology Exhibition (ITEX'13), Malaysia, 2013.
- Runner-up Best Young Author, 18th International Conference on Methods & Models in Automation & Robotics, Poland, 2013.
- First Place, Vice Chancellor's Best Final Year Project, Convocation Ceremony, Petronas University, 2012.
- Silver Medal, Vice Chancellor's Best Student, Convocation Ceremony, Petronas University, 2012.
- Petronas & MOPE BEng Scholarship, 2007 to 2012.

Selected Publications

1. **Haytham M. Fayek**, "Continual deep learning via progressive learning," *PhD thesis*, RMIT University, Australia, February 2019.
2. **Haytham M. Fayek**, Lawrence Cavedon, and Hong Ren Wu, "On the transferability of representations in neural networks between datasets and tasks," *Continual Learning Workshop, 32nd Neural Information Processing Systems (NeurIPS)*, Montréal, Canada, December 2018.
3. **Haytham M. Fayek**, "MatDL: A lightweight deep learning library in MATLAB," *Journal of Open Source Software*, vol. 2, no. 19, pp. 413, November 2017.
4. **Haytham M. Fayek**, Laurens van der Maaten, Griffin Romigh, and Ravish Mehra, and R. Mehra, "On data-driven approaches to head-related transfer function personalization," *Audio Engineering Society (AES) Convention 143*, New York, NY, USA, October 2017.
5. **Haytham M. Fayek**, Margaret Lech, and Lawrence Cavedon, "Evaluating deep learning architectures for speech emotion recognition," *Neural Networks*, vol. 92, pp. 60–68, 2017.
6. **Haytham M. Fayek**, Irraivan Elamvazuthi, Perumal Nallagownden, and Bala Venkatesh, "A controller based on optimal type-2 fuzzy logic: Systematic design, optimization and real-time implementation," *ISA Transactions*, vol. 53, no. 5, pp. 1583–1591, 2014.

Publications

Complete list of publication at <https://haythamfayek.com/publications>.

Talks

Complete list of talks at <https://haythamfayek.com/talks>.