0.5 MVI B, 25 H

CDA 2624H

ADD B

MUI C, 29H

A00 €

26244

STAT OIL HALT

(60100 operation: - AND & EXDR (02)

Immediate data 36H

memory location 3125H

B, 3011 MNI

3125M LOA

ANA B

31254 STA

DUT DIM

MALT

MVI B, 30M

3125 H LDA

SOM B KRA

SER SINSH 27 A

OUT OW

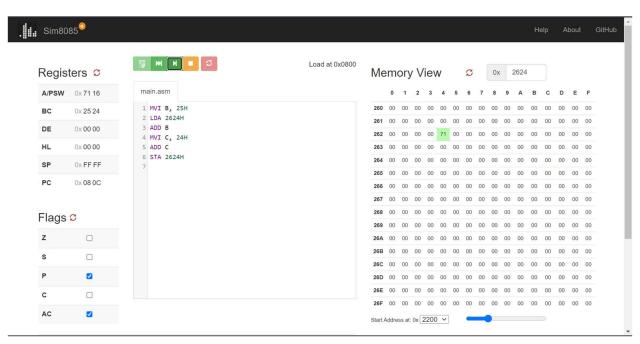
MALT

B, 158 1114 auson LDA B ORA 212511 STA 21254 JMP 014 2007

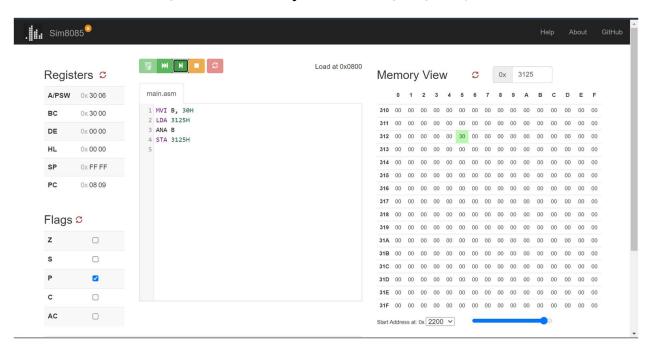
MALT

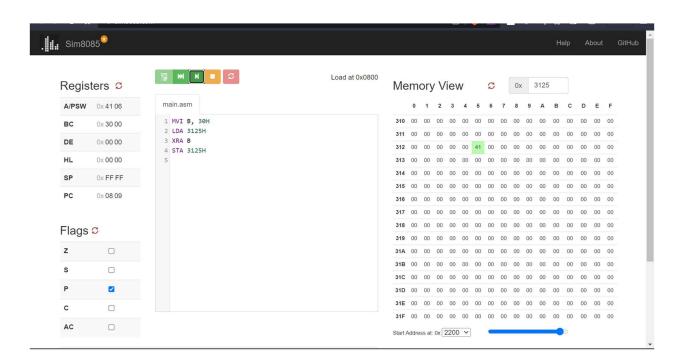
MICROPROCESSORS LAB

Q 1. To add three numbers using ALP of 8085 MP, using all the processor's addressing modes (except implicit addressing mode). Use the immediate data as 25 H & memory location 2624 H with 28H as its data (wherever necessary). Assume any immediate data as the third operand for addition.

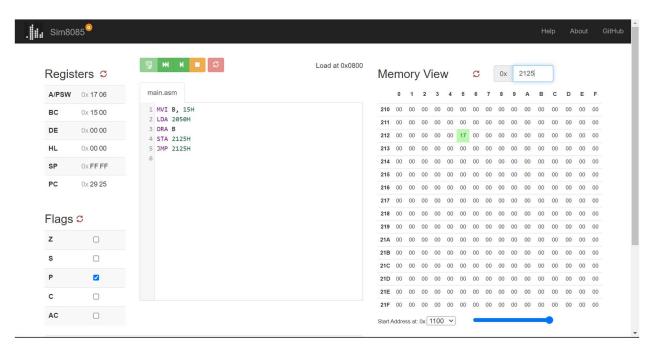


Q2. To do logical AND & EXOR operations in different ways using immediate data 30 H, memory location 3125H [71H].





Q3. To do unconditional jumping after executing the ALP to do OR operations between immediate data 15 H and data from 2050 H (12 H). Store the result of the program in 2125 H and jump to that location



Q4. Send serial data through port address 05H after calculating the addition of two numbers 12 H and 15 H and write the word alignment for SIM instruction when interrupts are disabled.

