

Unit3_Lesson(3)

write codes

main.c

```
5 typedef volatile unsigned int  vuint32;
6
7 #define RCC_Base    0x40021000
8 #define GPIOA_Base  0x40010800
9
10 #define RCC_APB2ENR    *(vuint32*) (RCC_Base+0x18)
11 #define GPIO_PA_CRH    *(vuint32*) (GPIOA_Base+0x04)
12
13
14 #define SetClock      (1<<2)
15
16 typedef union{
17     vuint32 all;
18     struct{
19         vuint32 reserved: 13;
20         vuint32 pinl3: 1;
21     }pin;
22 }P_IN;
23
24 volatile P_IN* ptr = (volatile P_IN*) (GPIOA_Base+0x0c);
25 int main(void)
26 {
27     RCC_APB2ENR|=SetClock;
28     GPIO_PA_CRH &= 0xff0fffff;
29     GPIO_PA_CRH|= 0x00200000;
30     while(1){
31         ptr->pin.pinl3=1;
32         for(int i=0 ;i<50000;i++);
33         ptr->pin.pinl3=0;
34         for(int i=0 ;i<50000;i++);
35     }
36 }
37
38
```

startUp.c

```
1  /* learn-in-depth
2     Unit3_lesson3
3     Eng\ Hazem Abd El-Halim
4     */
5
6  #include <stdint.h>
7
8  extern uint32_t _stack_top ;
9  extern int main(void);
10
11 void reset_handler(void);
12
13 void default_handler(){
14     reset_handler();
15 }
16
17 void NMI_handler(void) __attribute__((weak,alias("default_handler")));
18 void H_fault_handler(void) __attribute__((weak,alias("default_handler")));
19 void MM_fault_handler(void) __attribute__((weak,alias("default_handler")));
20 void Bus_fault(void) __attribute__((weak,alias("default_handler")));
21 void Usage_fault_handler(void) __attribute__((weak,alias("default_handler")));
22
23 uint32_t vectors[] __attribute__((section(".vectors"))) ={
24     (uint32_t) &_stack_top,
25     (uint32_t) &reset_handler,
26     (uint32_t) &NMI_handler,
27     (uint32_t) &H_fault_handler,
28     (uint32_t) &MM_fault_handler,
29     (uint32_t) &Bus_fault,
30     (uint32_t) &Usage_fault_handler
31 };
32
33 extern uint32_t _E_text ;
34 extern uint32_t _S_data ;
35 extern uint32_t _E_data ;
36 extern uint32_t _S_bss ;
37 extern uint32_t _E_bss ;
38
39 void reset_handler(){
40     uint32_t Data_size = (unsigned char*)&_E_data - (unsigned char*)&_S_data ;
41     unsigned char* p_src = (unsigned char*)&_E_text ;
42     unsigned char* p_dest = (unsigned char*)&_S_data ;
43     for(int i=0 ; i < Data_size ; i++){
44         *((unsigned char*)p_dest++) = *((unsigned char*)p_src++);
45     }
46     uint32_t Bss_size = (unsigned char*)&_E_data - (unsigned char*)&_S_data ;
47     p_dest = (unsigned char*)&_S_bss ;
48     for(int i=0 ; i < Bss_size ; i++){
49         *((unsigned char*)p_dest++) = (unsigned char)0 ;
50     }
51
52     main();
53 }
54
55
56
57
58
```

linker_script.ld

```
1
2  /* learn-in-depth
3     Unit3_lesson3
4     Eng\ Hazem Abd El-Halim
5     */
6
7
8  MEMORY
9  {
10     flash (RX) : ORIGIN = 0x08000000, LENGTH = 128k
11     sram (RWX) : ORIGIN = 0x20000000, LENGTH = 20k
12  }
13
14  SECTIONS
15  {
16     .text : {
17         *(.vectors*)
18         *(.text*)
19         *(.rodata)
20         _E_text = . ;
21     } >flash
22
23     .data : {
24         _S_data = . ;
25         *(.data)
26         _E_data = . ;
27     } > sram AT> flash
28
29     .bss : {
30         _S_bss = . ;
31         *(.bss*)
32         _E_bss = . ;
33         . = . + 0x1000 ;
34         _stack_top = . ;
35     } >sram
36  }
37
```

Makefile

```
1  CC=arm-none-eabi-
2  CFLAGS=-gdwarf-2 -mcpu=cortex-m3
3  INCS= -I .
4  LIBS=
5  SRC=$(wildcard *.c)
6  OBJ=$(SRC:.c=.o)
7  As_s=$(wildcard *.s)
8  As_o=$(As_s:.s=.o)
9  project_name= toggle_LED_cortexM3
10
11  all:$(project_name).bin
12
13  %.o: %.s
14      $(CC) as.exe $(CFLAGS) $< -o $@
15
16  %.o: %.c
17      $(CC) gcc.exe -c $(CFLAGS) $(INCS) $< -o $@
18
19  $(project_name).elf: $(OBJ) $(As_o)
20      $(CC) ld.exe -T linker_script.ld $(LIBS) $(As_o) $(OBJ) -o $@ -Map=Map_file.map
21
22  $(project_name).bin: $(project_name).elf
23      $(CC) objcopy.exe -O binary $< $@
24
25  clear_all:
26      rm *.o *.elf *.bin
```

main.o

mingw32-make.exe main.o

startUp.o

mingw32-make.exe startUp.o

To show sections for object_file

main.o

```
$ arm-none-eabi-objdump.exe -h main.o

main.o:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          0000007c  00000000  00000000  00000034  2**2
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data           00000004  00000000  00000000  000000b0  2**2
    CONTENTS, ALLOC, LOAD, DATA
  2 .bss            00000000  00000000  00000000  000000b4  2**0
    ALLOC
  3 .debug_info     00000105  00000000  00000000  000000b4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  4 .debug_abbrev   000000d3  00000000  00000000  000001b9  2**0
    CONTENTS, READONLY, DEBUGGING
  5 .debug_loc      00000038  00000000  00000000  0000028c  2**0
    CONTENTS, READONLY, DEBUGGING
  6 .debug_aranges  00000020  00000000  00000000  000002c4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  7 .debug_line     00000056  00000000  00000000  000002e4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  8 .debug_str       000000b1  00000000  00000000  0000033a  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .comment         0000007f  00000000  00000000  000003eb  2**0
    CONTENTS, READONLY
10 .debug_frame     0000002c  00000000  00000000  0000046c  2**2
    CONTENTS, RELOC, READONLY, DEBUGGING
11 .ARM.attributes  00000033  00000000  00000000  00000498  2**0
    CONTENTS, READONLY
```

Startup.o

```
$ arm-none-eabi-objdump.exe -h startUp.o

startUp.o:    file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          00000008  00000000  00000000  00000034  2**1
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data           00000000  00000000  00000000  0000003c  2**0
    CONTENTS, ALLOC, LOAD, DATA
  2 .bss            00000000  00000000  00000000  0000003c  2**0
    ALLOC
  3 .vectors         00000050  00000000  00000000  0000003c  2**0
    CONTENTS, RELOC, READONLY
  4 .debug_line     0000003b  00000000  00000000  0000008c  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  5 .debug_info     00000026  00000000  00000000  000000c7  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  6 .debug_abbrev   00000014  00000000  00000000  000000ed  2**0
    CONTENTS, READONLY, DEBUGGING
  7 .debug_aranges  00000020  00000000  00000000  00000108  2**3
    CONTENTS, RELOC, READONLY, DEBUGGING
  8 .debug_str       0000002a  00000000  00000000  00000128  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .ARM.attributes  00000021  00000000  00000000  00000152  2**0
    CONTENTS, READONLY
```

To show symbol table for main.o and startUp.o

```
$ arm-none-eabi-nm.exe main.o
00000000 T main
00000000 D ptr
```

```
$ arm-none-eabi-nm.exe startUp.o
U _E_data
U _E_text
U _S_bss
U _S_data
U _stack_top
00000000 W Bus_fault
00000000 T default_handler
00000000 W H_fault_handler
U main
00000000 W MM_fault_handler
00000000 W NMI_handler
00000000c T reset_handler
00000000 W Usage_fault_handler
00000000 D vectors
```

use linker_script to get executable_file (toggle_LED_cortexM3.elf) and Map_file.map

mingw32-make-exe toggle_LED_cortexM3.elf

To show sections for toggle_LED_cortexM3.elf

```
$ arm-none-eabi-objdump.exe -h toggle_LED_cortexM3.elf

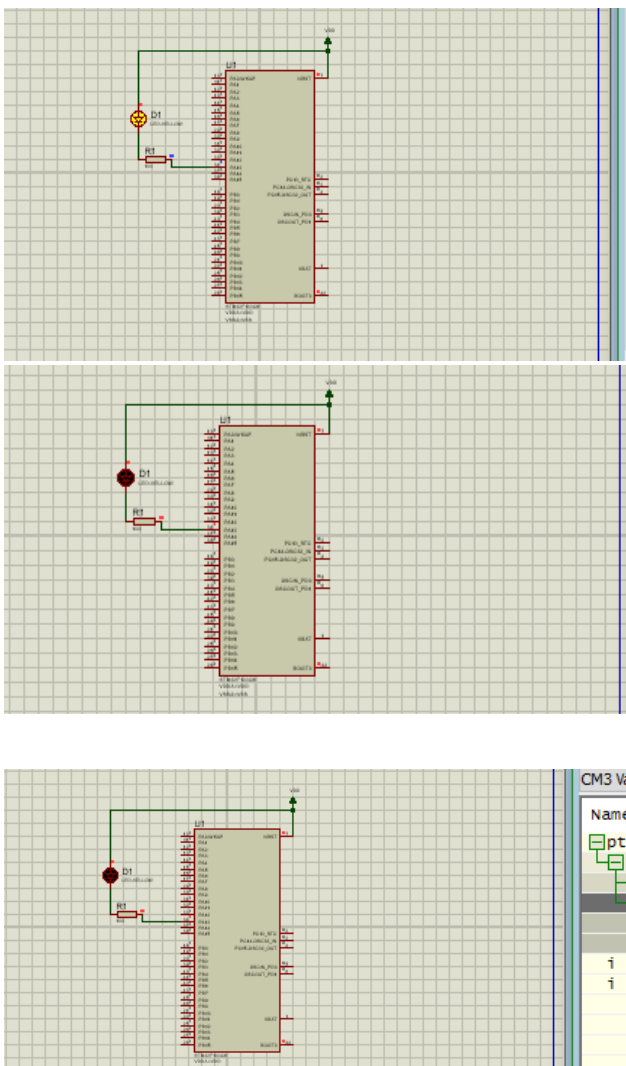
toggle_LED_cortexM3.elf:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA       LMA       File off  Algn
 0 .text          00000124  08000000  08000000  00010000  2**2
   CONTENTS, ALLOC, LOAD, READONLY, CODE
 1 .data          00000004  20000000  08000124  00020000  2**2
   CONTENTS, ALLOC, LOAD, DATA
 2 .bss           00001000  20000004  08000128  00020004  2**0
   ALLOC
 3 .debug_info     00000295  00000000  00000000  00020004  2**0
   CONTENTS, READONLY, DEBUGGING
 4 .debug_abbrev   000001a9  00000000  00000000  00020299  2**0
   CONTENTS, READONLY, DEBUGGING
 5 .debug_loc      000000b4  00000000  00000000  00020442  2**0
   CONTENTS, READONLY, DEBUGGING
 6 .debug_aranges  00000040  00000000  00000000  000204f6  2**0
   CONTENTS, READONLY, DEBUGGING
 7 .debug_line     0000013e  00000000  00000000  00020536  2**0
   CONTENTS, READONLY, DEBUGGING
 8 .debug_str      000001e2  00000000  00000000  00020674  2**0
   CONTENTS, READONLY, DEBUGGING
 9 .comment        0000007e  00000000  00000000  00020856  2**0
   CONTENTS, READONLY
10 .ARM.attributes 00000033  00000000  00000000  000208d4  2**0
   CONTENTS, READONLY
11 .debug_frame    0000007c  00000000  00000000  00020908  2**2
   CONTENTS, READONLY, DEBUGGING
```

To show symbol table for toggle_LED_cortexM3.elf

```
$ arm-none-eabi-nm.exe toggle_LED_cortexM3.elf
20000004 B _E_bss
20000004 D _E_data
08000124 T _E_text
20000004 B _S_bss
20000000 D _S_data
20001004 B _stack_top
08000098 W Bus_fault
08000098 T default_handler
08000098 W H_fault_handler
0800001c T main
08000098 W MM_fault_handler
08000098 W NMI_handler
20000000 D ptr
080000a4 T reset_handler
08000098 W Usage_fault_handler
08000000 T vectors
```

Proteus simulation



```
-----
          struct{
          vuint32 reserved: 13;
          vuint32 pin13: 1;
          }pin;
          }P_IN;
          volatile P_IN* ptr = (volatile P_IN*)(GPIOA_Base+0x0c);
          int main(void)
          {
            8000058 RCC_APB2ENR|=SetClock;
            800005E GPIO_PA_CRH &= 0xfffffff;
            800006A GPIO_PA_CRH|= 0x00200000;
            8000076 while(1){
              8000082 ptr->pin.pin13=1;
              800008E for(int i=0 ;i<5000;i++);
              80000A4 ptr->pin.pin13=0;
              80000B0 for(int i=0 ;i<5000;i++);
            }
          }
          -----
```

```
-----
          struct{
          vuint32 reserved: 13;
          vuint32 pin13: 1;
          }pin;
          }P_IN;
          volatile P_IN* ptr = (volatile P_IN*)(GPIOA_Base+0x0c);
          int main(void)
          {
            8000058 RCC_APB2ENR|=SetClock;
            800005E GPIO_PA_CRH &= 0xfffffff;
            800006A GPIO_PA_CRH|= 0x00200000;
            8000076 while(1){
              8000082 ptr->pin.pin13=1;
              800008E for(int i=0 ;i<5000;i++);
              80000A4 ptr->pin.pin13=0;
              80000B0 for(int i=0 ;i<5000;i++);
            }
          }
          -----
```

Name	Address
ptr	080000D4
ptr	4001080C
all	4001080C
pin	4001080C
reserved	4001080C
pin13	4001080C
i	BP+12 = @20000FD4
i	BP+16 = @20000FD0

[illegible][illegible]