

Unit3_Lesson(3)

write codes

main.c

```
5 typedef volatile unsigned int  vuint32;
6
7 #define RCC_Base    0x40021000
8 #define GPIOA_Base  0x40010800
9
10 #define RCC_APB2ENR    *(vuint32*) (RCC_Base+0x18)
11 #define GPIO_PA_CRH    *(vuint32*) (GPIOA_Base+0x04)
12
13
14 #define SetClock      (1<<2)
15
16 typedef union{
17     vuint32 all;
18     struct{
19         vuint32 reserved: 13;
20         vuint32 pinl3: 1;
21     }pin;
22 }P_IN;
23
24 volatile P_IN* ptr = (volatile P_IN*) (GPIOA_Base+0x0c);
25 int main(void)
26 {
27     RCC_APB2ENR|=SetClock;
28     GPIO_PA_CRH &= 0xff0fffff;
29     GPIO_PA_CRH|= 0x00200000;
30     while(1){
31         ptr->pin.pinl3=1;
32         for(int i=0 ;i<50000;i++);
33         ptr->pin.pinl3=0;
34         for(int i=0 ;i<50000;i++);
35     }
36 }
37
38
```

startUp.s

```
1  /* learn-in-depth
2      Unit3_lesson3
3      Eng\ Hazem Abd El-Halim
4      */
5
6  .section .vectors
7  .word 0x20001000 //stack_top
8  .word _reset    //1   reset
9  .word vector_handler // 2   NMI
10 .word vector_handler // 3   MM fault
11 .word vector_handler // 4   Bus fault
12 .word vector_handler // 5   Usage fault
13 .word vector_handler // 6   Reserved
14 .word vector_handler // 7   Reserved
15 .word vector_handler // 8   Reserved
16 .word vector_handler // 9   Reserved
17 .word vector_handler // 10  Reserved
18 .word vector_handler // 11  SVL call
19 .word vector_handler // 12  Debug reserved
20 .word vector_handler // 13  Reserved
21 .word vector_handler // 14  PendSv
22 .word vector_handler // 15  sysTick
23 .word vector_handler // 16  IRQ1
24 .word vector_handler // 17  IRQ2
25 .word vector_handler // 18  IRQ3
26 .word vector_handler // 19  ...
27
28 .section .text
29 _reset:
30     bl main
31     b .
32
33 .thumb_func
34 vector_handler:
35     b _reset
36
```

linker_script.ld

```
1  /* learn-in-depth
2     Unit3_lesson3
3     Eng\ Hazem Abd El-Halim
4     */
5
6
7  MEMORY
8  {
9     flash (RX) : ORIGIN = 0x08000000, LENGTH = 128k
10    sram (RWX) : ORIGIN = 0x20000000, LENGTH = 20k
11  }
12
13  SECTIONS
14  {
15     .text : {
16         *(.vectors*)
17         *(.text*)
18         *(.rodata)
19     } >flash
20
21     .data : {
22         *(.data)
23     } >flash
24
25     .bss : {
26         *(.bss*)
27     } >sram
28  }
```

Makefile

```
1  CC=arm-none-eabi-
2  CFLAGS=-gdwarf-2 -mcpu=cortex-m3
3  INCS= -I .
4  LIBS=
5  SRC=$(wildcard *.c)
6  OBJ=$(SRC:.c=.o)
7  As_s=$(wildcard *.s)
8  As_o=$(As_s:.s=.o)
9  project_name= toggle_LED_cortexM3
10
11  all:$(project_name).bin |
12
13  %.o: %.s
14      $(CC)as.exe $(CFLAGS) $< -o $@
15
16  %.o: %.c
17      $(CC)gcc.exe -c $(CFLAGS) $(INCS) $< -o $@
18
19  $(project_name).elf: $(OBJ) $(As_o)
20      $(CC)ld.exe -T linker_script.ld $(LIBS) $(As_o) $(OBJ) -o $@ -Map=Map_file.map
21
22  $(project_name).bin: $(project_name).elf
23      $(CC)objcopy.exe -O binary $< $@
24
25  clear_all:
26      rm *.o *.elf *.bin
```

main.o

mingw32-make.exe main.o

startUp.o

mingw32-make.exe startUp.o

To show sections for object_file

main.o

```
$ arm-none-eabi-objdump.exe -h main.o

main.o:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          0000007c  00000000      00000000      00000034  2**2
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data          00000004  00000000      00000000      000000b0  2**2
    CONTENTS, ALLOC, LOAD, DATA
  2 .bss           00000000  00000000      00000000      000000b4  2**0
    ALLOC
  3 .debug_info     00000105  00000000      00000000      000000b4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  4 .debug_abbrev   000000d3  00000000      00000000      000001b9  2**0
    CONTENTS, READONLY, DEBUGGING
  5 .debug_loc      00000038  00000000      00000000      0000028c  2**0
    CONTENTS, READONLY, DEBUGGING
  6 .debug_aranges  00000020  00000000      00000000      000002c4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  7 .debug_line     00000056  00000000      00000000      000002e4  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  8 .debug_str       000000b1  00000000      00000000      0000033a  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .comment         0000007f  00000000      00000000      000003eb  2**0
    CONTENTS, READONLY
10 .debug_frame     0000002c  00000000      00000000      0000046c  2**2
    CONTENTS, RELOC, READONLY, DEBUGGING
11 .ARM.attributes  00000033  00000000      00000000      00000498  2**0
    CONTENTS, READONLY
```

```
$ arm-none-eabi-objdump.exe -h startUp.o

startUp.o:    file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          00000008  00000000      00000000      00000034  2**1
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data          00000000  00000000      00000000      0000003c  2**0
    CONTENTS, ALLOC, LOAD, DATA
  2 .bss           00000000  00000000      00000000      0000003c  2**0
    ALLOC
  3 .vectors        00000050  00000000      00000000      0000003c  2**0
    CONTENTS, RELOC, READONLY
  4 .debug_line     0000003b  00000000      00000000      0000008c  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  5 .debug_info     00000026  00000000      00000000      000000c7  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  6 .debug_abbrev   00000014  00000000      00000000      000000ed  2**0
    CONTENTS, READONLY, DEBUGGING
  7 .debug_aranges  00000020  00000000      00000000      00000108  2**3
    CONTENTS, RELOC, READONLY, DEBUGGING
  8 .debug_str       0000002a  00000000      00000000      00000128  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .ARM.attributes  00000021  00000000      00000000      00000152  2**0
    CONTENTS, READONLY
```

Startup.o

To show symbol table for main.o and startUp.o

```
$ arm-none-eabi-nm.exe main.o
00000000 T main
00000000 D ptr

$ arm-none-eabi-nm.exe startUp.o
00000000 t _reset
          U main
00000006 t vector_handler
```

use linker_script to get executable_file (toggle_LED_cortexM3.elf) and Map_file.map

mingw32-make-exe toggle_LED_cortexM3.elf

To show sections for toggle_LED_cortexM3.elf

```
$ arm-none-eabi-objdump.exe -h toggle_LED_cortexM3.elf

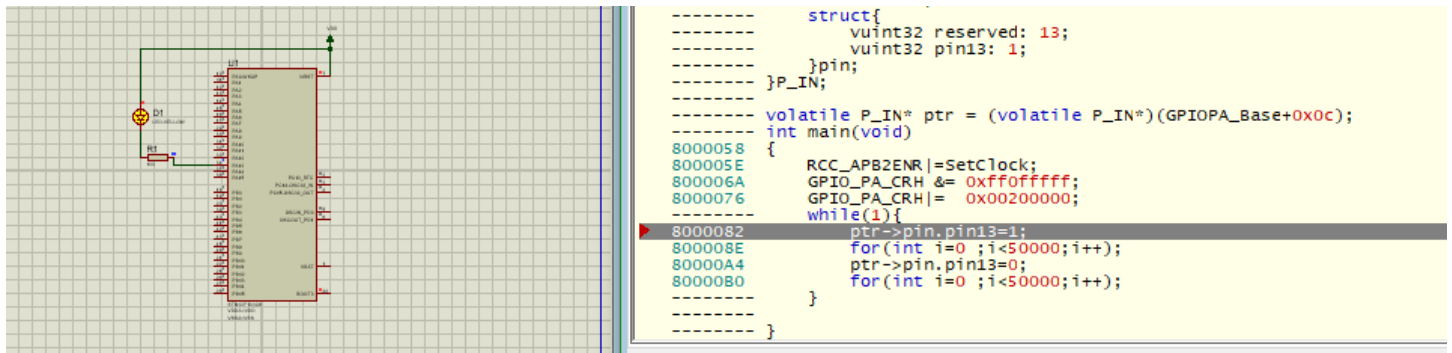
toggle_LED_cortexM3.elf:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          000000d4  08000000      08000000      00010000  2**2
    CONTENTS, ALLOC, LOAD, READONLY, CODE
  1 .data          00000004  080000d4      080000d4      000100d4  2**2
    CONTENTS, ALLOC, LOAD, DATA
  2 .ARM.attributes 0000002f  00000000      00000000      000100d8  2**0
    CONTENTS, READONLY
  3 .comment        0000007e  00000000      00000000      00010107  2**0
    CONTENTS, READONLY
  4 .debug_line     00000091  00000000      00000000      00010185  2**0
    CONTENTS, READONLY, DEBUGGING
  5 .debug_info     0000012b  00000000      00000000      00010216  2**0
    CONTENTS, READONLY, DEBUGGING
  6 .debug_abbrev   000000e7  00000000      00000000      00010341  2**0
    CONTENTS, READONLY, DEBUGGING
  7 .debug_aranges  00000040  00000000      00000000      00010428  2**3
    CONTENTS, READONLY, DEBUGGING
  8 .debug_str      000000ca  00000000      00000000      00010468  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .debug_loc      00000038  00000000      00000000      00010532  2**0
    CONTENTS, READONLY, DEBUGGING
 10 .debug_frame    0000002c  00000000      00000000      0001056c  2**2
    CONTENTS, READONLY, DEBUGGING
```

To show symbol table for toggle_LED_cortexM3.elf

```
$ arm-none-eabi-nm.exe toggle_LED_cortexM3.elf
08000050 t _reset
08000058 T main
080000d4 D ptr
08000056 t vector_handler
```

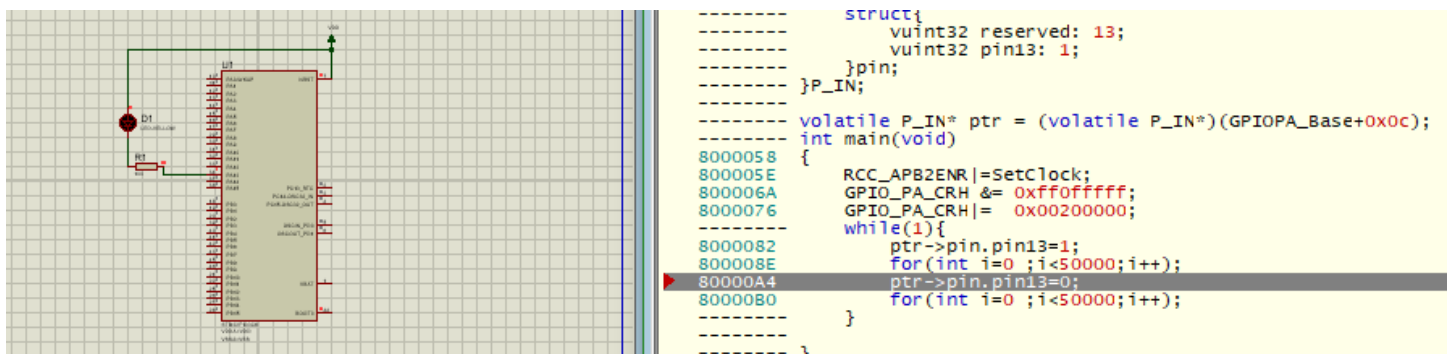
Proteus simulation



```

-----
struct{
    vuint32 reserved: 13;
    vuint32 pin13: 1;
}pin;
-----
}P_IN;
-----
volatile P_IN* ptr = (volatile P_IN*)(GPIOA_Base+0x0c);
int main(void)
{
    8000058 RCC_APB2ENR|=SetClock;
    800005E GPIO_PA_CRH &= 0xfffffff;
    800006A GPIO_PA_CRH|= 0x00200000;
    8000076 while(1){
        8000082 ptr->pin.pin13=1;
        800008E for(int i=0 ;i<50000;i++);
        80000A4 ptr->pin.pin13=0;
        80000B0 for(int i=0 ;i<50000;i++);
    }
}
-----

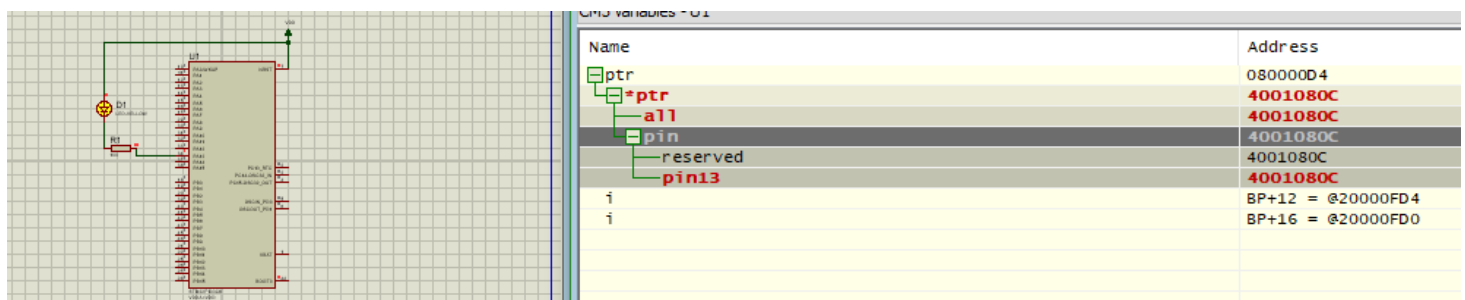
```



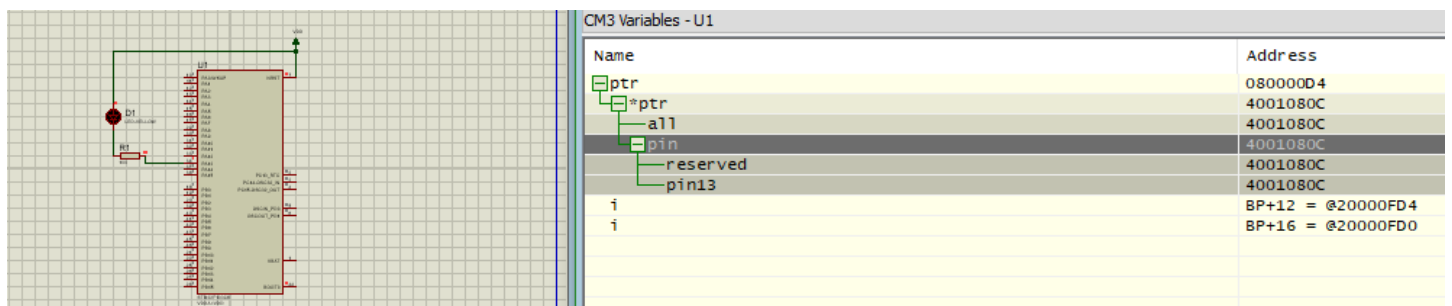
```

-----
struct{
    vuint32 reserved: 13;
    vuint32 pin13: 1;
}pin;
-----
}P_IN;
-----
volatile P_IN* ptr = (volatile P_IN*)(GPIOA_Base+0x0c);
int main(void)
{
    8000058 RCC_APB2ENR|=SetClock;
    800005E GPIO_PA_CRH &= 0xfffffff;
    800006A GPIO_PA_CRH|= 0x00200000;
    8000076 while(1){
        8000082 ptr->pin.pin13=1;
        800008E for(int i=0 ;i<50000;i++);
        80000A4 ptr->pin.pin13=0;
        80000B0 for(int i=0 ;i<50000;i++);
    }
}
-----

```



Name	Address
ptr	080000D4
*ptr	4001080C
all	4001080C
pin	4001080C
reserved	4001080C
pin13	4001080C
i	BP+12 = @20000FD4
i	BP+16 = @20000FD0



Name	Address
ptr	080000D4
*ptr	4001080C
all	4001080C
pin	4001080C
reserved	4001080C
pin13	4001080C
i	BP+12 = @20000FD4
i	BP+16 = @20000FD0

CM3 FLASH at 0x08000000 - U1

[illegible]

CM3 RAM at 0x20000000 - U1

[illegible]