Unit3_Lesson(3)

write codes

main.c

```
typedef volatile unsigned int vuint32;
     #define RCC Base 0x40021000
     #define GPIOPA Base 0x40010800
10
     #define RCC APB2ENR *(vuint32*)(RCC Base+0x18)
     #define GPIO PA CRH *(vuint32*)(GPIOPA Base+0x04)
11
12
13
14
     #define SetClock (1<<2)
15
16 typedef union{
17 vuint32 al:
         vuint32 all;
18
         struct{
19
             vuint32 reserved: 13;
20
             vuint32 pin13: 1;
21
         }pin;
    P_IN;
22
23
24
     volatile P IN* ptr = (volatile P IN*)(GPIOPA Base+0x0c);
    int main(void)
25
   □ {
26
27
         RCC APB2ENR|=SetClock;
28
         GPIO PA CRH &= 0xff0fffff;
        GPIO_PA_CRH|= 0x00200000;
29
30
        while(1){
31
             ptr->pin.pin13=1;
32
             for(int i=0 ;i<50000;i++);
             ptr->pin.pin13=0;
33
34
             for(int i=0 ;i<50000;i++);
35
         }
36
37
      }
38
```

startUp.s

```
/* learn-in-depth
               Unit3 lesson3
 3
               Eng\ Hazem Abd El-Halim
 4
               */
 5
 6
         .section .vectors
 7
         .word 0x20001000
                                            //stack top
                                         //l reset
 8
         .word _reset
       .word vector_handler // 2 NMI
.word vector_handler // 3 MM fault
.word vector_handler // 4 Bus fault
.word vector_handler // 5 Usage fault
9
10
11
12
       13
14
15
16
17
18
19
       .word vector_handler // 13 Reserved
.word vector_handler // 14 PendSv
.word vector_handler // 15 sysTick
.word vector_handler // 16 IRQ1
20
21
22
23
       .word vector_handler // 17 IRQ2
.word vector_handler // 18 IRQ3
.word vector_handler // 19 ...
24
25
26
27
28
        .section .text
29
          _reset:
30
             bl main
31
              b.
32
33
        .thumb func
34
        vector handler:
35
             b _reset
36
```

linker_script.ld

```
1
     /* learn-in-depth
         Unit3 lesson3
 3
         Eng\ Hazem Abd El-Halim
 4
 5
 6
     MEMORY
 8
     flash (RX) : ORIGIN = 0x08000000, LENGTH = 128k
 9
10
     sram (RWX) : ORIGIN = 0x20000000, LENGTH = 20k
11
12
    SECTIONS
13
14 {
15
         .text : {
16
                 *(.vectors*)
17
                 *(.text*)
18
                 *(.rodata)
19
        } >flash
20
21
         .data : {
22
                 *(.data)
23
         } >flash
24
25
         .bss : {
                *(.bss*)
26
27
         } >sram
28
```

Makefile

```
CC=arm-none-eabi-
     CFLAGS=-gdwarf-2 -mcpu=cortex-m3
3
    INCS= -I .
4
    LIBS=
 5
     SRC=$(wildcard *.c)
 6
     OBJ=$(SRC:.c=.o)
     As s=$(wildcard *.s)
8
     As o=$(As s:.s=.o)
9
     project_name= toggle_LED_cortexM3
10
11
     all:$(project_name).bin
12
    %.o: %.s
13
14
        $(CC)as.exe $(CFLAGS) $< -o $@
15
16
     %.o: %.c
17
         $(CC)gcc.exe -c $(CFLAGS) $(INCS) $< -o $@
18
     $(project name).elf: $(OBJ) $(As o)
19
         $(CC)ld.exe -T linker_script.ld $(LIBS) $(As_o) $(OBJ) -o $@ -Map=Map file.map
20
21
22
     $ (project_name).bin: $ (project_name).elf
23
         $(CC)objcopy.exe -0 binary $< $@
24
25
    clear all:
26
        rm *.o *.elf *.bin
```

main.o

mingw32-make.exe main.o

startUp.o

mingw32-make.exe startUp.o

To show sections for object file

main.o

```
arm-none-eabi-objdump.exe -h main.o
              file format elf32-littlearm
main.o:
Sections:
Idx Name
                     Size
                                                          File off
                                                                      Algn
                                 \/MA
                                              I MA
                                             00000000
  0 .text
                     0000007c
                                 00000000
                                                          00000034
                                 ALLOC, LOAD, RELOC, 00000000 00000000
                                                          READONLY,
                     CONTENTS, 00000004
                                                                      CODE
  1 .data
                                                          000000ь0
                     CONTENTS,
                                 ALLOC, LOAD, DATA
  2 .bss
                     00000000
                                 00000000 00000000
                                                          000000b4
                     ALLOC
  3 .debug_info
                     00000105
                                 00000000
                                             00000000
                                                          000000b4
                                                                      2**0
                     CONTENTS,
                                 RELOC, READONLY, DEBUGGING 00000000 00000000 0000001
    .debug_abbrev
                                                                      2**0
                     000000d3
                                                          000001b9
                     CONTENTS,
                                 READONLY,
                                             DEBUGGING
                     00000038
                                 00000000
                                                                      2**0
  5 .debug_loc
                                             00000000
                                                          0000028c
    CONTENTS, .debug_aranges 00000020
                                 READONLY, DEBUGGING 00000000 00000000
                                                           000002c4
                                 RELOC, READONLY, DEBUGGING 00000000 00000000 00000000
                     CONTENTS,
    .debug_line
                     00000056
                                                         000002e4
                                                                      2**0
                                 RELOC, READONLY, DEBUGGING 00000000 00000000 0000000
                     CONTENTS,
                                                                      2**0
  8 .debug_str
                     000000b1
                                                          0000033a
                     CONTENTS, 0000007f
                                 READONLY,
                                              DEBUGGING
  9 .comment
                                 00000000
                                              00000000
                                                          000003eb
                                                                      2**0
                     CONTENTS, READONLY
                     0000002c
 10 .debug_frame
                                 00000000
                                              00000000
                                                          0000046c
 CONTENTS, RELOC, READONLY, DEBUGGING
11 .ARM.attributes 00000033 00000000 00000000 00000498 2**0
                     CONTENTS, READONLY
```

Startup.o

```
arm-none-eabi-objdump.exe -h startUp.o
                   file format elf32-littlearm
tartUp.o:
Sections:
                                                             File off
00000034
Idx Name
                      Size
                                                I MA
                                                                          Algn
                      00000008
                                   00000000
                                                00000000
                                                                          2**1
  0 .text
                                   ALLOC, LOAD, RELOC 00000000 00000000
                                                             READONLY,
                                                                          CODE
                      CONTENTS,
                                                   RELOC,
                      00000000
                                                             0000003c
  1 .data
                      CONTENTS,
00000000
                                   ALLOC, LOAD, DATA 00000000 00000000
  2 .bss
                                                             0000003c
                       ALLOC
                                                                          2**0
                      00000050
                                   00000000
                                                00000000
                                                             0000003c
  3 .vectors
                      CONTENTS,
                                   RELOC, READONLY
  4 .debug_line
                      0000003b
                                   00000000 00000000
                                                             0000008c
                                                                          2**0
                                   RELOC, READONLY, DEBUGGING 00000000 00000000 00000000 00000000
                      CONTENTS,
                      00000026
                                                                          2**0
    .debug_info
                                   RELOC, RE
00000000
                                           READONLY, DE
00 00000000
                      CONTENTS,
                                                         DEBUGGING
                                                                          2**0
     debug_abbrev 00000014
                                                             000000ed
    CONTENTS, .debug_aranges 00000020
                                   READONLY, DEBUGGING 00000000 00000000
                                                              00000108
                                                                           2**3
                                   RELOC, READONLY, DEBUGGING 00000000 00000000 00000128
                      CONTENTS,
                      0000002a
                                                                          2**0
    .debug_str
    CONTENTS, READONLY, D
.ARM.attributes 00000021 00000000
                                               DEBUGGING
                                                               00000152
                                                  00000000
                                                                             2**0
                      CONTENTS, READONLY
```

To show symbol table for main.o and startUp.o

```
$ arm-none-eabi-nm.exe main.o
00000000 T main
00000000 D ptr
$ arm-none-eabi-nm.exe startUp.o
00000000 t _reset
U main
00000006 t vector_handler
```

use linker_script to get executable_file (toggle_LED_cortexM3.elf) and Map_file.map

mingw32-make-exe toggle_LED_cortexM3.elf

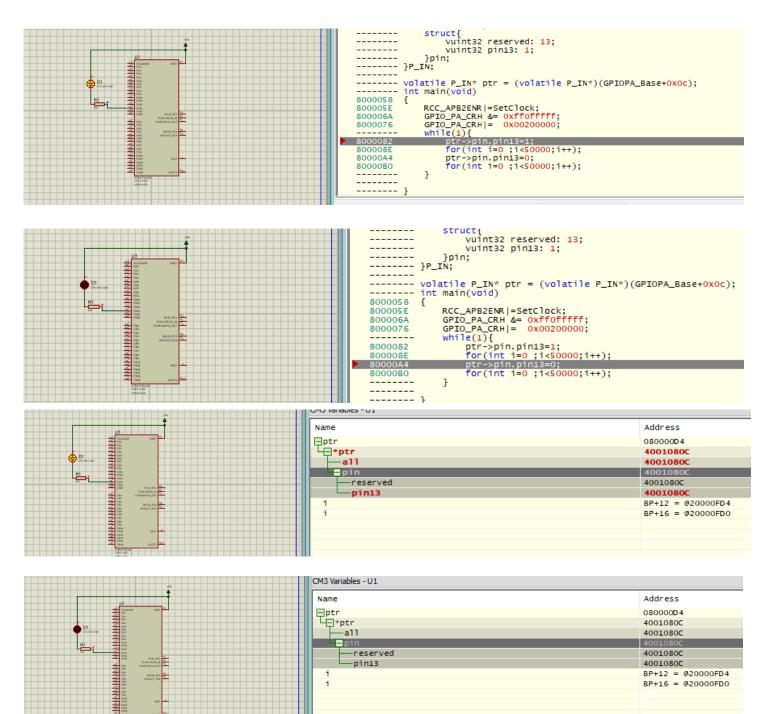
To show sections for toggle LED cortexM3.elf

```
arm-none-eabi-objdump.exe -h toggle_LED_cortexM3.elf
toggle_LED_cortexM3.elf:
                              file format elf32-littlearm
Sections:
Idx Name
                  Size
                             VMA
                                                  File off
                                                            Algn
                                       LMA
                  00000d4
                             08000000
                                       08000000
                                                  00010000
 0 .text
                  CONTENTS,
                             ALLOC, LOAD, READONLY, CODE
 1 .data
                             080000d4
                                       080000d4
                                                  000100d4
                  00000004
 CONTENTS, ALLOC, LOAD, DATA 2 .ARM.attributes 0000002f 00000000 00000000
                                                              2**0
                                                    000100d8
                  CONTENTS, READONLY
                                                  00010107
  3 .comment
                  0000007e
                             00000000
                                       00000000
                                                            2**0
                  CONTENTS, READONLY
 4 .debug_line
                                                  00010185
                                                            2**0
                  00000091
                             00000000
                                       00000000
                  CONTENTS, READONLY,
                                       DEBUGGING
                                                  00010216
                                                            2**0
 5 .debug_info
                  0000012b
                             00000000
                                       00000000
                  CONTENTS, READONLY,
 6 .debug_abbrev 000000e7
                                                  00010341
                                                            2**0
                  CONTENTS, READONLY,
                                       DEBUGGING
 7 .debug_aranges 00000040
                                                   00010428
                                                             2**3
                             00000000
                                        00000000
                  CONTENTS, READONLY, DEBUGGING
 8 .debug_str
                             00000000
                                                  00010468
                                                            2**0
                  000000ca
                                       00000000
                  CONTENTS, READONLY,
                                       DEBUGGING
 9 .debug_loc
                  00000038
                             00000000
                                       00000000
                                                  00010532
                                                            2**0
                  CONTENTS, READONLY, DEBUGGING
10 .debug_frame
                  0000002c
                                                  0001056c
                             00000000 00000000
                  CONTENTS, READONLY, DEBUGGING
```

To show symbol table for toggle_LED_cortexM3.elf

```
$ arm-none-eabi-nm.exe toggle_LED_cortexM3.elf
08000050 t _reset
08000058 T main
080000d4 D ptr
08000056 t vector_handler
```

Proteus simulation



CM3 FLAS	CM3 FLASH at 0x08000000 - U1														
08000000	00 10 00 20 50 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 PWWW	.ww													
08000024	57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 57 00 00 08 WWWWW	.WWW													
08000048	57 00 00 08 57 00 00 08 00 F0 02 F8 FE E7 FB E7 80 B4 83 B0 00 AF 1A 4B 1B 68 19 4A 43 F0 04 03 13 60 18 4B WW	K.h.JC`.K													
0800006C	1B 68 17 4A 23 F4 70 03 13 60 15 4B 1B 68 14 4A 43 F4 00 13 13 60 13 4B 1A 68 13 88 43 F4 00 53 13 80 00 23 .h.J#.p`.K.h.JC`.	K.hCS#													
08000090	7B 60 02 E0 7B 68 01 33 7B 60 7B 68 4C F2 4F 32 93 42 F7 DD 0A 4B 1A 68 13 88 6F F3 4D 33 13 80 00 23 3B 60 {`{h.3{`{hL.02.BK.	ho.M3#;`													
080000B4	02 E0 3B 68 01 33 3B 60 3B 68 4C F2 4F 32 93 42 F7 DD DC E7 18 10 02 40 04 08 01 40 D4 00 00 08 0C 08 01 40 ;h.3;`;hL.02.B	@@@													
080000D8															
080000FC															
08000120	00 00 00 00 00 00 00 00 00 00 00 00 00 00														
08000144															
08000168	00 00 00 00 00 00 00 00 00 00 00 00 00 00														
0800018C	00 00 00 00 00 00 00 00 00 00 00 00 00 00														
080001B0	00 00 00 00 00 00 00 00 00 00 00 00 00 00														
080001D4															
080001F8	00 00 00 00 00 00 00 00 00 00 00 00 00 00														

CM3 RAM at 0x20000000 - U1

20000BF4	00 00	00	00 00	00	00 00	00	00	00	00 0	0 00	00	00 00	00	00	00	00 (0 00	0 00	00	00	00	00	00 0	00 0	00 0	00 00	00	00 00	
20000C18	00 00																											00 00	
20000C3C	00 00		00 00																										
20000C60	00 00																												
20000C84	00 00																											00 00	
20000CA8	00 00																											00 00	
20000CCC	00 00																											00 00	
20000CF0	00 00																												
20000D14	00 00																											00 00	
20000D38	00 00																												
20000D5C	00 00																											00 00	
20000D80	00 00																												
20000DA4	00 00																												
20000DC8	00 00																												
20000DEC	00 00																												
20000E10	00 00																												
20000E34	00 00																												
20000E58	00 00																												
20000E7C	00 00																												
20000EA0	00 00																												
20000EC4	00 00																												
20000EE8	00 00																												
20000F0C	00 00																											00 00	
20000F30	00 00											00 00																00 00	
20000F54	00 00																											00 00	
20000F78	00 00											00 00																00 00	
20000F9C	00 00																											00 00	
20000FC0	00 00											00 50																00 00	
20000FE4	00 00																												
20001008	00 00											00 00																00 00	
2000102C	00 00		00 00																									00 00	
20001050	00 00		00 00																									00 00	
20001074	00 00																											00 00	
20001098	00 00																											00 00	
200010BC	00 00																												
200010E0	00 00																											00 00	
20001104	00 00																												
20001104	00 00																												
20001126 2000114C	00 00																												
20001170	00 00																												
20001170	00 00																												
20001194 200011B8	00 00																												
200011B8	00 00																												
20001100	00 00																												
20001200	00 00																												
20001224	00 00																												
20001248 2000126C	00 00																												
20001260	00 00	00	00 00		00 00	00			00 0	00		00 00			00	(00							00		00 00	