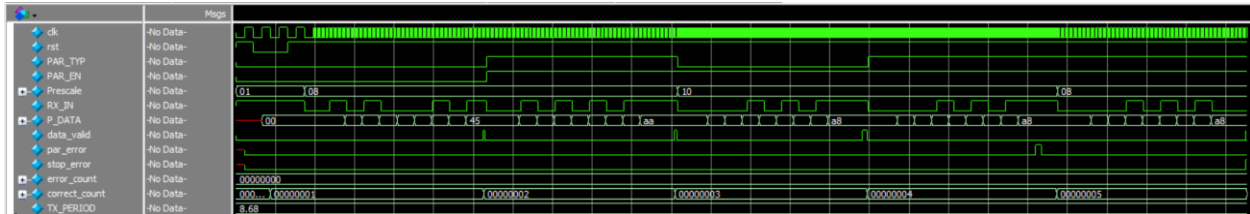


UART_RX

Hazem Elgharabawy

Wave snippet



Log snippet

```
Transcript
# -----
# TEST REPORT
# 6 tests passed with no errors
# -----
# ** Note: $stop      : UART_RX_tb.sv(50)
#      Time: 510573 ps Iteration: 1 Instance: /UART_RX_tb
# Break in Module UART_RX_tb at UART_RX_tb.sv line 50
VSIM 2>
```