

Lab 5: Capacitors and MOSFETs

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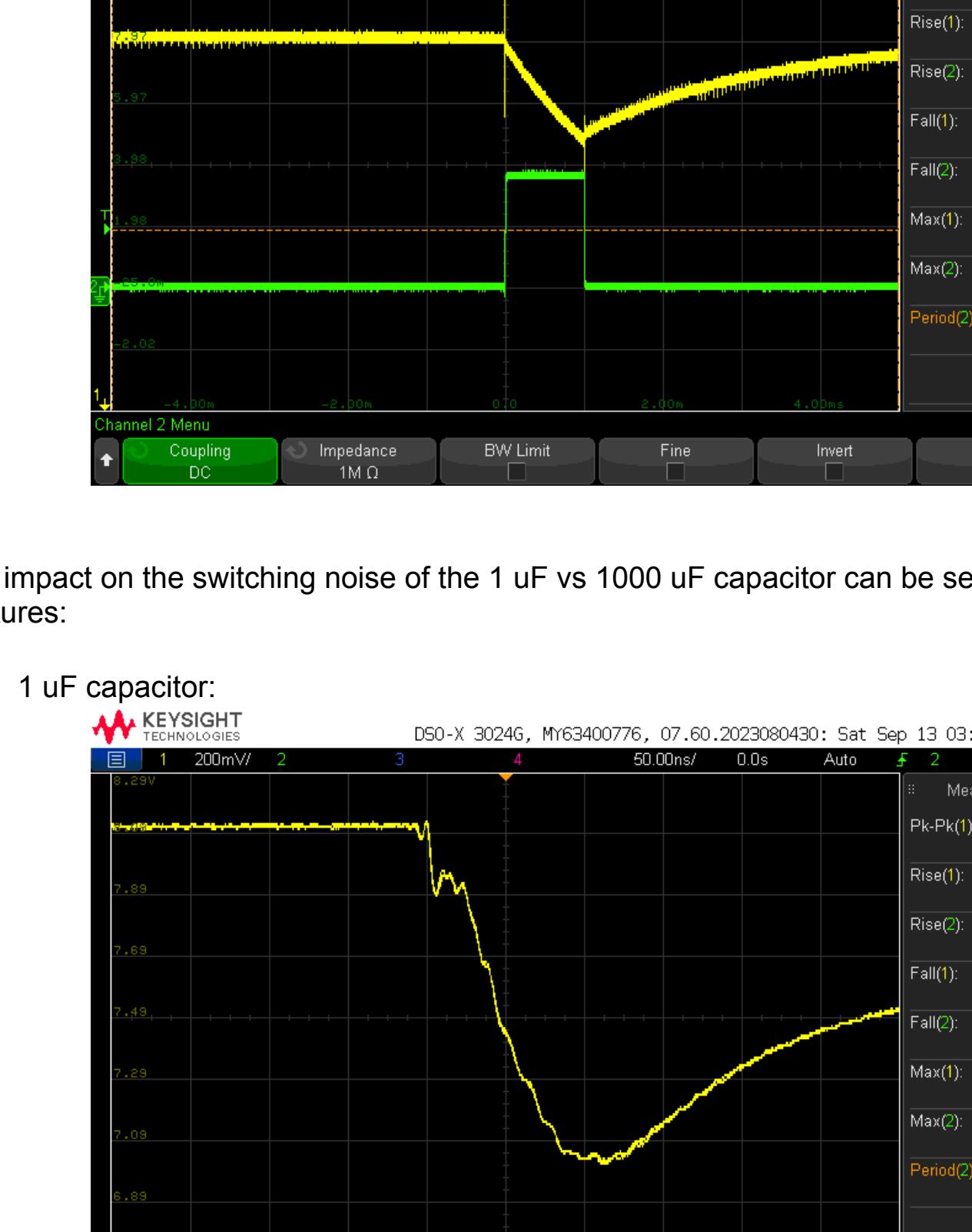
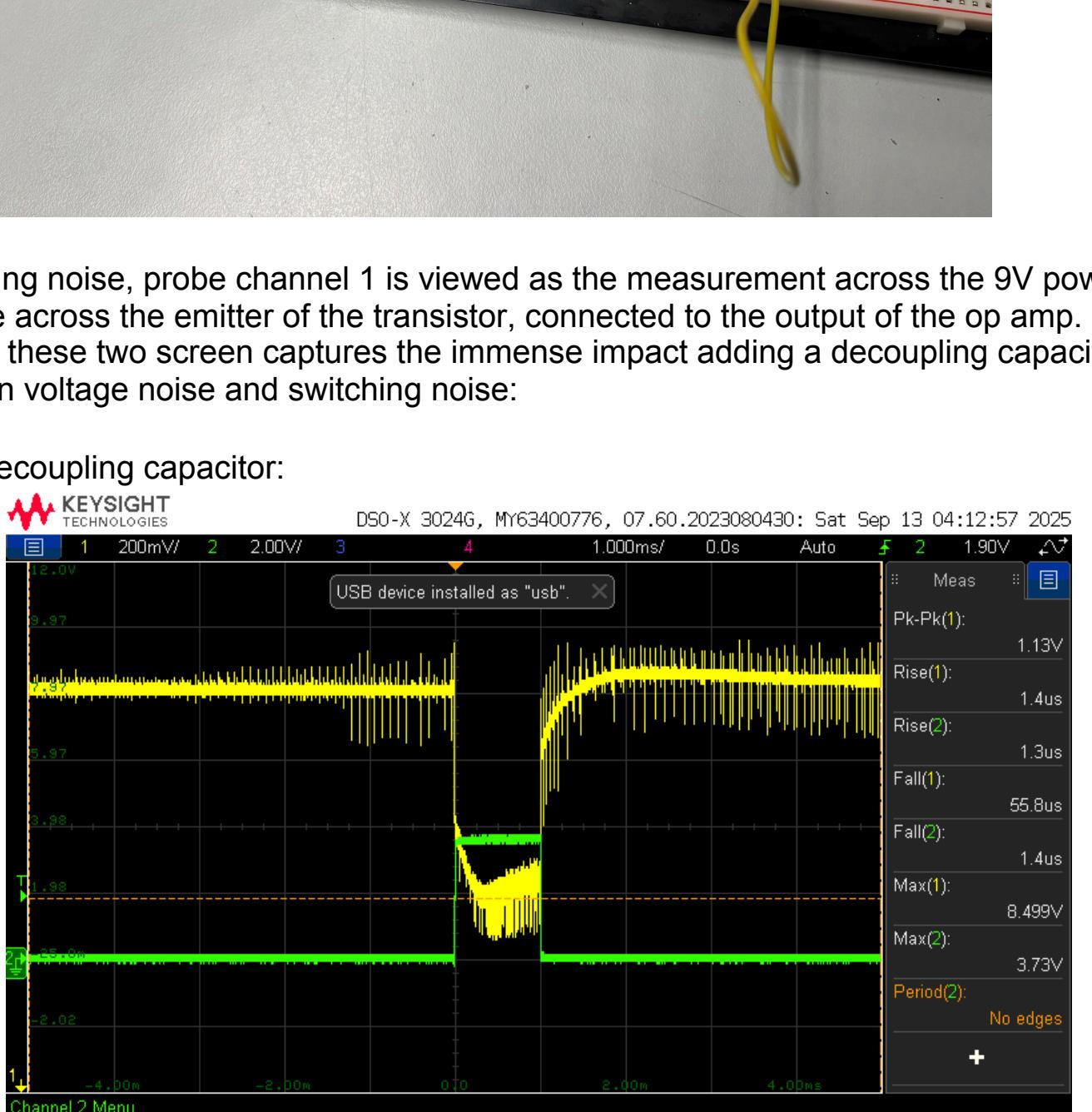
Objective

The objective of this lab was to see how decoupling capacitors effect voltage noise of a circuit, and determine how to select the right capacitor to mitigate the effects of voltage noise. A 1 ms on and 20 ms off signal from the Arduino gives us a 4.76% duty cycle ($Duty\ Cycle = \frac{T_{on}}{T} = \frac{1\ ms}{21\ ms} = 4.76\%$) which is then connected to an op amp, whose output feeds the base of the MOSFET. This slow output of an op amp turning on the MOSFET will cause the transistor to quickly pull from the 9V power rail to the collector. This sudden change in current results in a "ringing" that is visible in the signal rise time, which sought to mitigate by adding a decoupling capacitor across the 9V rail. The inductive nature of the wires beneath the breadboard resist sudden changes in current, which can be reduced by adding decoupling capacitors.

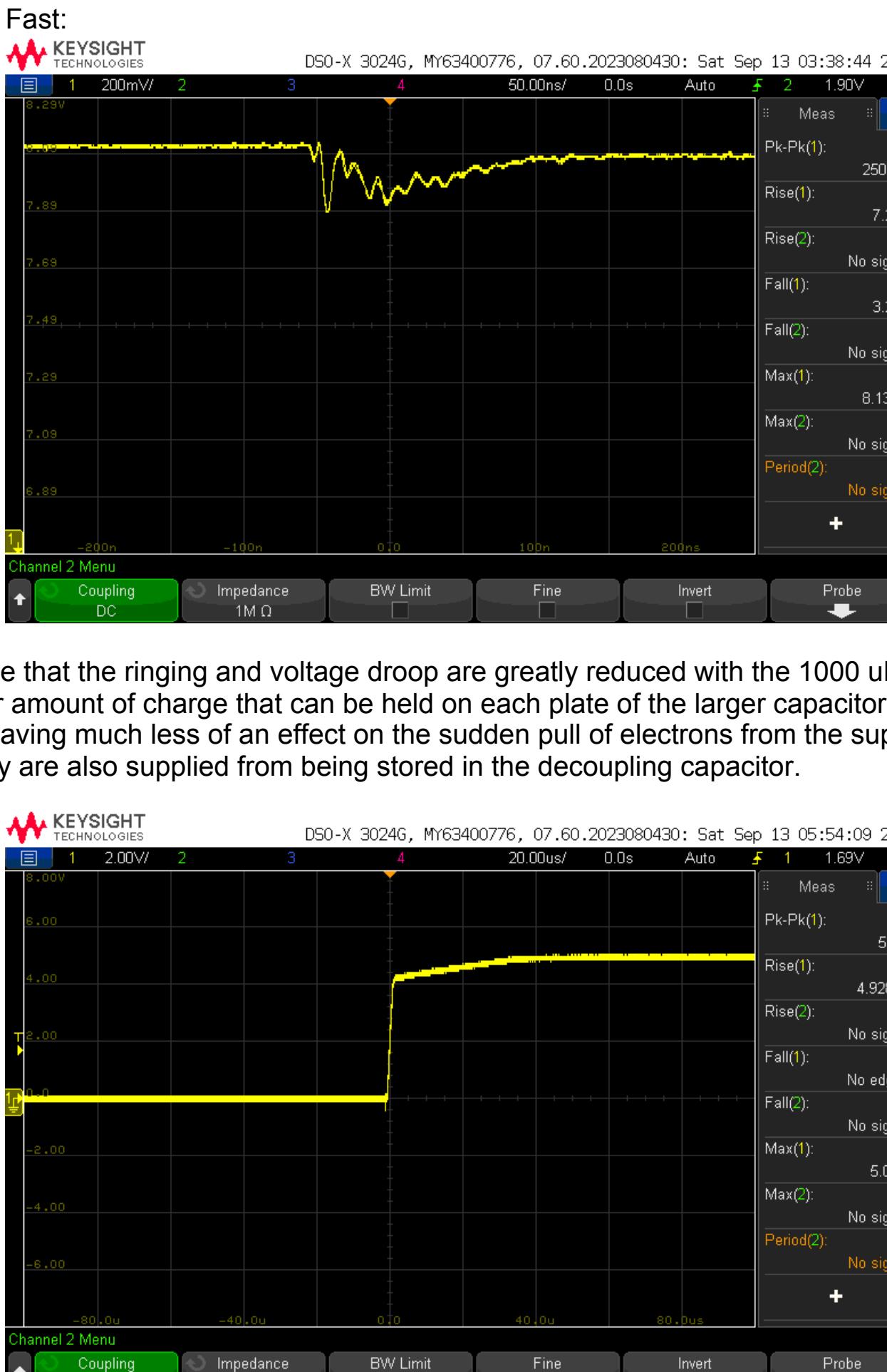
Component List

- o MCP601 Op amp
- o TIP41c MOSFET
- o 10 ohm resistor
- o 1uF capacitor
- o 1000 uF capacitor
- o Jumper wires
- o Solderless breadboard
- o Oscilloscope
- o x2 Scope probe

Circuit Photo and Back of Napkin Sketch

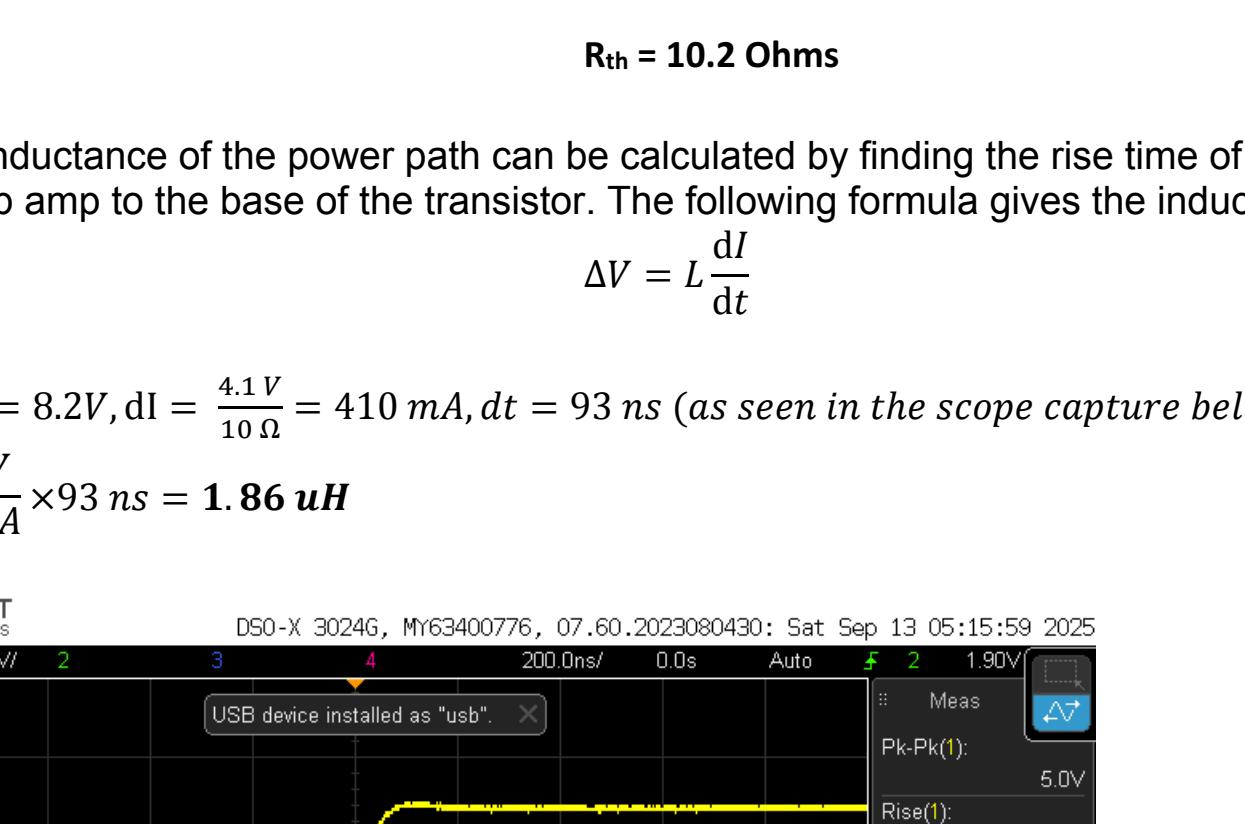


1000 uF capacitor:

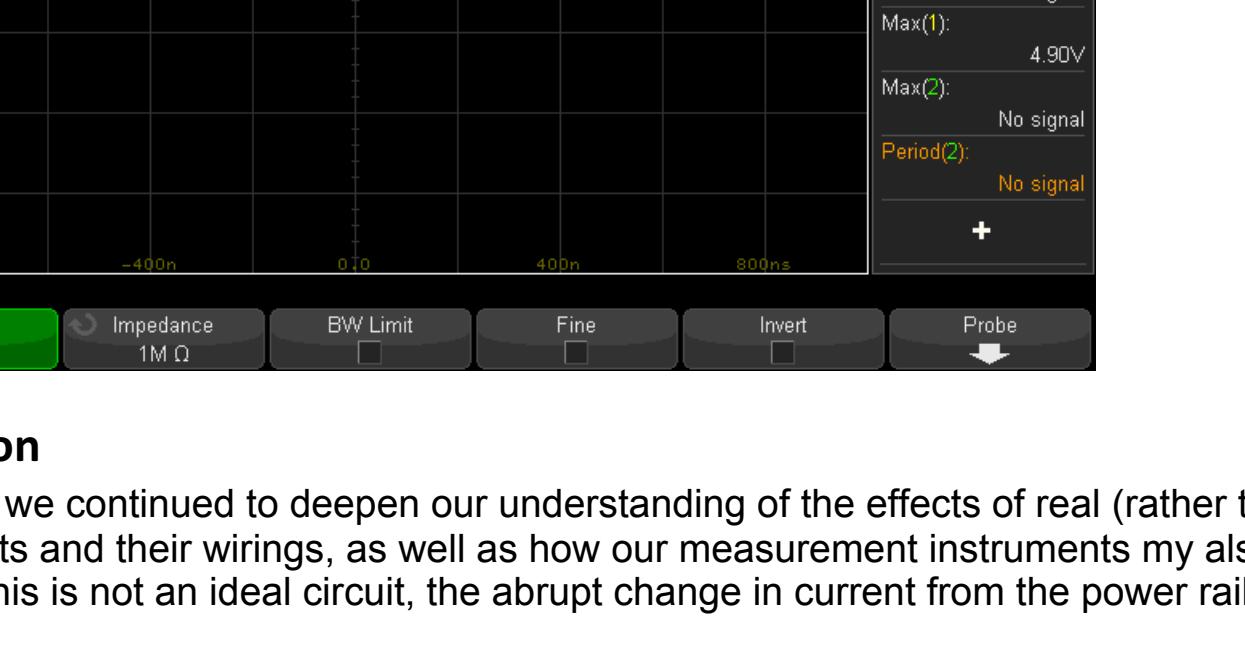


The impact on the switching noise of the 1 uF vs 1000 uF capacitor can be seen in the following screen captures:

1 uF capacitor:



1000 uF capacitor:



We can see that the ringing and voltage droop are greatly reduced with the 1000 uF capacitor. This is due to the greater amount of charge that can be held on each plate of the larger capacitor the moment the transistor turns on, having much less of an effect on the sudden pull of electrons from the supplied voltage from the 9V rail, as they are also supplied from being stored in the decoupling capacitor.

$$R_{th} = R_L \frac{V_{th} - V_L}{V_L}$$

$$R_{th} = 10\Omega \frac{8.3V - 4.1V}{4.1V}$$

$$R_{th} = 10.2 \text{ Ohms}$$

The loop inductance of the power path can be calculated by finding the rise time of the 5% duty cycle signal from the op amp to the base of the transistor. The following formula gives the inductance value:

$$\Delta V = L \frac{dI}{dt}$$

Where $\Delta V = 8.2V$, $dI = \frac{4.1V}{10\Omega} = 410\text{ mA}$, $dt = 93\text{ ns}$ (as seen in the scope capture below).

$$\Delta V = \frac{8.2V}{0.41A} \times 93\text{ ns} = 1.86\text{ uH}$$



Conclusion

In this lab, we continued to deepen our understanding of the effects of real (rather than ideal) circuit components and their wirings, as well as how our measurement instruments may also influence what we see. Because this is not an ideal circuit, the abrupt change in current from the power rail, or the switching noise, or causes