16.06
Prof. J. P. How
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11/03/17
Due: 11/10/17

16.06 Homework Assignment # 9

Format: Please staple your solutions and submit it at the beginning of the session on Friday.

Turn in any MATLAB plots, code, or Simulink systems generated as part of this Problem Set.

1. Consider the system:

$$G(s) = \frac{s + z}{s^2 + 2s + 10}$$

   a) If $z = 2$, is the point in the s-plane associated with $\zeta = 0.625$ and $\omega_n = 4$ on the root locus? Show enough work to justify your answer.

   b) What value of z would be necessary to ensure that the point in the s-plane associated with $\zeta = 0.707$ and $\omega_n = 6$ is on the root locus?

   c) Find the gain that produces the closed-loop poles at the desired location in b).

2. Consider the plant:

$$G(s) = \frac{29}{s^2 + 10s + 29}$$

   Overall goal: Design a Phase-Lead Compensator

   (a) The percent overshoot (P.O.) should not exceed 5% and $\omega_n = 8.5$ rad/s for the dominant quadratic mode. Where should the closed loop poles be located?

   (b) Using root locus techniques, work through, by hand, one iteration in the design of a phase-lead compensator to satisfy the above performance characteristics.

   (c) Sketch the pole-zero plot of the closed-loop compensated system. Give the exact location of all closed-loop poles and zeros.

   (d) Use MATLAB to plot the step response of the compensated system. Does the compensated system meet the design specifications? If not, explain why.

   (e) Conduct one more iteration of your compensator design. For this second iteration you can use MATLAB (e.g., the SISOtool). You do not need to show detailed workings by hand, but please explain the steps you went through. Give your final compensator design and show that it satisfies the design requirements.

3. Please consider the compensated plant transfer function found in Problem 2 above. This time, please design a phase lag compensator.

(a) Your design specifications are now: P.O = 5%, ω_n = 8.5 rad/s for the dominant quadratic mode, and e_{ss} < 10% for a step response. Where should the closed loop poles be located?

(b) Using root locus techniques, work through one iteration in the design of the phase-lag compensator to satisfy the design requirements (P.O., frequency, and steady state error).

(c) Sketch the pole-zero plot of the closed-loop compensated system. Give the exact location of all closed-loop poles and zeros.

(d) Use MATLAB to plot the step response of the compensated system. Does the compensated system meet the design specifications? If not, explain why, and describe (but do not carry out) the steps you would perform in the next iteration of your phase lag compensator.