Some more reading

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Hereafter, we provide you with some links if you want to dig further the different notions seen in this course. We encourage you to occasionally refer to these links whenever you deem useful.

1 Ocaml

- https://caml.inria.fr/distrib/ocaml-4.08/ocaml-4.08-refman.pdf: OCaml 4.08 reference manual (Part 4 for libraries reference).
- https://www.lri.fr/~filliatr/ens/compil/: "Langages de programmation et compilation" course page.

2 Architecture-specific documentation

2.1 RISC-V

- https://github.com/riscv/riscv-isa-manual/releases/download/draft-20200727-8088ba4/riscv-spec.pdf: RISC-V ISA (Chapter 24)
- http://home.ustc.edu.cn/~louwenqi/reference_books_tools/Computer%20Organization%20and%20Design%20RISC-V%20edition.pdf: The reference book on computer architecture, RISC-V edition.

2.2 MIPS

- https://cgi.cse.unsw.edu.au/~cs3231/doc/R3000.pdf: MIPS R3000 ISA (Appendix A).
- https://s3-eu-west-1.amazonaws.com/downloads-mips/I7200/I7200+product+launch/MIPS_nanomips32_ISA_TRM_01_01_MD01247.pdf: nanoMIPS32 ISA.
- http://courses.missouristate.edu/KenVollmar/MARS/: a MIPS Simulator (with its assembler).

2.3 X86

- https://www.intel.com/content/dam/www/public/us/en/documents/manuals/64-ia-32-architectures-software-developer-instruction-set-reference-manual-325383.pdf: IA-32, IA-64 ISA

2.4 ARM

- https://documentation-service.arm.com/static/5f1074ce0daa596235e834b5: ARMv7-A ISA
- https://documentation-service.arm.com/static/5f20515cbb903e39c84dc459: ARMv8-A ISA