謝 橋/Hsieh, Chiao/シェ チャオ

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RESEARCH INTEREST

Software Verification and Testing · Formal Methods · Cyber-physical System Verification

Topics: Assuring and certifying safety of cyber-physical systems containing complex or incompletely specified components. My main insight is to develop search algorithms for approximate abstractions of these components so that the approximations are tractable for existing formal verification techniques. In particular, I have been studying systems with vision-based perception, blackbox dynamics, and distributed coordination.

ONGOING RESEARCH PROJECTS

科研費
KAKENHI

FY 2024 – 2025 "Safety Verification of Black Box Cyber-Physical Systems via Lyapunov Stability Certificates"
Category: JSPS Grant-in-Aid for Research Activity Start-Up (Grant # 24K23861)

EDUCATION

CS@Illinois   *2015 – 2023*   Doctor of Philosophy

Reliable Autonomy Group in Department of Computer Science at University of Illinois Urbana-Champaign
Thesis: "Abstractions for Safety Assurance of Autonomous Systems"
Advisor: Prof. Mitra, Sayan · mitras@illinois.edu

臺大電子所   *2013 – 2015*   Master of Science

Verification Automation & Software Testing Lab in Graduate Institute of Electronics Engineering at National Taiwan University
Thesis: "Verifying Recursive Programs Using Intraprocedural Analyzers"
Advisors: Prof. Chen, Yu-Fang · yfc@iis.sinica.edu.tw, Prof. Wang, Farn · farn@ntu.edu.tw

臺大電機系   *2007 – 2011*   Bachelor of Science

Design Verification Lab in Department of Electrical Engineering at National Taiwan University
Projects: "Symbolic model checking on SystemC designs" with Prof. Huang, Chung-Yang · cyhuang@ntu.edu.tw

WORK EXPERIENCES

京都大学
情報学研究科   *2023 – Today*   Postdoc in **Computer Software Group** at Graduate School of Informatics, Kyoto University

CSL@Illinois   *2018 – 2023*   Graduate Research Assistant in **Reliable Autonomy Group** at Coordinated Science Laboratory, UIUC

CS@Illinois   *2017 – 2018*   TA at Department of Computer Science, UIUC

IBM Research   *Summer 2016*   Research Intern in **Cloud DevOps and Cognitive Platform group** at IBM Research, New York

CS@Illinois   *2015 – 2017*   RA in **Automated Software Engineering Group** at Department of Computer Science, UIUC

中研院資訊所   *2012 – 2015*   RA in **Programming Languages and Formal Methods Group** at Institute of Information Science, Academia Sinica, Taipei

海軍   *2011 – 2012*   Secondary Lieutenant. Compulsory Military Service at Republic of China Navy

SERVICES AND VOLUNTEERING

2024 · Fast Abstracts Track Committee at 35th Intl. Symp. Software Reliability Engineering (ISSRE 2024)
2022 · Student Volunteer at 34th Intl. Conf. Computer Aided Verification (CAV 2022)
2022 · Artifact Evaluation Committee at 2022 European Conf. Object-Oriented Programming (ECOOP 2022)
2018 · Graduate Mentor in Promoting Undergraduate Research in Engineering (PURE) at UIUC
2017 · Student Volunteer at 32nd Intl. Conf. Automated Software Engineering (ASE 2017)

PUBLICATIONS

京都大学   *2023-Now*   **Computer Software Group**

1. "Assuring Safety of Vision-based Swarm Formation Control"
Keyur Joshi, <u>Chiao Hsieh</u>, Sayan Mitra, Sasa Misailovic. *To appear in 35th Intl. Symp. Software Reliability Engineering (ISSRE 2024).* preprint doi:10.48550/arXiv.2210.00982

2. "Assuring Safety of Vision-based Swarm Formation Control"
<u>Chiao Hsieh</u>, Yangge Li, Yubin Koh, Sayan Mitra. *Proc. 2024 American Control Conf. (ACC 2024).* preprint doi:10.48550/arXiv.2210.00982

CS@Illinois   *2018-2023*   **Reliable Autonomy Group**

3. "Verifying Controllers with Vision-based Perception using Safe Approximate Abstractions"
<u>Chiao Hsieh</u>, Yangge Li, Dawei Sun, Keyur Joshi, Sasa Misailovic, Sayan Mitra. *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems (TCAD), vol. 41, no. 11, Nov. 2022.* doi:10.1109/TCAD.2022.3197508

4. "Industry-track: Challenges in Rebooting Autonomy with Deep Learned Perception"
Michael Abraham, Aaron Mayne, Tristan Perez, Italo Romani De Oliveira, Huafeng Yu, <u>Chiao Hsieh</u>, Yangge Li, Dawei Sun, and Sayan Mitra. *Proc. 2022 Intl. Conf. Embedded Software (EMSOFT 2022).* doi:10.1109/EMSOFT55006.2022.00016

5. "Programming Abstractions for Simulation and Testing on Smart Manufacturing Systems"
<u>Chiao Hsieh</u>, Daniel Wu, Yubin Koh, Sayan Mitra. *Proc. 18th Intl. Conf. Automation Science and Engineering (CASE 2022).* doi:10.1109/CASE49997.2022.9926564

6. "SkyTrakx: A Toolkit for Simulation and Verification of Unmanned Air-Traffic Management Systems."
<u>Chiao Hsieh</u>, Hussein Sibai, Hebron Taylor, Yifeng Ni, Sayan Mitra. *Proc. 2021 IEEE Intl. Intelligent Transportation Systems Conf. (ITSC 2021).* doi:10.1109/ITSC48978.2021.9564492

7. "Koord: A Language for Programming and Verifying Distributed Robotics Application."
Ritwika Ghosh, <u>Chiao Hsieh</u>, Sasa Misailovic, Sayan Mitra. *Proc. 2020 ACM SIGPLAN Intl. Conf. Object-Oriented Programming, Systems, Languages, and Applications (OOPSLA 2020).* doi:10.1145/3428300

8. "CyPhyHouse: A Programming, Simulation, and Deployment Toolchain for Heterogeneous Distributed Coordination."
Ritwika Ghosh, Joao P. Jansch-Porto, <u>Chiao Hsieh</u>, Amelia Gosse, Minghao Jiang, Hebron Taylor, Peter Du, Sayan Mitra, Geir Dullerud. *Proc. 2020 IEEE Intl. Conf. Robotics and Automation (ICRA 2020),* doi:10.1109/ICRA40945.2020.9196513

9. "Dione: A Protocol Verification System Built with Dafny for I/O Automata."
<u>Chiao Hsieh</u>, Sayan Mitra. *Proc. 15th Intl. Conf. integrated Formal Methods (iFM 2019).* doi:10.1007/978-3-030-34968-4_13

中研院資訊所   *2014-2016*   **Programming Languages and Formal Methods Group**

10. "PAC Learning-Based Verification and Model Synthesis."
Yu-Fang Chen, <u>Chiao Hsieh</u>, Ondřej Lengál, Tsung-Ju Lii, Ming-Hsien Tsai, Bow-Yaw Wang, Farn Wang. *Proc. 38th Intl. Conf. Software Engineering (ICSE 2016).* doi:10.1145/2884781.2884860

11. "CPArec: Verifying Recursive Programs via Source-to-Source Program Transformation - (Competition Contribution)."
Yu-Fang Chen, <u>Chiao Hsieh</u>, Ming-Hsien Tsai, Bow-Yaw Wang, Farn Wang. *Proc. 21st Intl. Conf. Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2015).* doi:10.1007/978-3-662-46681-0_35

12. "Verifying Recursive Programs Using Intraprocedural Analyzers."
Yu-Fang Chen, <u>Chiao Hsieh</u>, Ming-Hsien Tsai, Bow-Yaw Wang, Farn Wang. *Proc. 21st Intl. Static Analysis Symposium (SAS 2014).* doi:10.1007/978-3-319-10936-7_8

臺大電機系   *2012*   **Design Verification Lab**

13. "Symbolic model checking on SystemC designs."
Chun-Nan Chou, Yen-Sheng Ho, <u>Chiao Hsieh</u>, Chung-Yang (Ric) Huang. *Proc. 49th Annual Design Automation Conf. (DAC 2012).* doi:10.1145/2228360.2228421

TEACHING EXPERIENCE

CS@Illinois   *Spring 2018*   TA for CS 421 Programming Languages and Compilers

            Fall 2017   TA for CS 527 Topics in Software Engineering

臺大   *Summer 2015*   TA for Formosan Summer School on Logic, Language, and Computation (FLOLAC 2015)