addi t4,t4,2 t4=2

sw t4,0(t2) 0(mem)=2

lw t1,0(t2) t1=2

addi t3,t1,1 t3=3

test4 32(t2)=2

addi t3,t1,1 t3= 3

lw t1,32(t2) t1 = 2