**MARKING SCHEME – Exercise 2**

**AUTUMN 2018/2019**

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| **Student Name:** | SONIA MUBASHER |
| **Student No:** | 20129428 |
| **Module Code:** | **COMP1027** |
| **Title of Module:** | **Computer Fundamentals** |

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| **Components** | **Marks distribution** | **Marks Awarded** |
| Part I: Combinatorial Circuits |  |  |
| * Task A (Mux & DMux) | 5 |  |
| * Task B (Mux4way16, Mux8way16, DMux4way, DMux8way) | 15 |  |
| * Task C (Alu) | 20 |  |
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| Part II: Sequential Circuits |  |  |
| - Task D (Bit & Register) | 10 |  |
| - Task D (RAM8, RAM64) | 20 |  |
| * Task E (RAM512, RAM4K) | 10 |  |
| * Task E (PC) | 20 |  |
| **Deductions**: |  |  |
| * Late submission |  |  |
| * Penalties (Presentation,….) |  |  |
| * Lack of Understanding |  |  |
|  | Out of 100 |  |
| **Total Marks** | |  |

**Examiner’s Comments/Feedback:**

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This marksheet constitutes the **written feedback** of the submitted work. Contact the module convenor if more detailed breakdown is required. Date: \_\_\_\_\_\_\_\_\_\_\_\_\_