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1 The following assembler directives are supported
2
3 .data           - start of data segment
4 .text          - start of code segment
5 .code          - start of code segment (same as .text)
6 .org <n>        - start address
7 .space <n>      - leave n empty bytes
8 .asciiZ <s>    - enters zero terminated ascii string
9 .ascii <s>     - enter ascii string
10 .align <n>     - align to n-byte boundary
11 .word <n1>,<n2>.. - enters word(s) of data (64-bits)
12 .byte <n1>,<n2>.. - enter bytes
13 .word32 <n1>,<n2>.. - enters 32 bit number(s)
14 .word16 <n1>,<n2>.. - enters 16 bit number(s)
15 .double <n1>,<n2>.. - enters floating-point number(s)
16
17 where <n> denotes a number like 24, <s> denotes a string like "fred", and
18 <n1>,<n2>.. denotes numbers separated by commas. The integer registers can
19 be referred to as r0-r31, or R0-R31, or $0-$31 or using standard MIPS
20 pseudo-names, like $zero for r0, $t0 for r8 etc. Note that the size of an
21 immediate is limited to 16-bits. The maximum size of an immediate register
22 shift is 5 bits (so a shift by greater than 31 is illegal).
23
24 Floating point registers can be referred to as f0-f31, or F0-F31
25
26 The following instructions are supported
27
28 lb           - load byte
29 lbu          - load byte unsigned
30 sb           - store byte
31 lh           - load 16-bit half-word
32 lhu          - load 16-bit half word unsigned
33 sh           - store 16-bit half-word
34 lw           - load 32-bit word
35 lwu          - load 32-bit word unsigned
36 sw           - store 32-bit word
37 ld           - load 64-bit double-word
38 sd           - store 64-bit double-word
39 ld           - load 64-bit floating-point
40 sd           - store 64-bit floating-point
41 halt        - stops the program
42
43 daddi        - add immediate
44 daddui       - add immediate unsigned
45 andi         - logical and immediate
46 ori          - logical or immediate
47 xori         - exclusive or immediate
48 lui          - load upper half of register immediate
49
50 slti         - set if less than or equal immediate
51 sltiu        - set if less than or equal immediate unsigned
52
53 beq          - branch if pair of registers are equal
54 bne          - branch if pair of registers are not equal
55 beqz         - branch if register is equal to zero
56 bnez         - branch if register is not equal to zero
57
58 j            - jump to address
59 jr           - jump to address in register
60 jal          - jump and link to address (call subroutine)
61 jalr         - jump and link to address in register (call subroutine)
62
63 dsll         - shift left logical
64 dsrl         - shift right logical

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65 dsra        - shift right arithmetic
66 dsllv       - shift left logical by variable amount
67 dsrlv       - shift right logical by variable amount
68 dsrav       - shift right arithmetic by variable amount
69 movz        - move if register equals zero
70 movn        - move if register not equal to zero
71 nop         - no operation
72 and         - logical and
73 or          - logical or
74 xor         - logical xor
75 slt         - set if less than
76 sltu        - set if less than unsigned
77 dadd        - add integers
78 daddu       - add integers unsigned
79 dsub        - subtract integers
80 dsubu       - subtract integers unsigned
81 dmul        - signed integer multiplication
82 dmulu       - unsigned integer multiplication
83 ddiv        - signed integer division
84 ddivu       - unsigned integer division
85
86 add.d       - add floating-point
87 sub.d       - subtract floating-point
88 mul.d       - multiply floating-point
89 div.d       - divide floating-point
90 mov.d       - move floating-point
91 cvt.d.l     - convert 64-bit integer to a double floating-point format
92 cvt.l.d     - convert double floating-point to a 64-bit integer format
93 c.lt.d      - set FP flag if less than
94 c.le.d      - set FP flag if less than or equal to
95 c.eq.d      - set FP flag if equal to
96 bc1f       - branch to address if FP flag is FALSE
97 bc1t       - branch to address if FP flag is TRUE
98 mtc1        - move data from integer register to floating-point register
99 mfc1        - move data from floating-point register to integer register
100
101 Memory Mapped I/O area
102
103 Addresses of CONTROL and DATA registers
104
105 CONTROL: .word32 0x10000
106 DATA:   .word32 0x10008
107
108 Set CONTROL = 1, Set DATA to Unsigned Integer to be output
109 Set CONTROL = 2, Set DATA to Signed Integer to be output
110 Set CONTROL = 3, Set DATA to Floating Point to be output
111 Set CONTROL = 4, Set DATA to address of string to be output
112 Set CONTROL = 5, Set DATA+5 to x coordinate, DATA+4 to y coordinate, and DATA to RGB
    colour to be output
113 Set CONTROL = 6, Clears the terminal screen
114 Set CONTROL = 7, Clears the graphics screen
115 Set CONTROL = 8, read the DATA (either an integer or a floating-point) from the keyboard
116 Set CONTROL = 9, read one byte from DATA, no character echo.

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