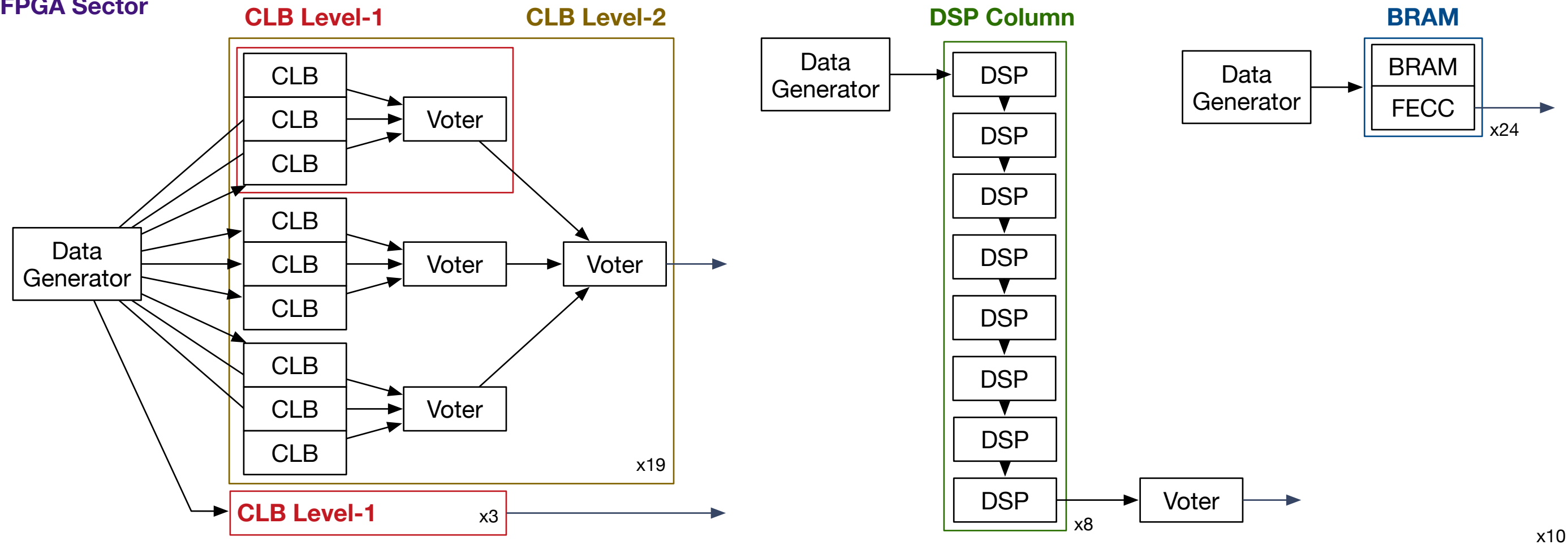


**FPGA Sector**



**SEM**

