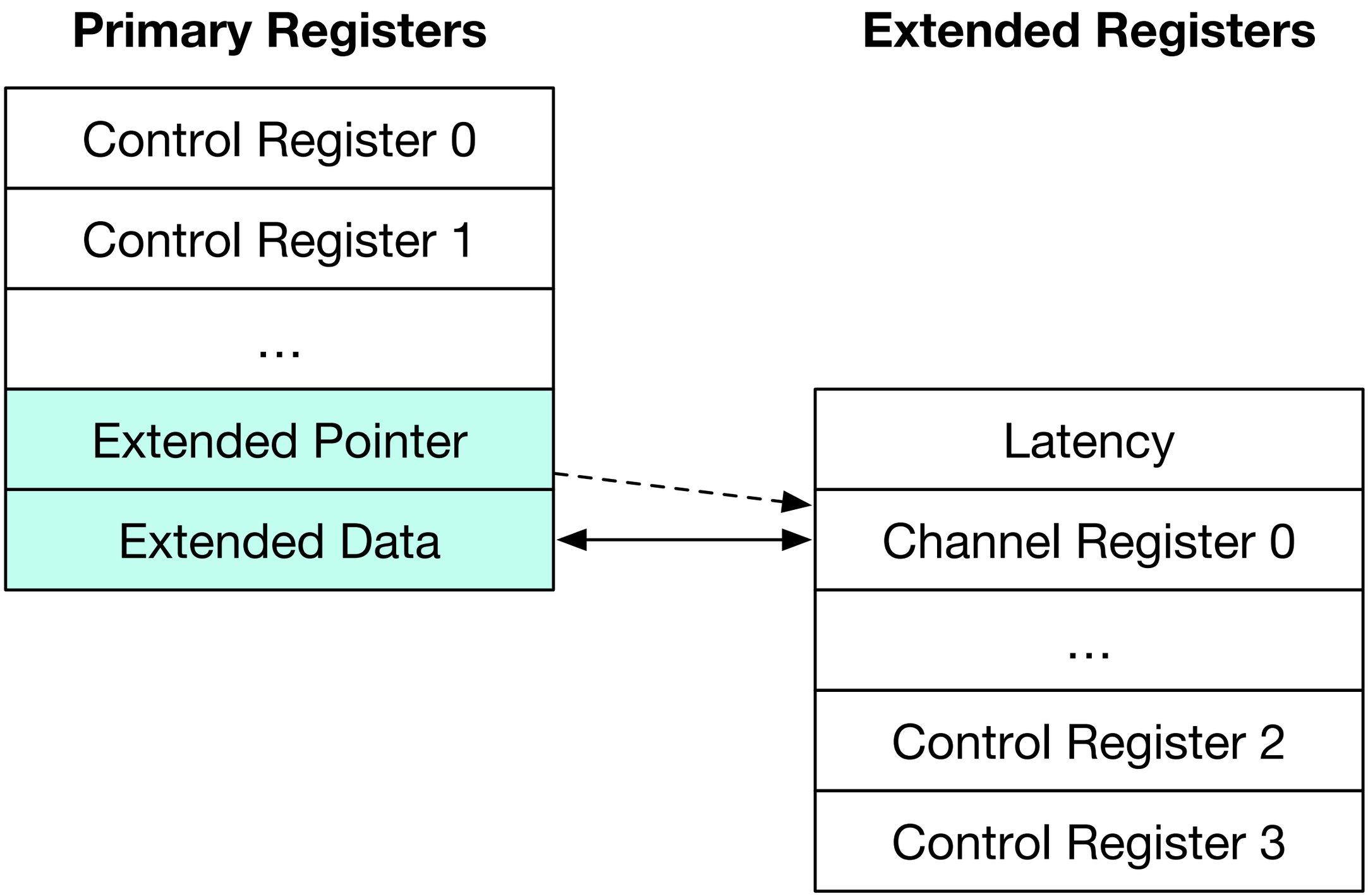
Primary Registers
Extended Registers
Control Register 0
Control Register 1
...
Extended Pointer
Extended Data
Latency
Channel Register 0
...
Control Register 2
Control Register 3