Performance Analysis and Automatic Tuning of Hash Aggregation on GPUs

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GPU cores

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Introduction

Hash aggregation:

- Used to implement GROUP BY and DISTINCT.
- Can be significantly accelerated on GPUs.

Example:

- Query: SELECT G, sum(V)
 FROM R GROUP BY G
- Processor: AMD A10-7850K APU (CPU and GPU cores on same die).
- GPU cores 1.6 4.8 times faster.

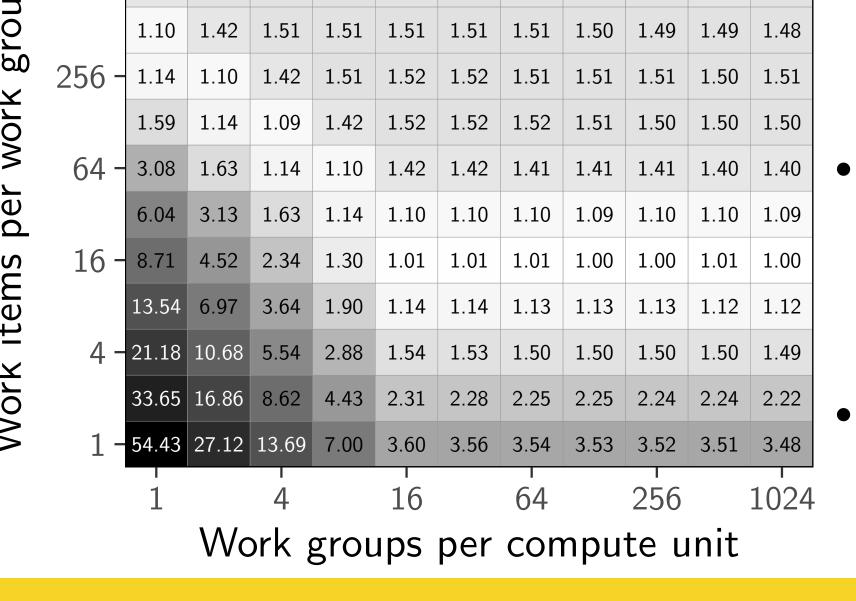
Previous work:

- Analyzed influence of parallelization strategy and thread configuration on aggregation performance.
- Formulated heuristics based on analysis of single NVIDIA Kepler GPU.

Our contributions:

- 1. Performance analysis on six diverse AMD and NVIDIA GPUs.
- 2. Automatic tuning of execution parameters at runtime.

Thread configuration search space



1.51 1.50 1.51 1.50 1.51 1.49 1.48 1.49 1.46 1.49

• Number of threads determines execution runtime.

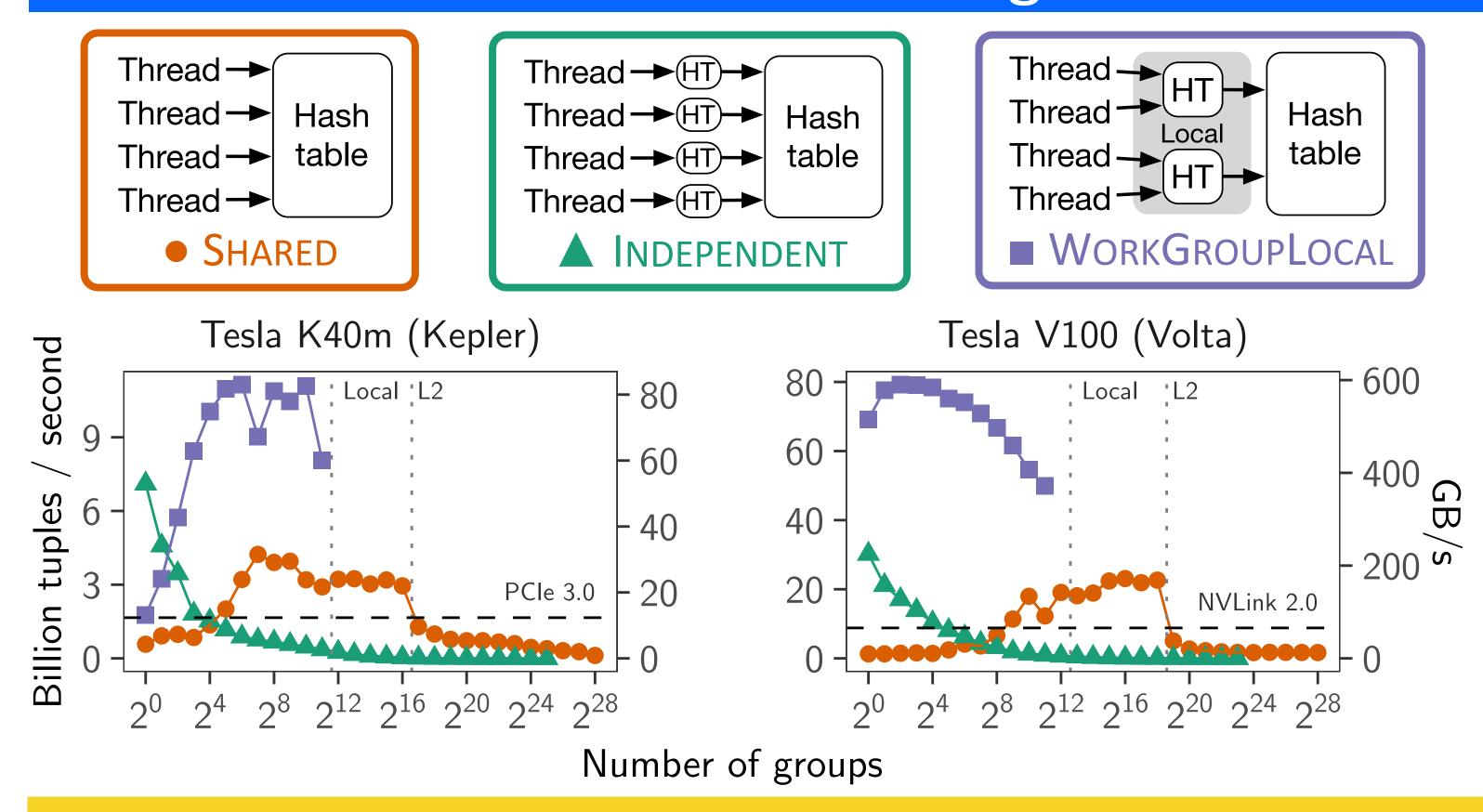
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CPU cores

- Performance plateaus:
 Adjacent thread configurations
 have similar runtimes.
- AMD GPUs exhibit high degree of runtime variation.

(3) Single local minimum when we account for runtime variation.

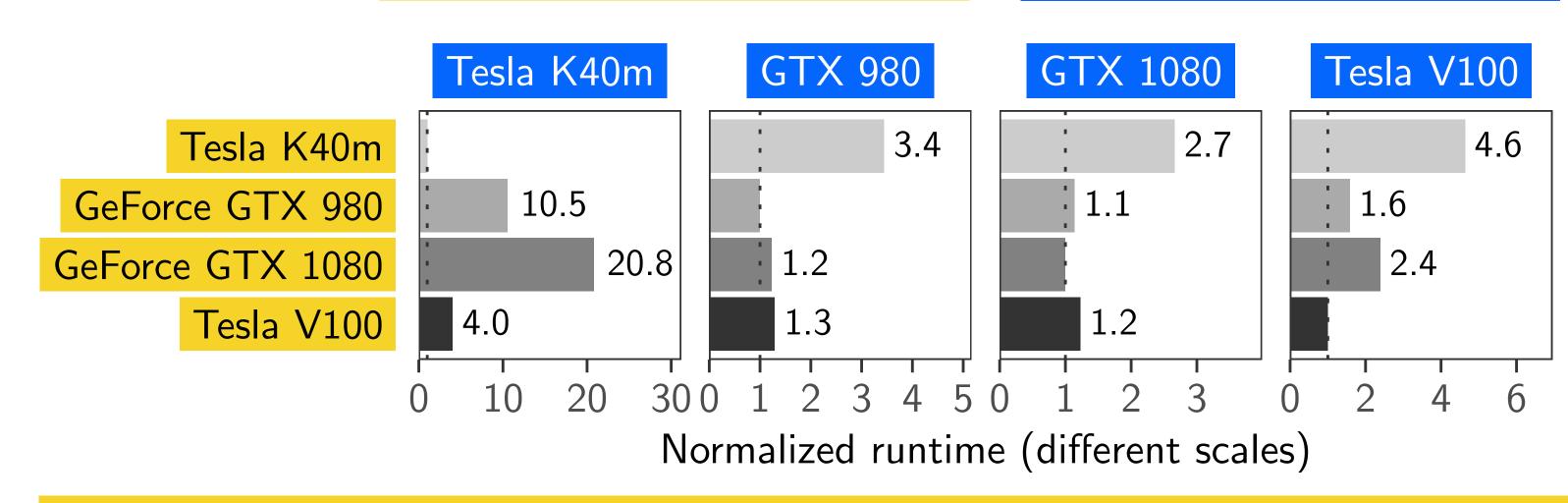
Parallelization strategies



- (1) INDEPENDENT aggregation is not competitive on newer GPUs which implement fast atomics on local memory.
- (2) Aggregation throughput limited by global GPU memory latency (and not transfer bandwidth) when hash table exceeds L2 cache.

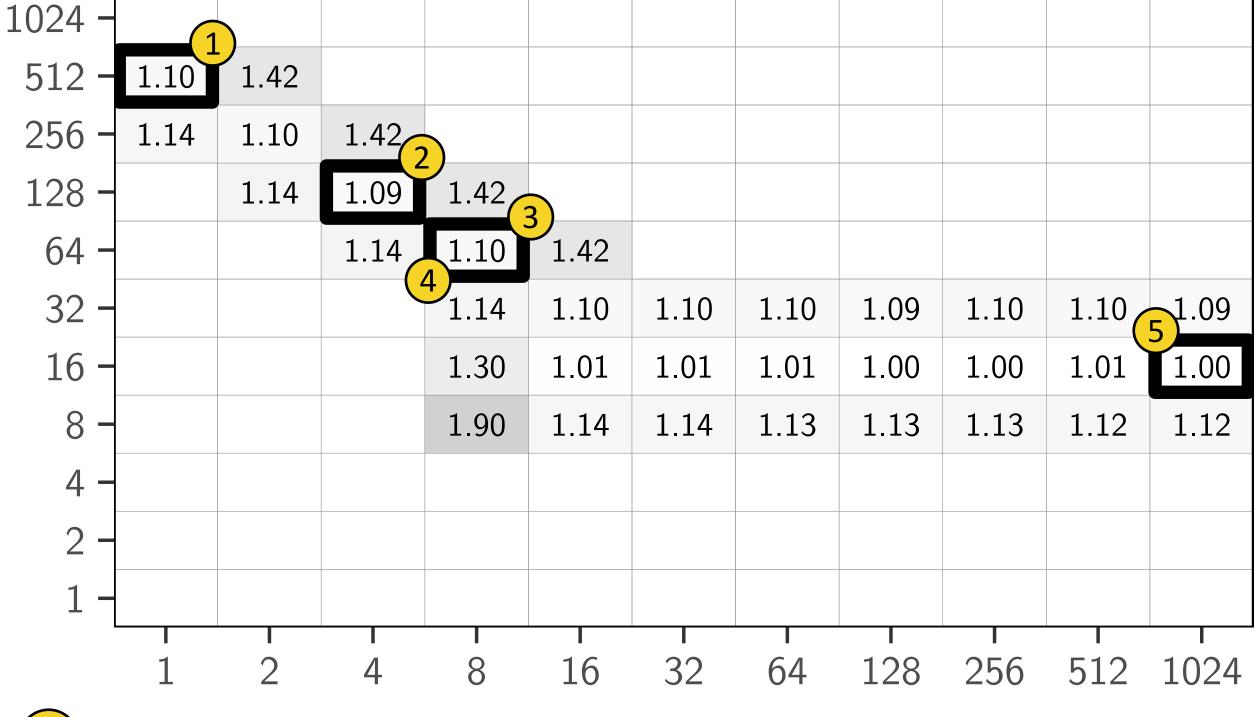
Performance penalty

A configuration optimized for a specific GPU is executed on another GPU.



- (4) The optimal thread configuration is highly GPU-dependent.
- (5) Suboptimal configurations lead to large performance penalties.

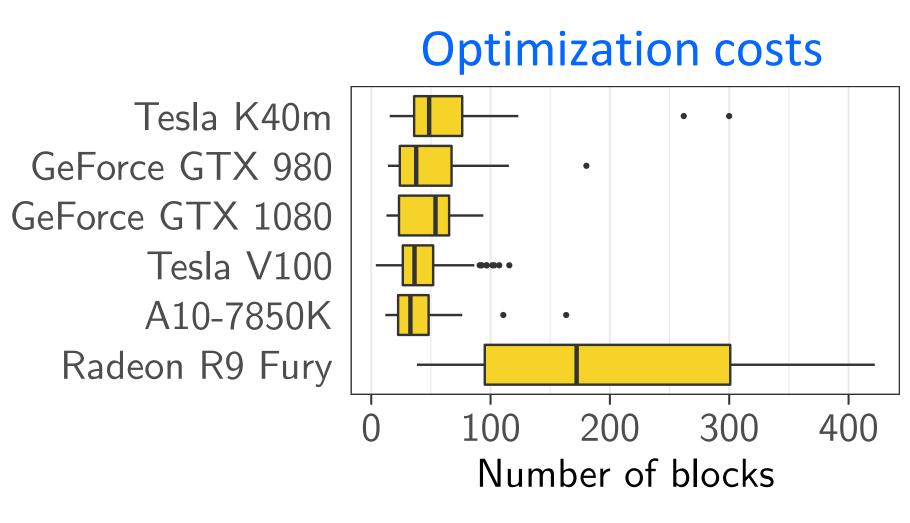
Optimization algorithm

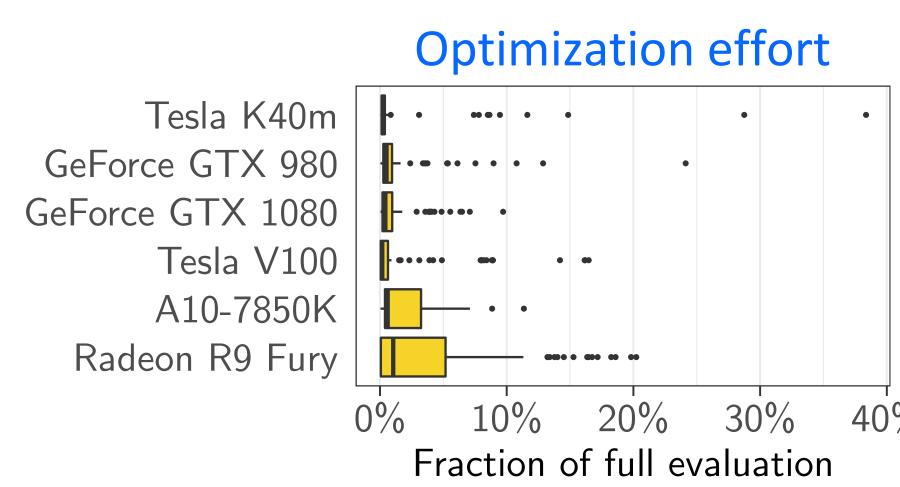


- 1) Start with initial thread configuration.
- 2 Follow gradient in search space to local minimum.
- Branch search path at performance plateaus.
- Prune slow branches.
- Stop at minimum when there are no more branches.

Evaluation







Main takeaways

- (1) Heuristics to select execution parameters, which are derived from analyzing a single GPU, are not generalizable to other GPUs.
- (2) We can optimize execution parameters by following the gradient in the search space and branching the search at performance plateaus.











