

IC CAD Project

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1 Theoretical Design

There are 3 criteria that determine the design of the amplifier. First is the central frequency, which is the frequency of the LC tank:

$$f = \frac{1}{2\pi\sqrt{LC}} = 8 \text{ GHz} \Rightarrow LC = 3.95 \times 10^{-22}$$

As the inductor values are limited due to the design rules, I first checked to see what values are available and then chose the capacitor accordingly:

$$L = 1.44 \text{ nH} \quad , \quad C = 274 \text{ fF}$$

Then I used the fractional bandwidth to find the quality factor of the LC tank:

$$\frac{f_C}{\Delta f} = Q = 10 = \frac{R_p}{L\omega} \Rightarrow R_p = 10L\omega = 725 \Omega$$

Then, I find the transconductance of the transistors by ignoring the output resistance of the transistor compared to the parallel resistance of the tank and assuming the capacitor to be ideal:

$$A_v = 25 \text{ dB} = 17.78 = R_p g_m \Rightarrow g_m = 23.4 \text{ mS}$$

Then, using ADS, I plotted the g_m versus the drain current of an NMOS transistor with $W = 50 \mu m$, as seen in figure 1 and as the current for this width would be too much, I increased the width as well to reach $W = 90 \mu m$ and a current of 2.16 mA. The size of the tail transistor is chosen to be twice that of the input transistors and the size of the current mirror transistor is one-tenth of the tail transistor.

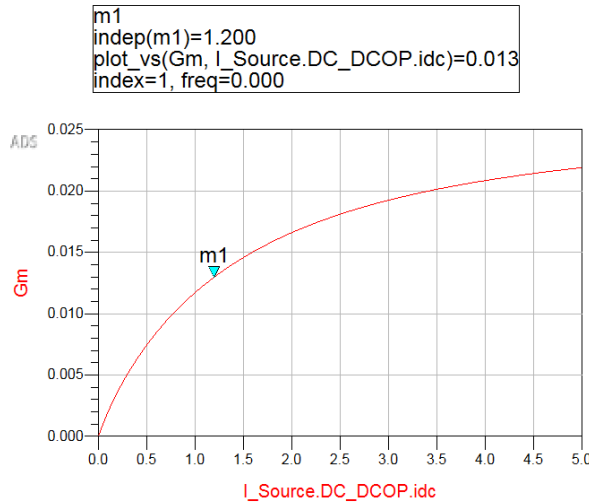


Figure 1: g_m vs. I_D

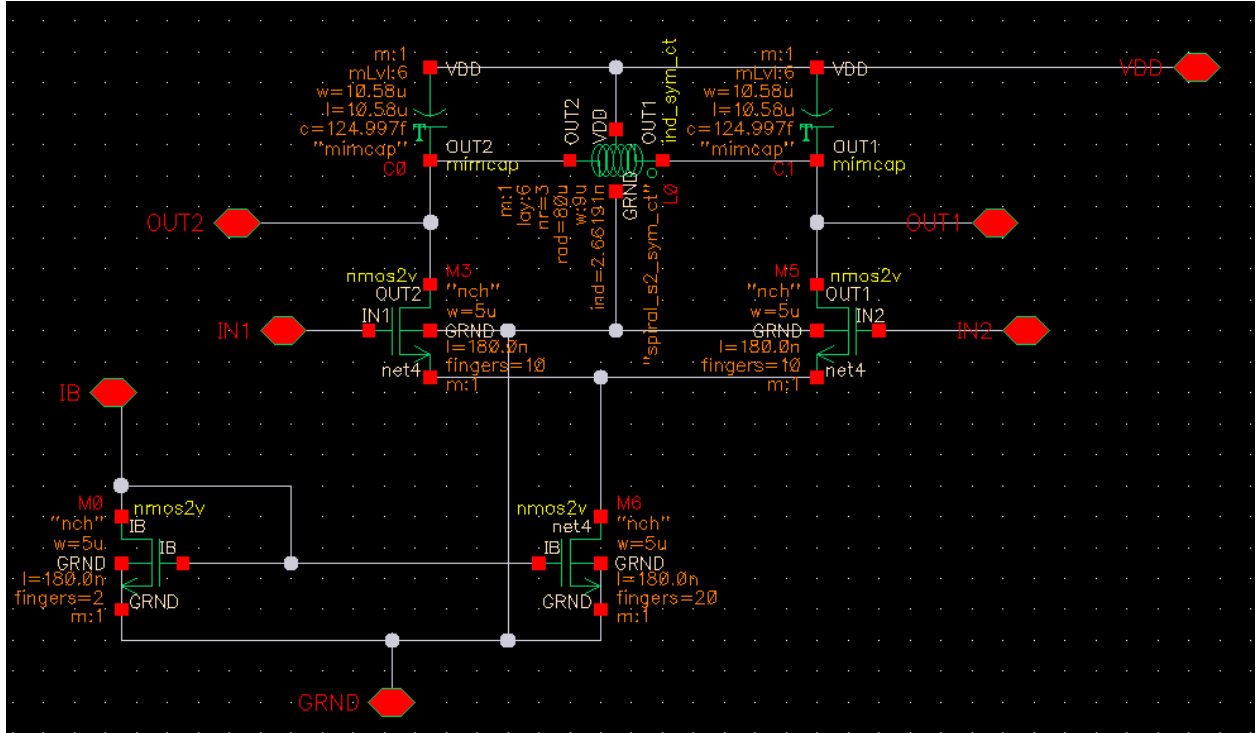


Figure 2: The final schematics

	Width (um)	Length (nm)	Fingers
Input transistors (M3 & M5)	5	180	10
Tail transistor (M6)	5	180	20
Current mirror transistor (M0)	5	180	2

Table 1: The transistor specification

2 Schematic

After some trial and error, the circuit in fig. 2 was reached that satisfies all but one of the requirements. I created a symbol from the schematic and used it for further testing.

I used two test benches to test the properties of my design: one for the AC analysis to find the gain and bandwidth, and another for the PSS analysis to show the linearity requirements (fig. 3).

The analysis specifications for the AC analysis (fig. 4), the compression point analysis (fig. 82), the IPn analysis (fig. 6), and the noise analysis (fig. 7) are shown below. The analysis results, including the operating points of the transistors from a DC analysis, are shown in the following figures and in table 4.

	Radius (um)	Number of turns	Width (um)	Inductnce (nH)
Inductor (L0)	80	3	9	2.661

Table 2: Inductor specifications

	Width (um)	Length (um)	Capacitance (fF)
Capacitors (C0 & C1)	10.58	10.58	124.997

Table 3: Capacitor specifications

Power comsumption (mW)	8.378
Gain (@ 8GHz, dB)	25.891
Bandwidth (GHz)	1.153
1dB compression point (dBm)	-4.359
3rd input intercept point (dBm)	4.668
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.071

Table 4: The circuit properties at normal temperature

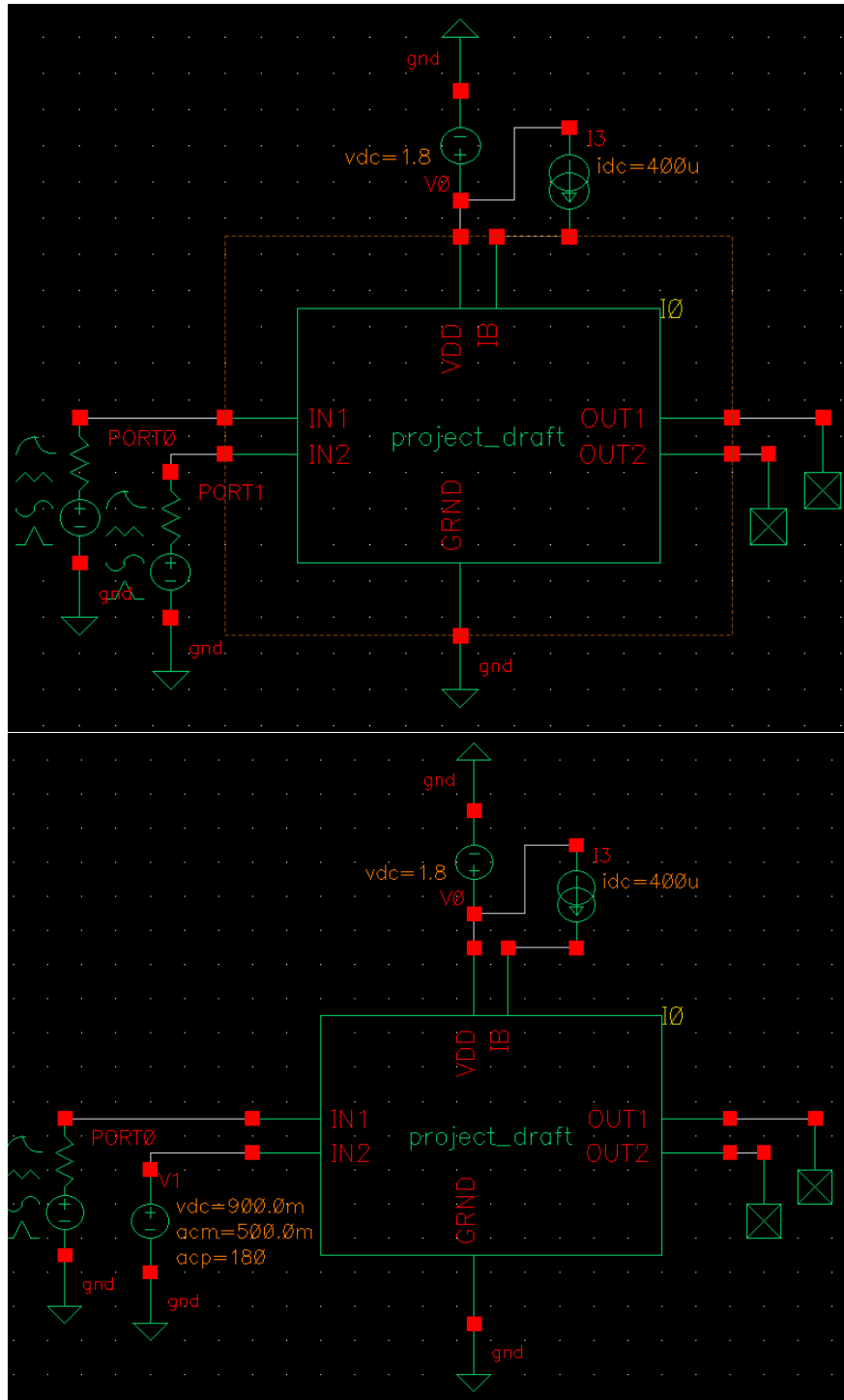


Figure 3: The test benches

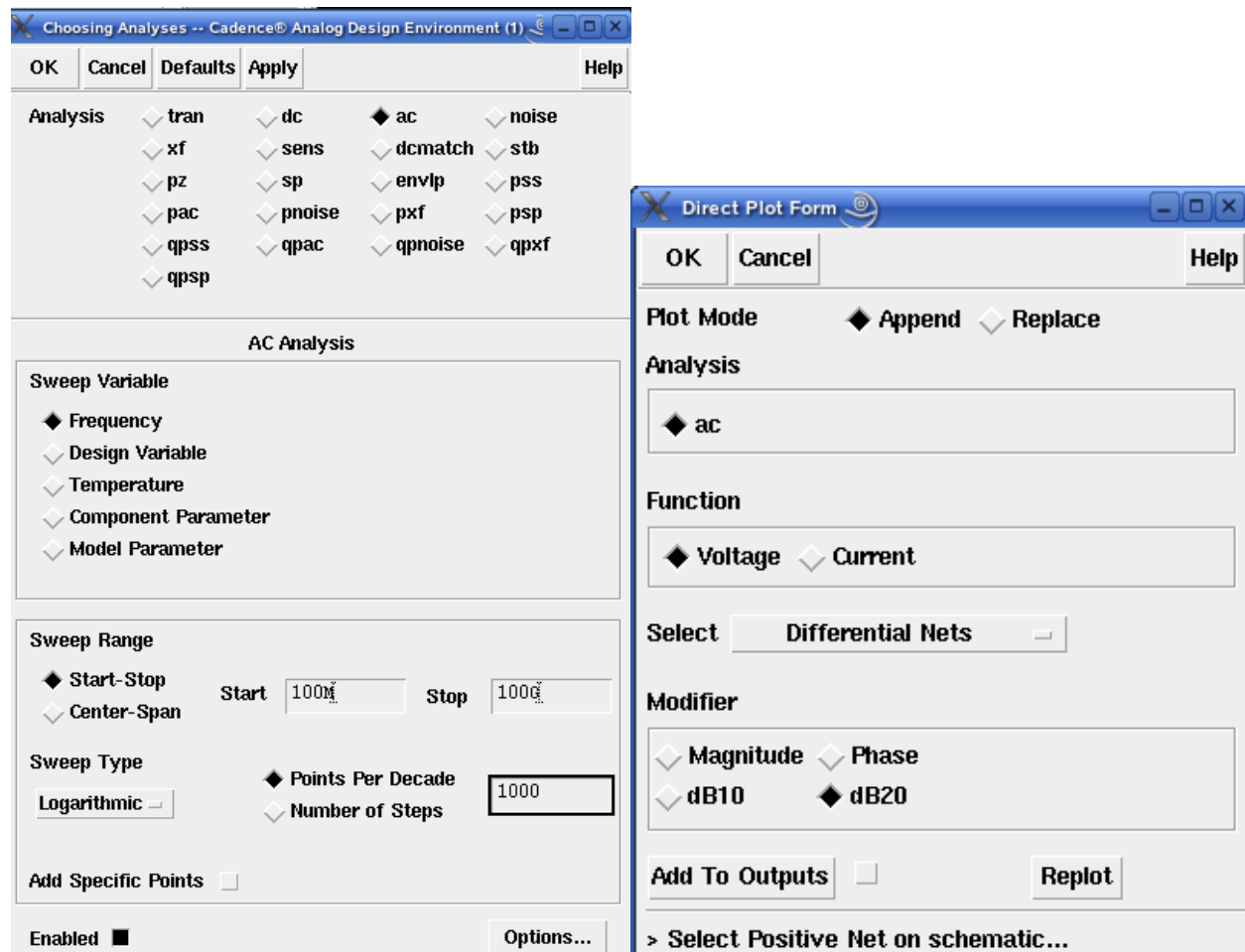


Figure 4: AC analysis setup

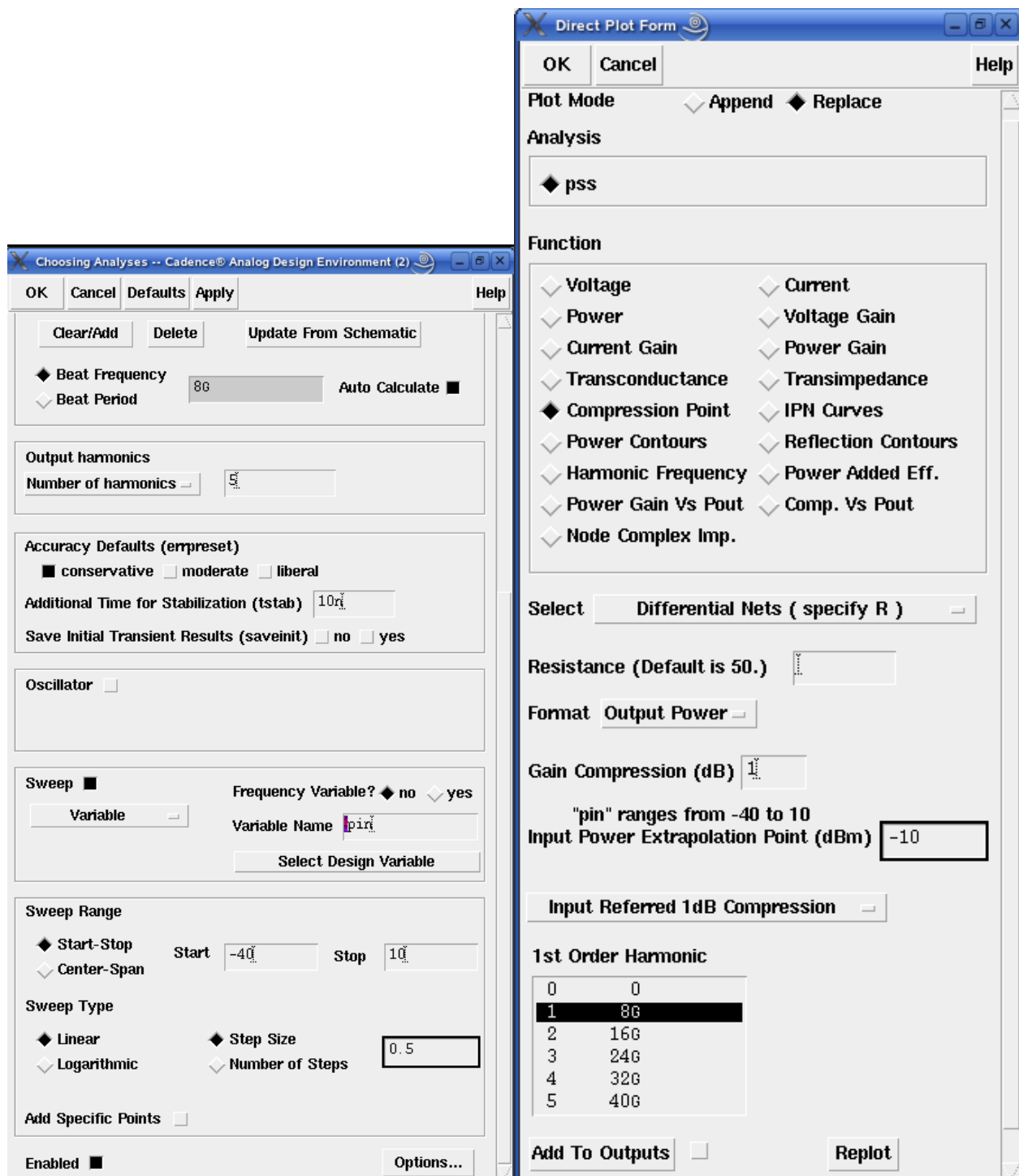


Figure 5: The compression point analysis setup

Choosing Analyses -- Cadence® Analog Design Environment (2)

OK Cancel Defaults Apply Help

☒ Beat Frequency ☐ Beat Period 10M Auto Calculate ☒

Output harmonics From (Hz) 0 Max. Order 3
 Select from range To (Hz) 1e12

Index	Frequency	frf1	frf2
1599	15.99G	1	1
1600	16G	2	0
2397	23.97G	0	3
2398	23.98G	1	2
2399	23.99G	2	1
2400	24G	3	0

Accuracy Defaults (empreset)
☒ conservative ☐ moderate ☐ liberal

Additional Time for Stabilization (tstab) 10n

Save Initial Transient Results (saveinit) ☐ no ☐ yes

Oscillator ☐

Sweep ☒ Frequency Variable? ☒ no ☐ yes
 Variable Variable Name pin
 Select Design Variable

Sweep Range
☒ Start-Stop Start -40 Stop 10
☐ Center-Span

Sweep Type
☒ Linear ☐ Step Size 0.5
☐ Logarithmic ☐ Number of Steps

Direct Plot Form

OK Cancel Help

Plot Mode ☐ Append ☒ Replace

Analysis
☒ pss

Function
☐ Voltage ☐ Current
☐ Power ☐ Voltage Gain
☐ Current Gain ☐ Power Gain
☐ Transconductance ☐ Transimpedance
☐ Compression Point ☒ IPN Curves
☐ Power Contours ☐ Reflection Contours
☐ Harmonic Frequency ☐ Power Added Eff.
☐ Power Gain Vs Pout ☐ Comp. Vs Pout
☐ Node Complex Imp.

Select Differential Nets (specify R)

Resistance (Default is 50.)

Circuit Input Power ☐ Single Point
☒ Variable Sweep ("pin")

"pin" ranges from -40 to 10
 Input Power Extrapolation Point (dBm) -20

Input Referred IP3 Order 3rd

0	0
1	10M
798	7.98G
799	7.99G
800	8G
801	8.01G

0	0
1	10M
798	7.98G
799	7.99G
800	8G
801	8.01G

Add To Outputs ☐ Replot

Figure 6: The IP3 analysis setup

Choosing Analyses -- Cadence® Analog Design Environment (2)

OK Cancel Defaults Apply Help

Analysis

<input type="checkbox"/> tran	<input type="checkbox"/> dc	<input type="checkbox"/> ac	<input checked="" type="checkbox"/> noise
<input type="checkbox"/> xf	<input type="checkbox"/> sens	<input type="checkbox"/> dcmatch	<input type="checkbox"/> stb
<input type="checkbox"/> pz	<input type="checkbox"/> sp	<input type="checkbox"/> envlp	<input type="checkbox"/> pss
<input type="checkbox"/> pac	<input type="checkbox"/> pnoise	<input type="checkbox"/> pxf	<input type="checkbox"/> psp
<input type="checkbox"/> qpss	<input type="checkbox"/> qpac	<input type="checkbox"/> qpnoise	<input type="checkbox"/> qpxf
<input type="checkbox"/> qpzp			

Noise Analysis

Sweep Variable

☒ Frequency

☐ Design Variable

☐ Temperature

☐ Component Parameter

☐ Model Parameter

Sweep Range

☒ Start-Stop

Start Stop

☐ Center-Span

Sweep Type

☒ Points Per Decade

☐ Number of Steps

Add Specific Points ☐

Output Noise

Positive Output Node

Negative Output Node

Input Noise

Input Voltage Source

Enabled ☒

Figure 7: The noise analysis setup

signal	OP("I0.M3" "??")	signal	OP("I0.M5" "??")
betaeff	152.169m	betaeff	152.169m
cbb	14.1671f	cbb	14.1671f
cbd	43.9535a	cbd	43.9535a
cbg	-9.88887f	cbg	-9.88887f
cbs	-4.32222f	cbs	-4.32222f
cdb	-6.92121a	cdb	-6.92121a
cdd	18.3155f	cdd	18.3155f
cdg	-18.3449f	cdg	-18.3449f
cds	36.2751a	cds	36.2751a
cgb	-7.43434f	cgb	-7.43434f
cgd	-17.8413f	cgd	-17.8413f
cgg	86.9657f	cgg	86.9657f
cgs	-61.6901f	cgs	-61.6901f
cjd	18.8953f	cjd	18.8953f
cjs	25.7643f	cjs	25.7643f
csb	-6.72587f	csb	-6.72587f
csd	-518.225a	csd	-518.225a
csg	-58.7319f	csg	-58.7319f
css	65.976f	css	65.976f
gds	661.066u	gds	661.066u
gm	17.2657m	gm	17.2657m
gmbs	2.85131m	gmbs	2.85131m
gmoverid	8.11562	gmoverid	8.11562
ibulk	-81.416p	ibulk	-81.416p
id	2.12746m	id	2.12746m
ids	2.12746m	ids	2.12746m
is	-2.12746m	is	-2.12746m
pwr	1.83323m	pwr	1.83323m
region	2	region	2
reversed	0	reversed	0
ron	405.035	ron	405.035
type	0	type	0
vbs	-928.966m	vbs	-928.966m
vds	861.696m	vds	861.696m
vdsat	136.884m	vdsat	136.884m
vgs	871.034m	vgs	871.034m
vth	717.844m	vth	717.844m
signal	OP("I0.M6" "??")	signal	OP("I0.M0" "??")
betaeff	317.012m	betaeff	31.6968m
cbb	38.6198f	cbb	3.86565f
cbd	33.5063a	cbd	2.13252a
cbg	-21.7144f	cbg	-2.16887f
cbs	-16.9389f	cbs	-1.69891f
cdb	-19.6438a	cdb	-4.09178a
cdd	36.6262f	cdd	3.66618f
cdg	-36.675f	cdg	-3.67487f
cds	68.4437a	cds	12.7847a
cgb	-15.5338f	cgb	-1.53488f
cgd	-36.413f	cgd	-3.64246f
cgg	175.336f	cgg	17.5322f
cgs	-123.389f	cgs	-12.3549f
cjd	44.7061f	cjd	4.78904f
cjs	67.0198f	cjs	10.5896f
csb	-23.0663f	csb	-2.32668f
csd	-246.725a	csd	-25.8479a
csg	-116.946f	csg	-11.6885f
css	140.259f	css	14.041f
gds	931.349u	gds	106.196u
gm	34.5142m	gm	3.31249m
gmbs	9.73182m	gmbs	943.709u
gmoverid	8.1116	gmoverid	8.28122
ibulk	-807.071p	ibulk	-54.9841f
id	4.25492m	id	400u
ids	4.25492m	ids	400u
is	-4.25492m	is	-400u
pwr	3.95268m	pwr	268.082u
region	2	region	2
reversed	0	reversed	0
ron	218.327	ron	1.67551K
type	0	type	0
vbs	0	vbs	0
vds	928.966m	vds	670.205m
vdsat	128.391m	vdsat	128.065m
vgs	670.205m	vgs	670.205m
vth	516.042m	vth	516.817m

Figure 8: The DC operating points of all transistors

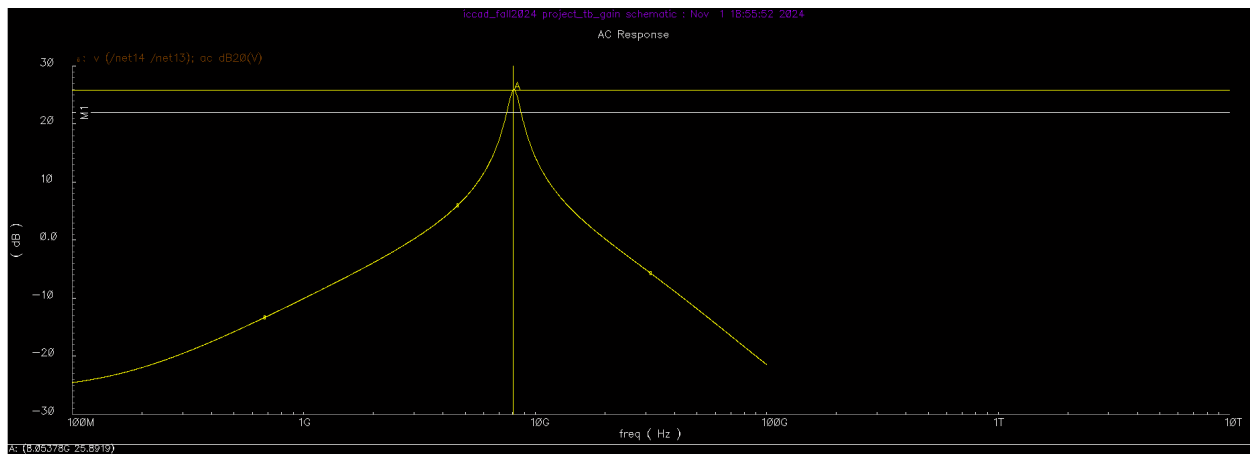


Figure 9: The gain plot

```
Curve name map:
-----

Curve1      - v (/net14 /net13); ac dB20(V)

Curve table:
-----

                                Y value                Curve1
M1                                22      7.535350187G
                                           8.6887684874G
```

Figure 10: The bandwidth, using a horizontal marker

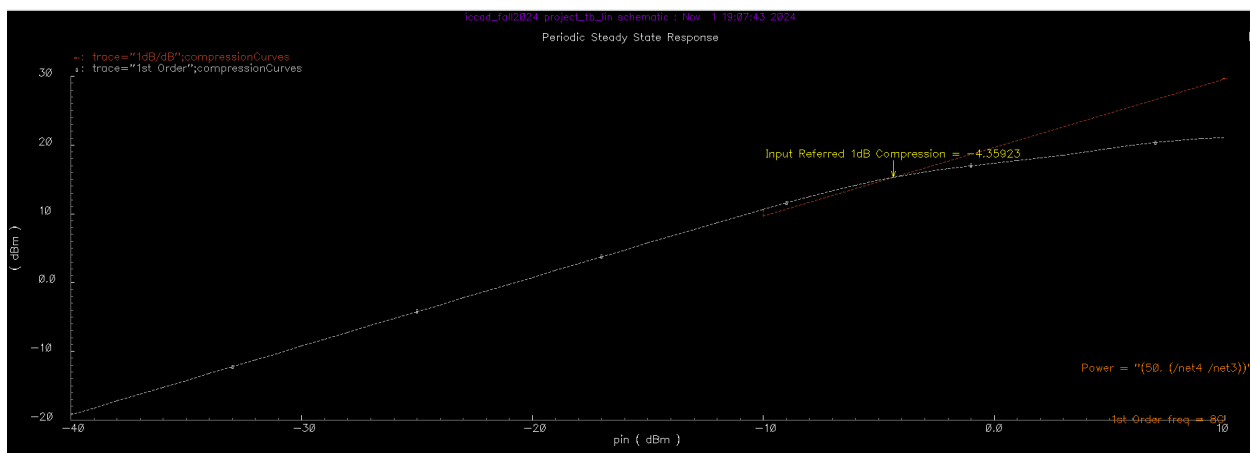


Figure 11: The 1dB compression point

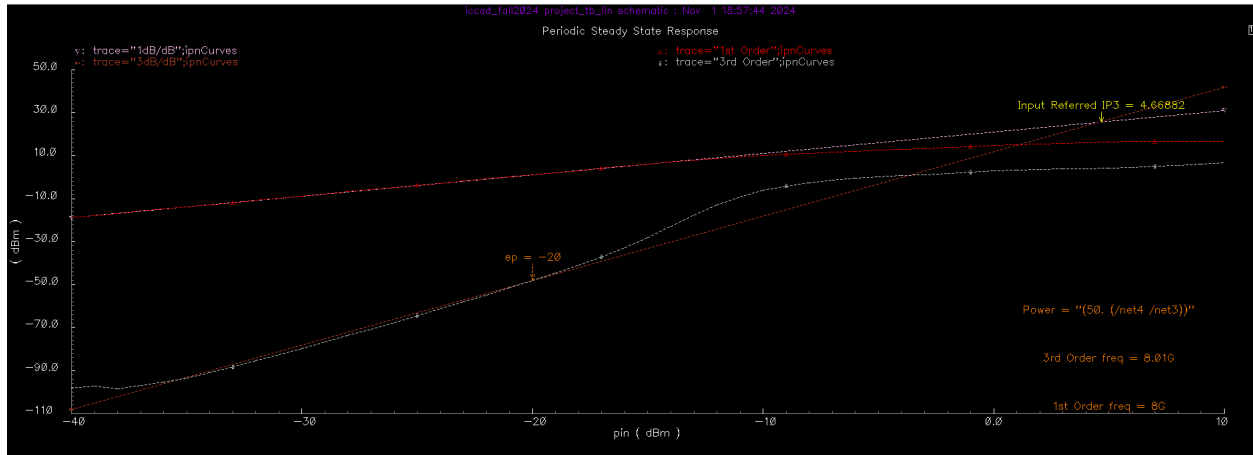


Figure 12: The IIP3 plot

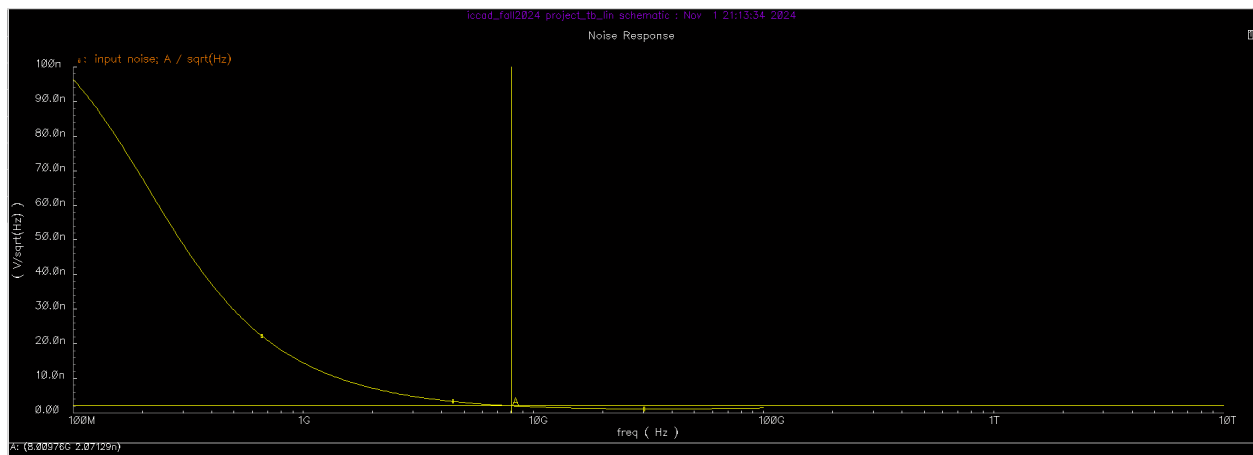


Figure 13: The input-referred noise plot

3 Simulation Results

The results for the TT corner at 27 degrees were shown in the last section. Here we will investigate every property at the TT, FF, and SS corners for temperatures of -40, 27, and 120 degrees.

3.1 TT

To change the temperature, I used **Analog Environment > Setup > Temperature...** and set the temperature to the desired value

3.1.1 -40°

The analysis results are shown in the following figures and in table 5.

Power consumption (mW)	8.380
Gain (@ 8GHz, dB)	27.543
Bandwidth (GHz)	1.463
1dB compression point (dBm)	-6.004
3rd input intercept point (dBm)	-3.563
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.964

Table 5: The circuit properties at -40°

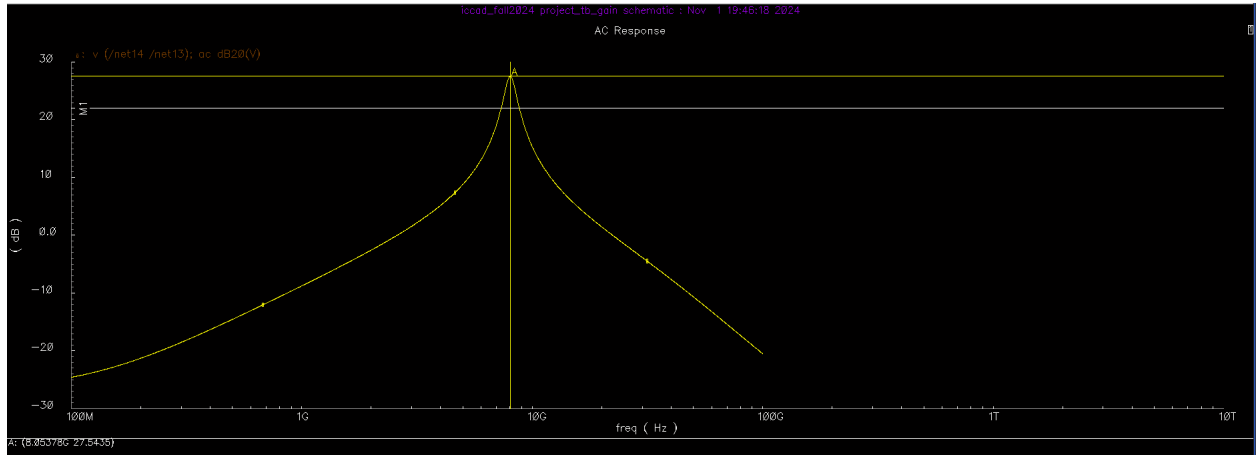


Figure 14: The gain plot

```

Curve name map:
-----
Curve1      - v (/net14 /net13); ac dB20(V)

Curve table:
-----

```

	Y value	Curve1
M1	22	7.32760367136 8.79093909836

Figure 15: The bandwidth, using a horizontal marker

signal	OP("I0.M3" "??")	signal	OP("I0.M5" "??")
betaeff	242.242m	betaeff	242.242m
cbb	14.1908f	cbb	14.1908f
cbd	44.6383a	cbd	44.6383a
cbg	-9.92328f	cbg	-9.92328f
cbs	-4.31217f	cbs	-4.31217f
cdb	-6.20222a	cdb	-6.20222a
cdd	18.3122f	cdd	18.3122f
cdg	-18.3417f	cdg	-18.3417f
cds	35.7046a	cds	35.7046a
cgb	-7.63889f	cgb	-7.63889f
cgd	-17.8395f	cgd	-17.8395f
cgg	87.1894f	cgg	87.1894f
cgs	-61.711f	cgs	-61.711f
cjd	18.5658f	cjd	18.5658f
cjs	25.1196f	cjs	25.1196f
csb	-6.54572f	csb	-6.54572f
csd	-517.278a	csd	-517.278a
csq	-58.9244f	csq	-58.9244f
css	65.9874f	css	65.9874f
gds	732.295u	gds	732.295u
gm	20.2802m	gm	20.2802m
gmbs	3.25439m	gmbs	3.25439m
gmoverid	9.52891	gmoverid	9.52891
ibulk	-199.905p	ibulk	-199.905p
id	2.12829m	id	2.12829m
ids	2.12829m	ids	2.12829m
is	-2.12829m	is	-2.12829m
pwr	1.85108m	pwr	1.85108m
region	2	region	2
reversed	0	reversed	0
ron	408.663	ron	408.663
type	0	type	0
vbs	-922.95m	vbs	-922.95m
vds	869.751m	vds	869.751m
vdsat	103.865m	vdsat	103.865m
vgs	877.05m	vgs	877.05m
vth	754.956m	vth	754.956m
signal	OP("I0.M6" "??")	signal	OP("I0.M0" "??")
betaeff	502.727m	betaeff	50.2666m
cbb	38.5993f	cbb	3.86327f
cbd	34.8265a	cbd	2.66512a
cbg	-21.7699f	cbg	-2.17479f
cbs	-16.8643f	cbs	-1.69114f
cdb	-18.6126a	cdb	-3.60428a
cdd	36.6217f	cdd	3.66456f
cdg	-36.6725f	cdg	-3.67337f
cds	69.372a	cds	12.4195a
cgb	-15.844f	cgb	-1.56734f
cgd	-36.41f	cgd	-3.64178f
cgg	175.77f	cgg	17.5747f
cgs	-123.516f	cgs	-12.3656f
cjd	43.6466f	cjd	4.63356f
cjs	63.3565f	cjs	10.0055f
csb	-22.7367f	csb	-2.29232f
csd	-246.562a	csd	-25.4395a
csq	-117.328f	csq	-11.7265f
css	140.311f	css	14.0443f
gds	1.03179m	gds	111.438u
gm	40.1873m	gm	3.87347m
gmbs	11.1817m	gmbs	1.08818m
gmoverid	9.44125	gmoverid	9.68368
ibulk	-1.26856n	ibulk	-269.671f
id	4.25657m	id	400u
ids	4.25657m	ids	400u
is	-4.25657m	is	-400u
pwr	3.9286m	pwr	272.853u
region	2	region	2
reversed	0	reversed	0
ron	216.83	ron	1.70533K
type	0	type	0
vbs	0	vbs	0
vds	922.95m	vds	682.132m
vdsat	97.8347m	vdsat	97.5602m
vgs	682.132m	vgs	682.132m
vth	559.542m	vth	560.263m

Figure 16: The DC operating points of all transistors

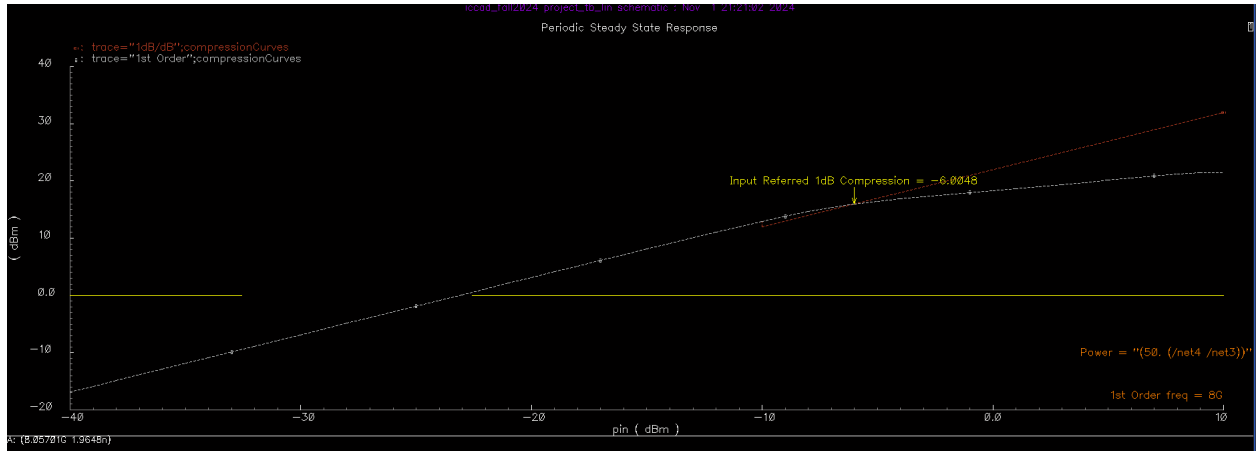


Figure 17: The 1dB compression point

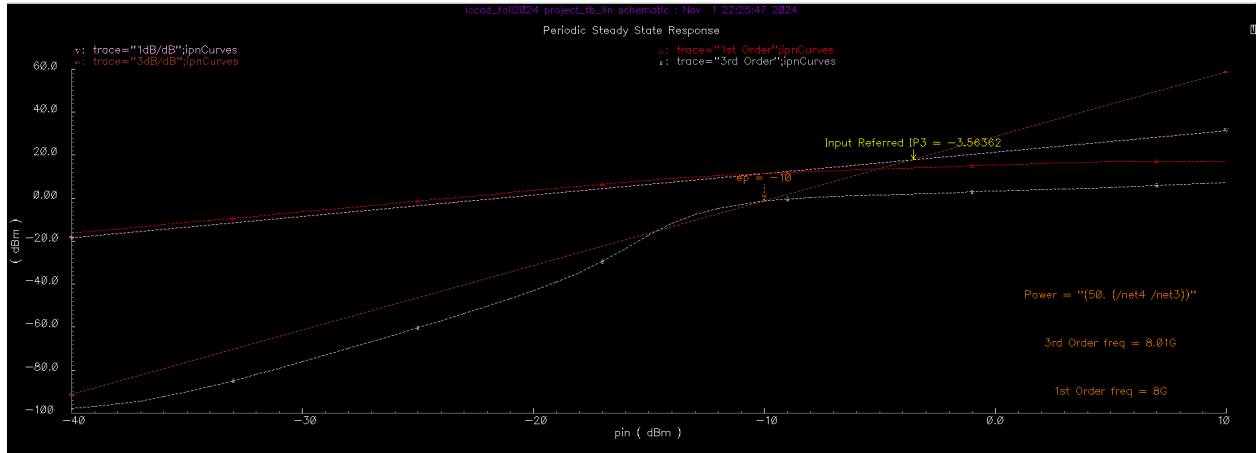


Figure 18: The IIP3 plot

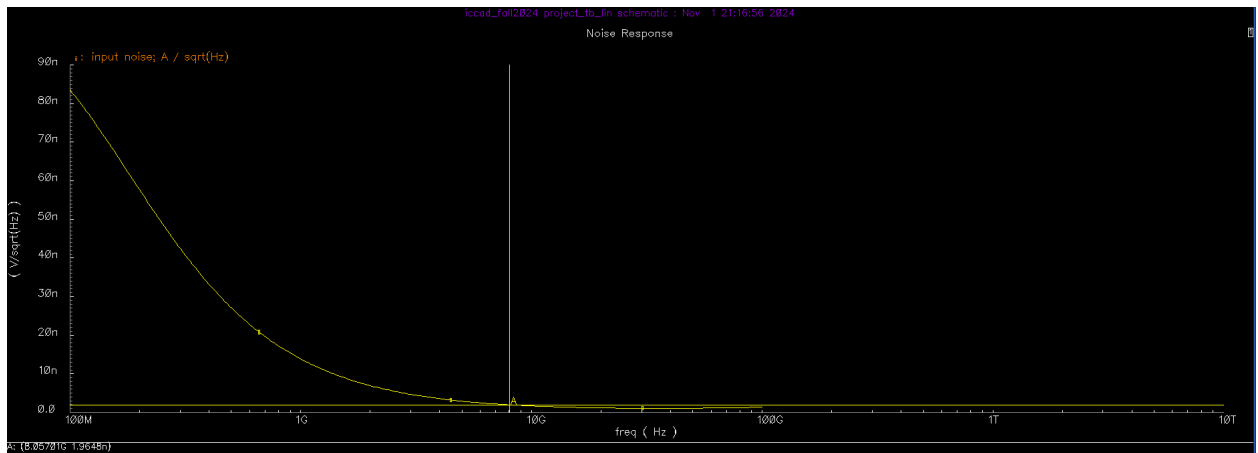


Figure 19: The input-referred noise

3.1.2 120°

The analysis results are shown in the following figures and in table 6.

Power consumption (mW)	8.377
Gain (@ 8GHz, dB)	23.792
Bandwidth (GHz)	0.794
1dB compression point (dBm)	-2.411
3rd input intercept point (dBm)	1.201
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.232

Table 6: The circuit properties at 120°

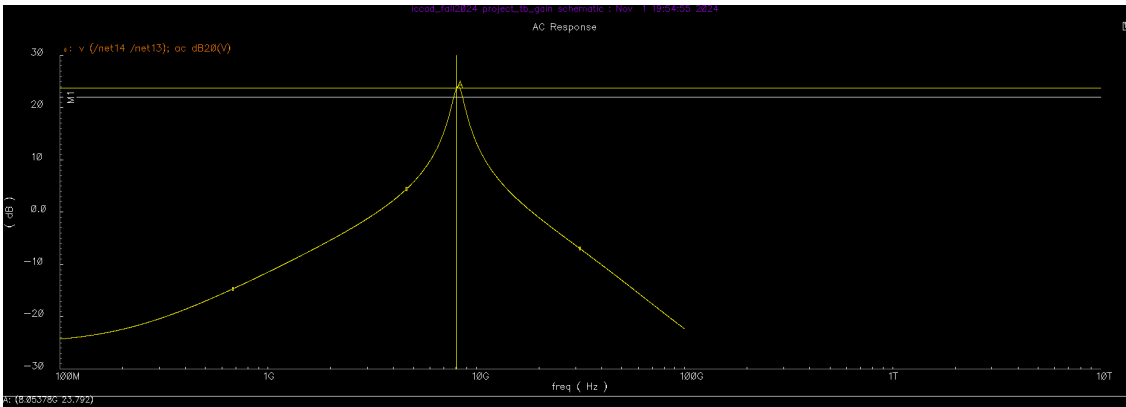


Figure 20: The gain plot

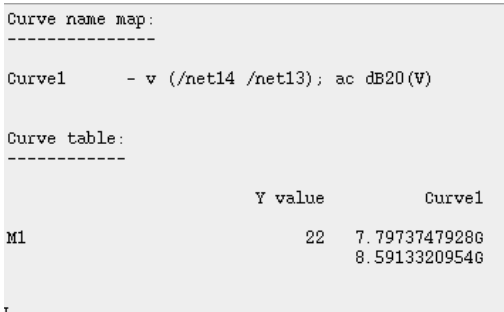


Figure 21: The bandwidth, using a horizontal marker

signal	OP("I0. M3" "??")	signal	OP("I0. M5" "??")
betaeff	91.5292m	betaeff	91.5292m
cbb	14.146f	cbb	14.146f
cbd	42.3969a	cbd	42.3969a
cbg	-9.85569f	cbg	-9.85569f
cbs	-4.33269f	cbs	-4.33269f
cdb	-8.0975a	cdb	-8.0975a
odd	18.3215f	odd	18.3215f
cdg	-18.3501f	cdg	-18.3501f
cds	36.6926a	cds	36.6926a
cgb	-7.12538f	cgb	-7.12538f
cgd	-17.8442f	cgd	-17.8442f
cgg	86.8266f	cgg	86.8266f
cgs	-61.857f	cgs	-61.857f
cjd	18.9894f	cjd	18.9894f
cjs	26.2519f	cjs	26.2519f
csb	-7.01251f	csb	-7.01251f
csd	-519.709a	csd	-519.709a
csg	-58.6208f	csg	-58.6208f
css	66.153f	css	66.153f
gds	607.104u	gds	607.104u
gm	14.4773m	gm	14.4773m
gmbs	2.48576m	gmbs	2.48576m
gmoverid	6.80461	gmoverid	6.80461
ibulk	-58.0854p	ibulk	-58.0854p
id	2.12758m	id	2.12758m
ids	2.12758m	ids	2.12758m
is	-2.12758m	is	-2.12758m
pwr	1.82051m	pwr	1.82051m
region	2	region	2
reversed	0	reversed	0
ron	402.181	ron	402.181
type	0	type	0
vbs	-932.155m	vbs	-932.155m
vds	855.671m	vds	855.671m
vdsat	184.233m	vdsat	184.233m
vgs	867.845m	vgs	867.845m
vth	665.475m	vth	665.475m
signal	OP("I0. M6" "??")	signal	OP("I0. M0" "??")
betaeff	192.116m	betaeff	19.2085m
cbb	38.6256f	cbb	3.86662f
cbd	30.9686a	cbd	1.13849a
cbg	-21.6434f	cbg	-2.16114f
cbs	-17.0132f	cbs	-1.70662f
cdb	-21.7294a	cdb	-4.88385a
odd	36.6344f	odd	3.66917f
cdg	-36.6808f	cdg	-3.67732f
cds	68.0822a	cds	13.0342a
cgb	-14.9857f	cgb	-1.4788f
cgd	-36.4168f	cgd	-3.64354f
cgg	175.092f	cgg	17.5093f
cgs	-123.689f	cgs	-12.387f
cjd	45.4452f	cjd	4.92393f
cjs	72.1042f	cjs	11.4005f
csb	-23.6182f	csb	-2.38294f
csd	-248.636a	csd	-26.7638a
csg	-116.767f	csg	-11.6708f
css	140.634f	css	14.0805f
gds	858.923u	gds	105.018u
gm	29.2912m	gm	2.79422m
gmbs	8.38898m	gmbs	809.068u
gmoverid	6.8837	gmoverid	6.98554
ibulk	-395.319p	ibulk	-3.33425p
id	4.25515m	id	400u
ids	4.25515m	ids	400u
is	-4.25515m	is	-400u
pwr	3.96646m	pwr	264.104u
region	2	region	2
reversed	0	reversed	0
ron	219.065	ron	1.65065K
type	0	type	0
vbs	0	vbs	0
vds	932.155m	vds	660.259m
vdsat	171.831m	vdsat	171.446m
vgs	660.259m	vgs	660.259m
vth	455.677m	vth	456.491m

Figure 22: The DC operating points of all transistors

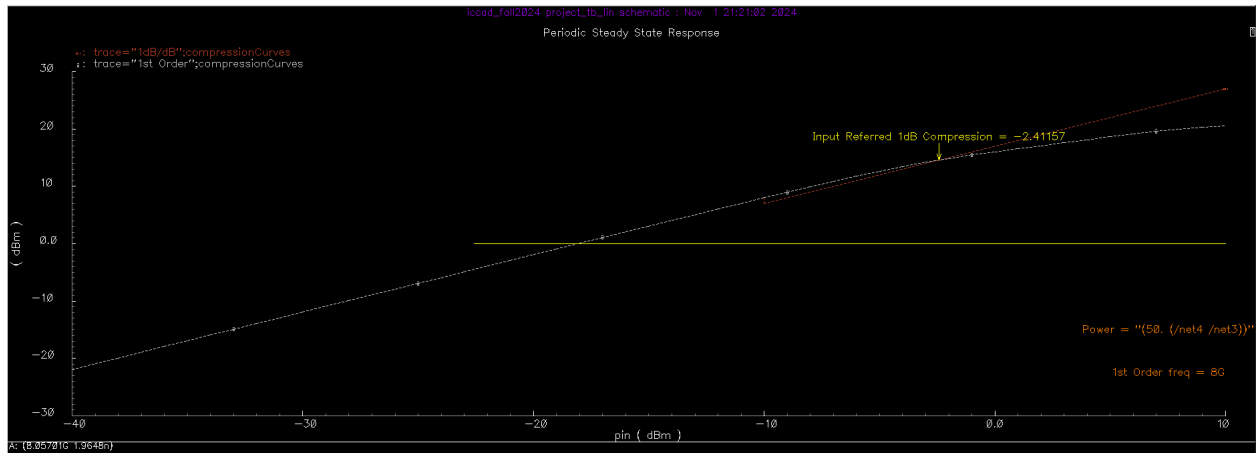


Figure 23: The 1dB compression point

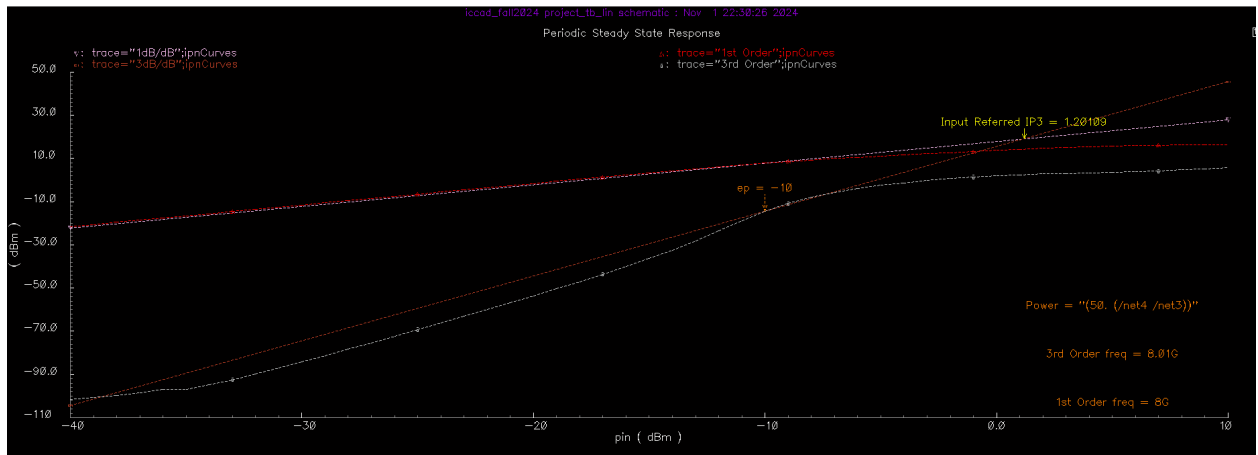


Figure 24: The IIP3 plot

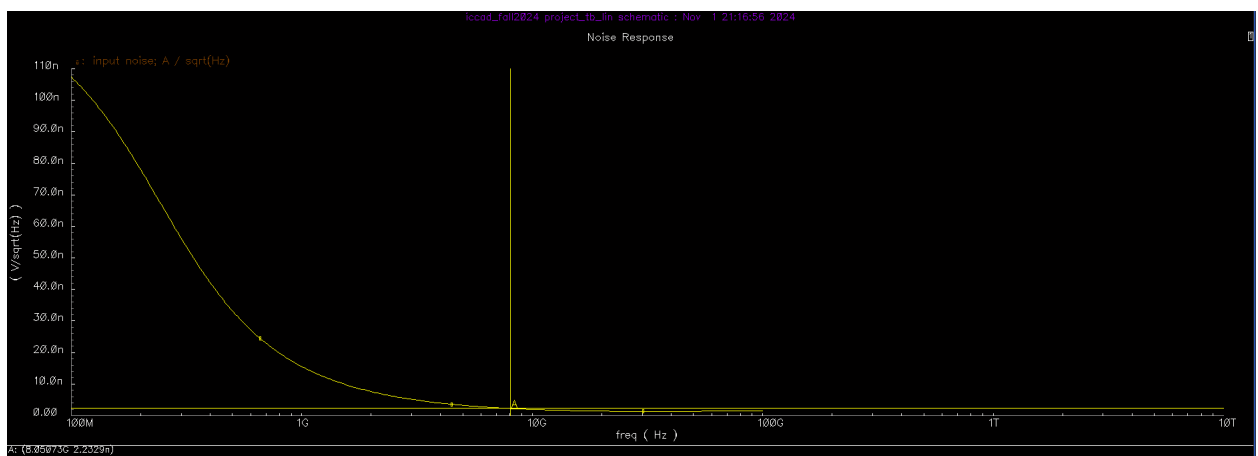


Figure 25: The input-referred noise

3.2 FF

To change the corner, I use **Analog Environment > Setup > Model Libraries...** and changed the corner of the transistor model, the MIM capacitor model, and the M model to FF.

3.2.1 -40°

The analysis results are shown in the following figures and in table 7.

Power consumption (mW)	9.003
Gain (@ 8GHz, dB)	26.227
Bandwidth (GHz)	1.651
1dB compression point (dBm)	-1.160
3rd input intercept point (dBm)	-1.343
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.906

Table 7: The circuit properties at -40°

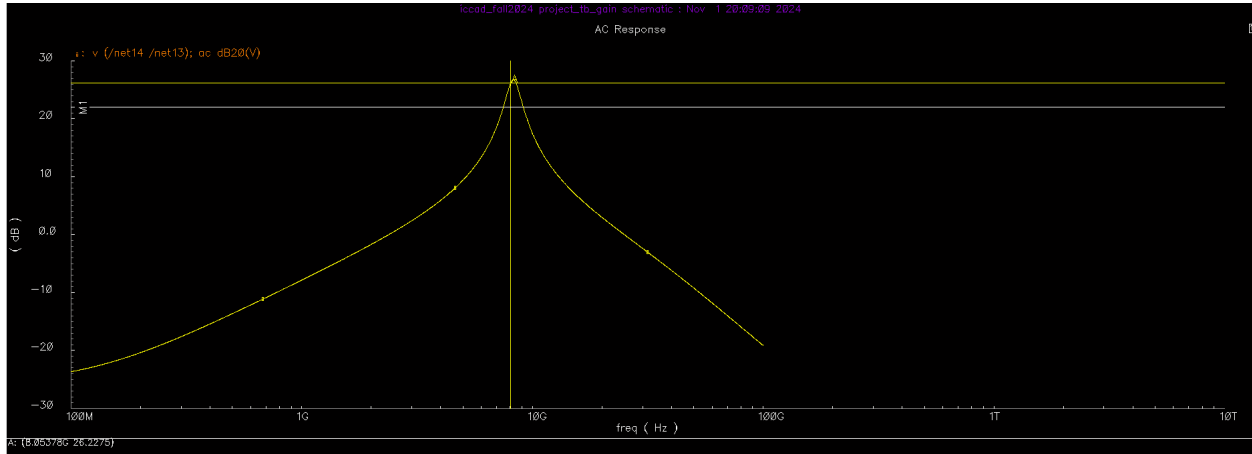


Figure 26: The gain plot

```

Curve name map:
-----
Curve1      - v (/net14 /net13); ac dB20(V)

Curve table:
-----

```

	Y value	Curve1
M1	22	7.47276040116 9.12362341286

Figure 27: The bandwidth, using a horizontal marker

signal	OP("I0. M3" "??")	signal	OP("I0. M5" "??")
betaeff	296.209m	betaeff	296.209m
cbb	13.5948f	cbb	13.5948f
cbd	74.2728a	cbd	74.2728a
cbg	-9.73662f	cbg	-9.73662f
cbs	-3.93243f	cbs	-3.93243f
cdb	-5.46399a	cdb	-5.46399a
cdd	19.2678f	cdd	19.2678f
cdg	-19.3033f	cdg	-19.3033f
cde	41.0278a	cde	41.0278a
cgb	-8.94875f	cgb	-8.94875f
cgd	-18.4637f	cgd	-18.4637f
cgg	87.8978f	cgg	87.8978f
cgs	-60.4853f	cgs	-60.4853f
cjd	17.6426f	cjd	17.6426f
cjs	23.4238f	cjs	23.4238f
csb	-4.64056f	csb	-4.64056f
csd	-878.33a	csd	-878.33a
csg	-58.8578f	csg	-58.8578f
css	64.3767f	css	64.3767f
gds	1.06472m	gds	1.06472m
gm	22.5571m	gm	22.5571m
gmbs	2.58625m	gmbs	2.58625m
gmoverid	9.79913	gmoverid	9.79913
ibulk	-31.8792p	ibulk	-31.8792p
id	2.30194m	id	2.30194m
ids	2.30194m	ids	2.30194m
is	-2.30194m	is	-2.30194m
pwr	1.79943m	pwr	1.79943m
region	2	region	2
reversed	0	reversed	0
ron	339.582	ron	339.582
type	0	type	0
vbs	-1.01041	vbs	-1.01041
vds	781.699m	vds	781.699m
vdsat	97.5725m	vdsat	97.5725m
vgs	789.593m	vgs	789.593m
vth	672.873m	vth	672.873m
signal	OP("I0. M6" "??")	signal	OP("I0. M0" "??")
betaeff	599.564m	betaeff	59.9246m
cbb	37.3854f	cbb	3.7413f
cbd	138.422a	cbd	12.5666a
cbg	-21.4588f	cbg	-2.142f
cbs	-16.065f	cbs	-1.61187f
cdb	-12.7015a	cdb	-3.7166a
cdd	38.5232f	cdd	3.85589f
cdg	-38.5654f	cdg	-3.86641f
cde	54.9211a	cde	14.2358a
cgb	-18.2607f	cgb	-1.79898f
cgd	-37.8065f	cgd	-3.78204f
cgg	177.183f	cgg	17.6933f
cgs	-121.116f	cgs	-12.1123f
cjd	40.669f	cjd	4.48669f
cjs	60.212f	cjs	9.50755f
csb	-19.112f	csb	-1.93861f
csd	-855.108a	csd	-86.4134a
csg	-117.159f	csg	-11.6849f
css	137.126f	css	13.7099f
gds	1.49664m	gds	158.543u
gm	44.9198m	gm	4.16144m
gmbs	10.6772m	gmbs	1.00925m
gmoverid	9.75693	gmoverid	10.4036
ibulk	-6.37072n	ibulk	-24.6915f
id	4.60389m	id	400u
ids	4.60388m	ids	400u
is	-4.60388m	is	-400u
pwr	4.6518m	pwr	245.523u
region	2	region	2
reversed	0	reversed	0
ron	219.468	ron	1.53452K
type	0	type	0
vbs	0	vbs	0
vds	1.01041	vds	613.807m
vdsat	91.4926m	vdsat	89.8858m
vgs	613.807m	vgs	613.807m
vth	498.857m	vth	503.162m

Figure 28: The DC operating points of all transistors

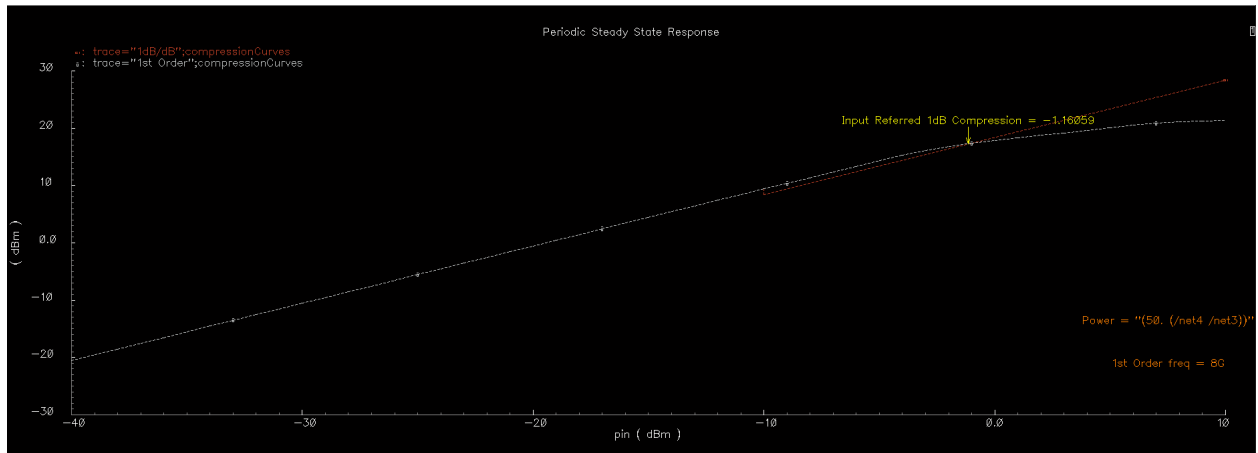


Figure 29: The 1dB compression point

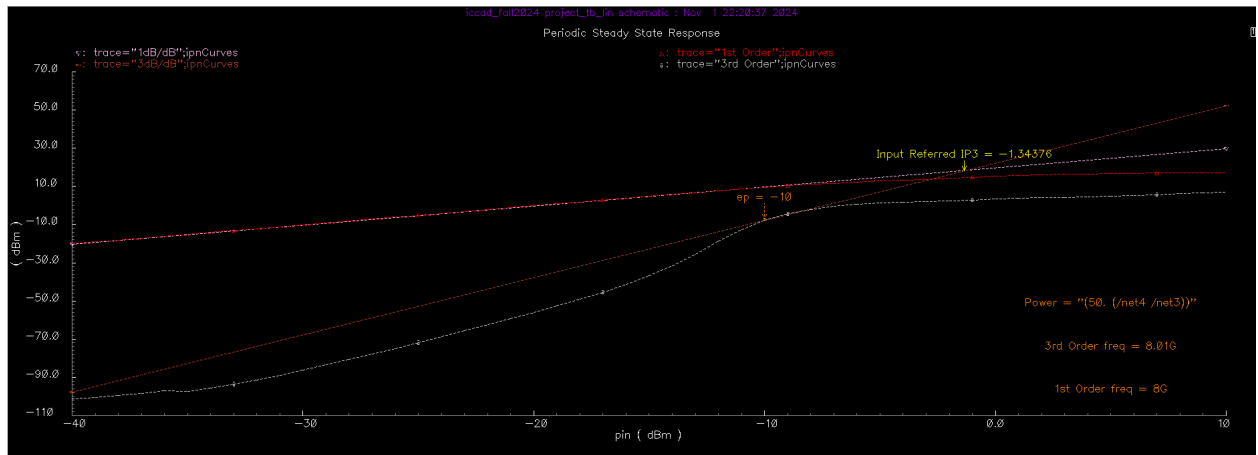


Figure 30: The IIP3 plot

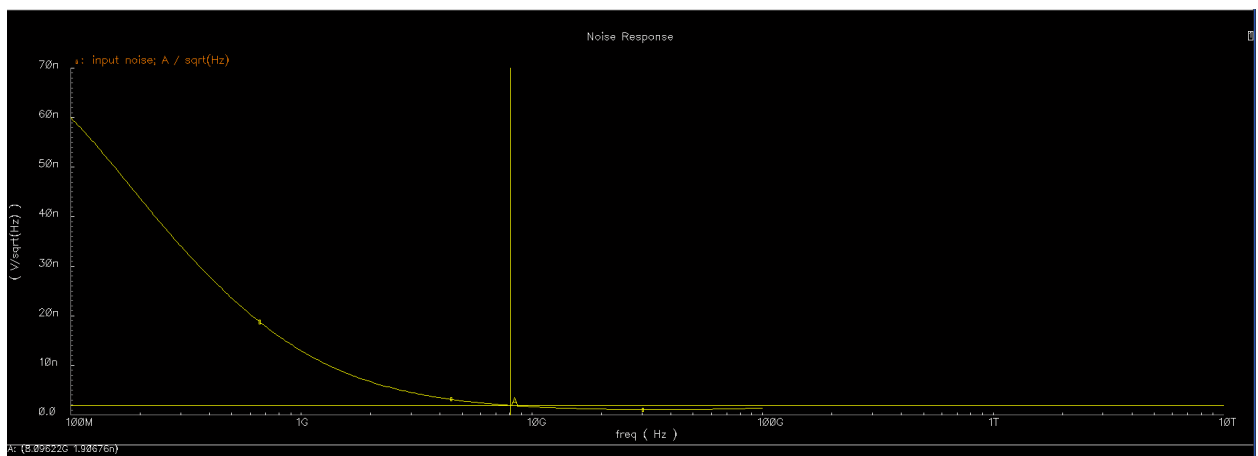


Figure 31: The input-referred noise

3.2.2 27°

The analysis results are shown in the following figures and in table 8.

Power consumption (mW)	11.152
Gain (@ 8GHz, dB)	24.509
Bandwidth (GHz)	1.308
1dB compression point (dBm)	-0.485
3rd input intercept point (dBm)	1.647
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.017

Table 8: The circuit properties at 27°

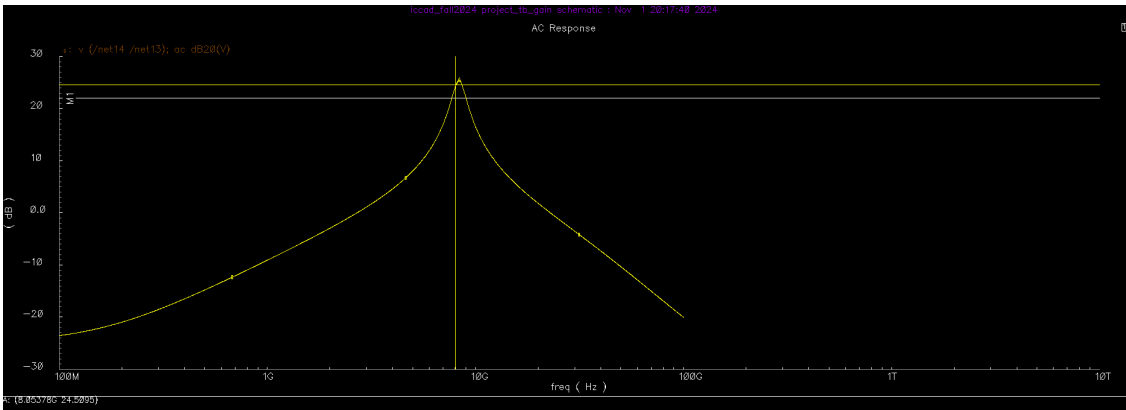


Figure 32: The gain plot

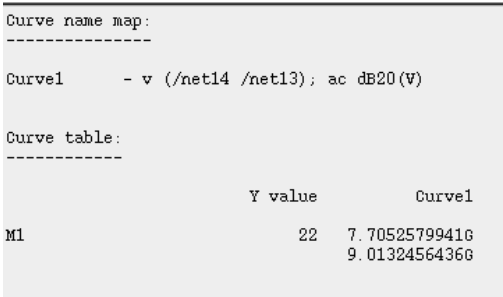


Figure 33: The bandwidth, using a horizontal marker

signal	OP("I0_M3" "??")	signal	OP("I0_M5" "??")
betaeff	186.563m	betaeff	186.563m
cbb	13.5678f	cbb	13.5678f
cbd	73.4129a	cbd	73.4129a
cbg	-9.69971f	cbg	-9.69971f
cbs	-3.94148f	cbs	-3.94148f
cdb	-6.20723a	cdb	-6.20723a
cdd	19.2721f	cdd	19.2721f
cdg	-19.3076f	cdg	-19.3076f
cds	41.7524a	cds	41.7524a
cgb	-8.745f	cgb	-8.745f
cgd	-18.4676f	cgd	-18.4676f
cgg	87.646f	cgg	87.646f
cgs	-60.4335f	cgs	-60.4335f
cjd	17.9556f	cjd	17.9556f
cjs	23.9886f	cjs	23.9886f
csb	-4.81657f	csb	-4.81657f
csd	-877.94a	csd	-877.94a
csg	-58.6387f	csg	-58.6387f
css	64.3332f	css	64.3332f
gds	961.23u	gds	961.23u
gm	19.3505m	gm	19.3505m
gmbs	2.30229m	gmbs	2.30229m
gmoverid	8.4507	gmoverid	8.4507
ibulk	-10.4167p	ibulk	-10.4167p
id	2.28981m	id	2.28981m
ids	2.28981m	ids	2.28981m
is	-2.28981m	is	-2.28981m
pwr	1.76689m	pwr	1.76689m
region	2	region	2
reversed	0	reversed	0
ron	336.984	ron	336.984
type	0	type	0
vbs	-1.01832	vbs	-1.01832
vds	771.631m	vds	771.631m
vdsat	128.22m	vdsat	128.22m
vgs	781.681m	vgs	781.681m
vth	636.645m	vth	636.645m
signal	OP("I0_M6" "??")	signal	OP("I0_M0" "??")
betaeff	379.944m	betaeff	37.9693m
cbb	37.4108f	cbb	3.74405f
cbd	138.226a	cbd	11.9352a
cbg	-21.4053f	cbg	-2.13637f
cbs	-16.1437f	cbs	-1.61962f
cdb	-13.2658a	cdb	-4.28416a
cdd	38.526f	cdd	3.85793f
cdg	-38.5667f	cdg	-3.8684f
cds	53.945a	cds	14.7553a
cgb	-17.9658f	cgb	-1.76685f
cgd	-37.8134f	cgd	-3.78326f
cgg	176.708f	cgg	17.6502f
cgs	-120.929f	cgs	-12.1001f
cjd	41.5902f	cjd	4.64854f
cjs	63.6931f	cjs	10.0626f
csb	-19.4318f	csb	-1.97292f
csd	-850.809a	csd	-86.6068a
csg	-116.736f	csg	-11.6455f
css	137.018f	css	13.705f
gds	1.32492m	gds	149.662u
gm	38.8077m	gm	3.57699m
gmbs	9.37579m	gmbs	882.509u
gmoverid	8.47401	gmoverid	8.94249
ibulk	-4.68637n	ibulk	-3.43465f
id	4.57963m	id	400u
ids	4.57962m	ids	400u
is	-4.57962m	is	-400u
pwr	4.66352m	pwr	239.671u
region	2	region	2
reversed	0	reversed	0
ron	222.359	ron	1.49794K
type	0	type	0
vbs	0	vbs	0
vds	1.01832	vds	599.178m
vdsat	119.712m	vdsat	117.835m
vgs	599.178m	vgs	599.178m
vth	455.632m	vth	460.181m

Figure 34: The DC operating points of all transistors

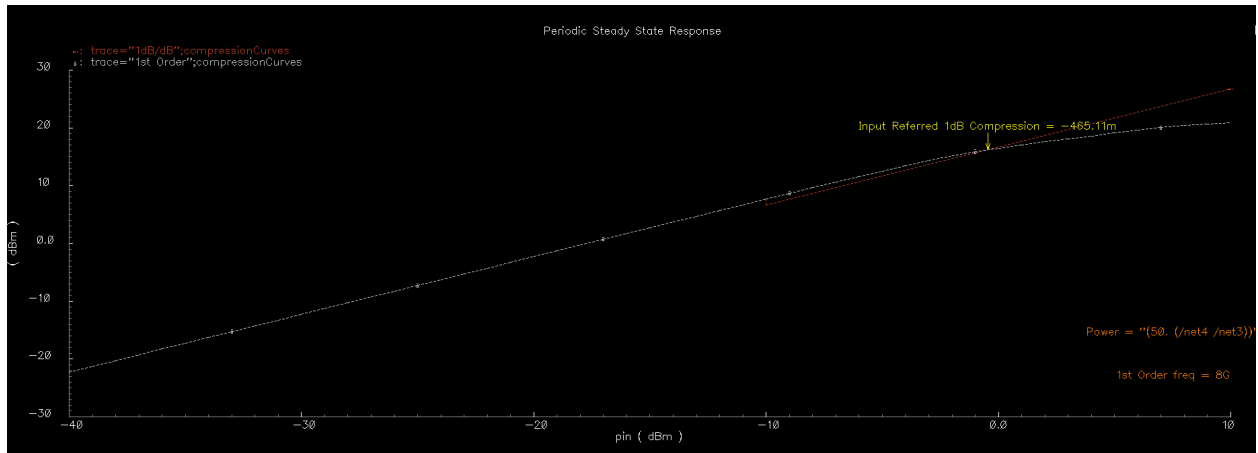


Figure 35: The 1dB compression point

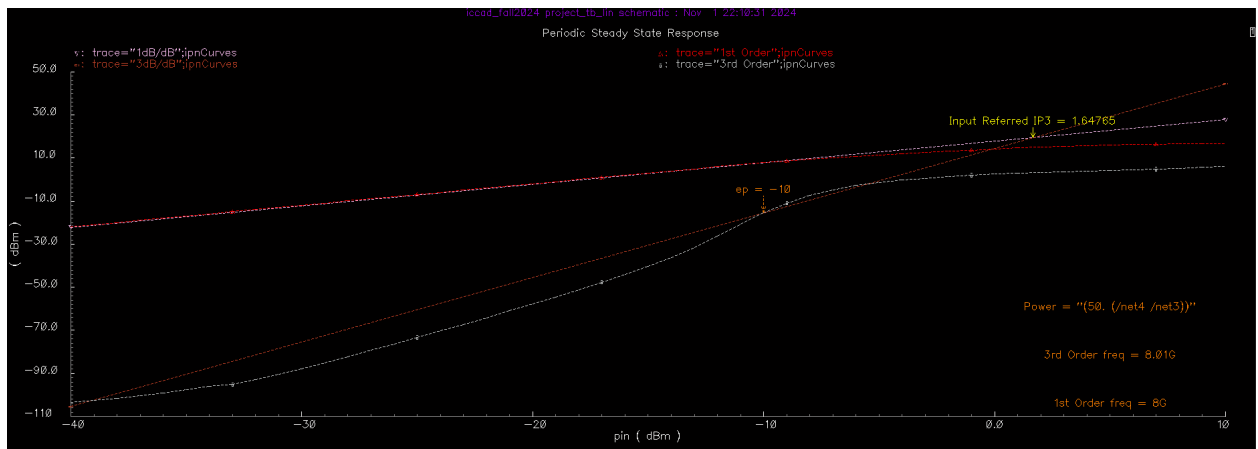


Figure 36: The IIP3 plot

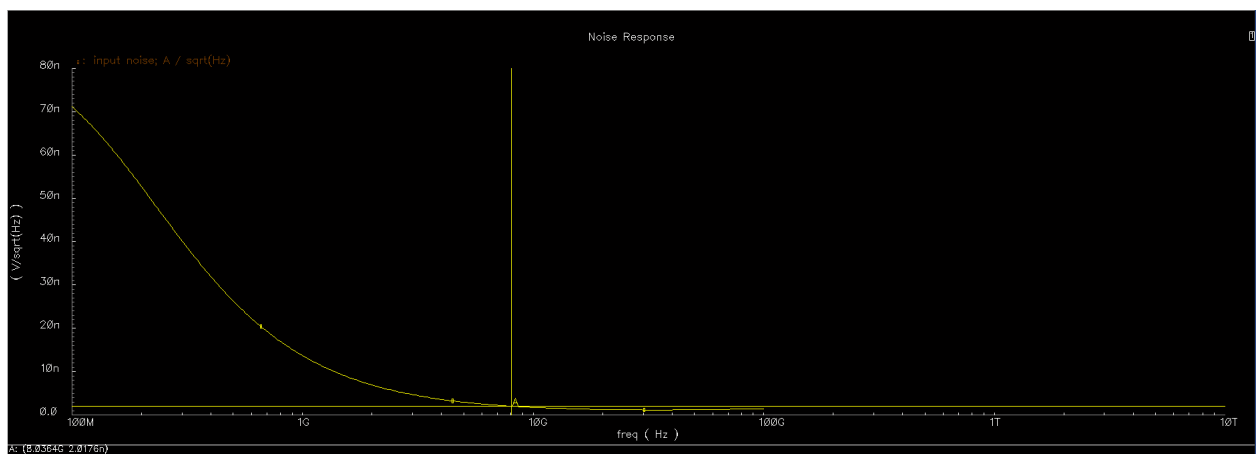


Figure 37: The input-referred noise

3.2.3 120°

The analysis results are shown in the following figures and in table 9.

Power consumption (mW)	10.839
Gain (@ 8GHz, dB)	22.223
Bandwidth (GHz)	0.877
1dB compression point (dBm)	0.425
3rd input intercept point (dBm)	4.604
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.196

Table 9: The circuit properties at 120°

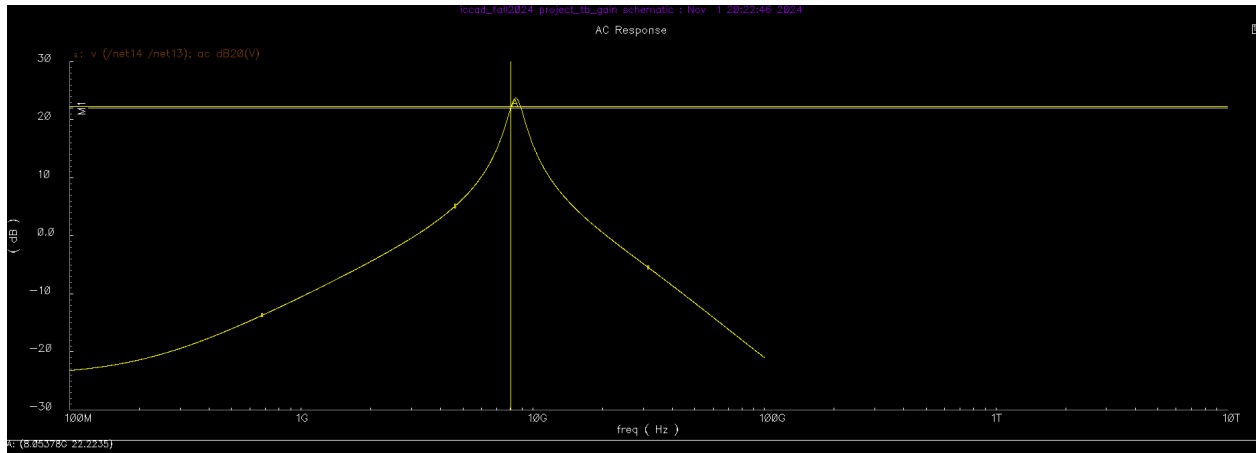


Figure 38: The gain plot

Curve name map:		

Curve1	-	v (/net14 /net13); ac dB20(V)
Curve table:		

	Y value	Curve1
M1	22	8.0206948889G 8.8978673949G

Figure 39: The bandwidth, using a horizontal marker

signal	OP("I0. M3" "??")	signal	OP("I0. M5" "??")
betaeff	108.934m	betaeff	108.934m
cbb	13.54f	cbb	13.54f
cbd	71.2006a	cbd	71.2006a
cbg	-9.66222f	cbg	-9.66222f
cbs	-3.94903f	cbs	-3.94903f
cdb	-7.54762a	cdb	-7.54762a
cdd	19.2805f	cdd	19.2805f
cdg	-19.3152f	cdg	-19.3152f
cds	42.1959a	cds	42.1959a
cgb	-8.41826f	cgb	-8.41826f
cgd	-18.4721f	cgd	-18.4721f
cgg	87.4851f	cgg	87.4851f
cgs	-60.5947f	cgs	-60.5947f
cjd	18.0316f	cjd	18.0316f
cjs	24.3894f	cjs	24.3894f
csb	-5.11424f	csb	-5.11424f
csd	-879.621a	csd	-879.621a
csg	-58.5077f	csg	-58.5077f
cse	64.5015f	cse	64.5015f
gds	877.515u	gds	877.515u
gm	16.1621m	gm	16.1621m
gmbs	2.0343m	gmbs	2.0343m
gmoverid	7.08495	gmoverid	7.08495
ibulk	-73.4332p	ibulk	-73.4332p
id	2.28119m	id	2.28119m
ids	2.28119m	ids	2.28119m
is	-2.28119m	is	-2.28119m
pwr	1.73975m	pwr	1.73975m
region	2	region	2
reversed	0	reversed	0
ron	334.321	ron	334.321
type	0	type	0
vbs	-1.02407	vbs	-1.02407
vds	762.65m	vds	762.65m
vdsat	175.568m	vdsat	175.568m
vgs	775.931m	vgs	775.931m
vth	581.81m	vth	581.81m
signal	OP("I0. M6" "??")	signal	OP("I0. M0" "??")
betaeff	224.421m	betaeff	22.4236m
cbb	37.4258f	cbb	3.74598f
cbd	137.389a	cbd	10.5741a
cbg	-21.3313f	cbg	-2.12786f
cbs	-16.2319f	cbs	-1.62869f
cdb	-14.5331a	cdb	-5.31382a
cdd	38.5314f	cdd	3.86217f
cdg	-38.5702f	cdg	-3.87201f
cds	53.3346a	cds	15.1577a
cgb	-17.4056f	cgb	-1.70758f
cgd	-37.8197f	cgd	-3.785f
cgg	176.418f	cgg	17.6284f
cgs	-121.193f	cgs	-12.1358f
cjd	42.1718f	cjd	4.80636f
cjs	68.8885f	cjs	10.891f
csb	-20.0057f	csb	-2.03308f
csd	-849.179a	csd	-87.7428a
csg	-116.516f	csg	-11.6285f
cse	137.371f	cse	13.7494f
gds	1.17683m	gds	146.29u
gm	32.8205m	gm	2.99926m
gmbs	8.11994m	gmbs	758.335u
gmoverid	7.19374	gmoverid	7.49815
ibulk	-2.65439n	ibulk	-6.56707p
id	4.56238m	id	400u
ids	4.56237m	ids	400u
is	-4.56237m	is	-400u
pwr	4.67219m	pwr	233.704u
region	2	region	2
reversed	0	reversed	0
ron	224.46	ron	1.46065K
type	0	type	0
vbs	0	vbs	0
vds	1.02407	vds	584.259m
vdsat	162.72m	vdsat	160.488m
vgs	584.259m	vgs	584.259m
vth	391.182m	vth	395.957m

Figure 40: The DC operating points of all transistors

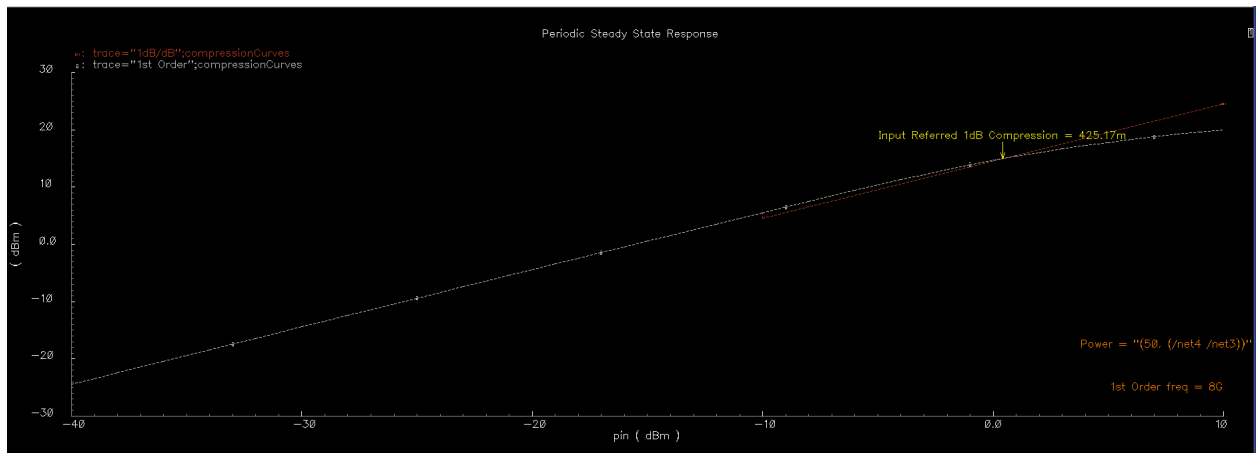


Figure 41: The 1dB compression point

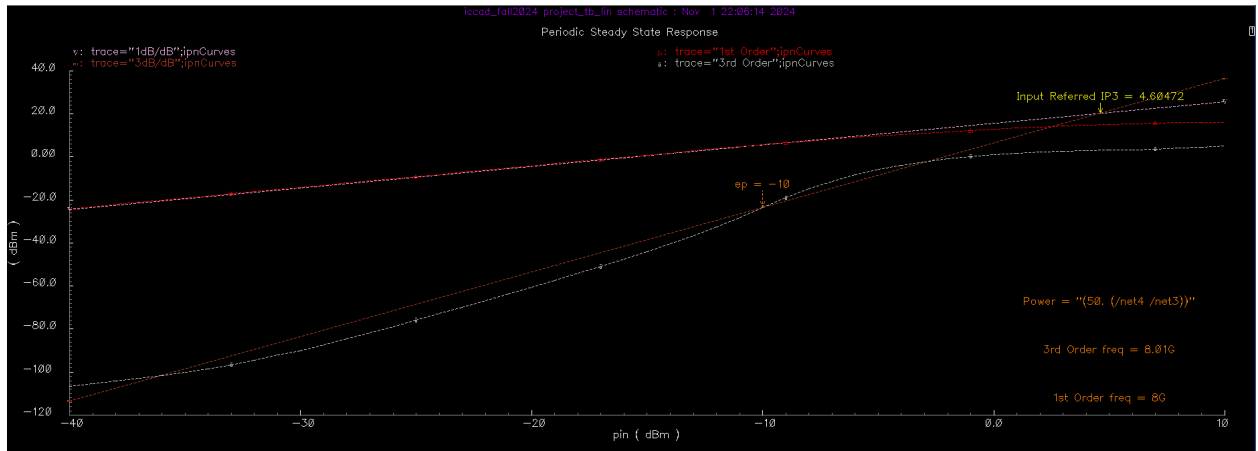


Figure 42: The IIP3 plot

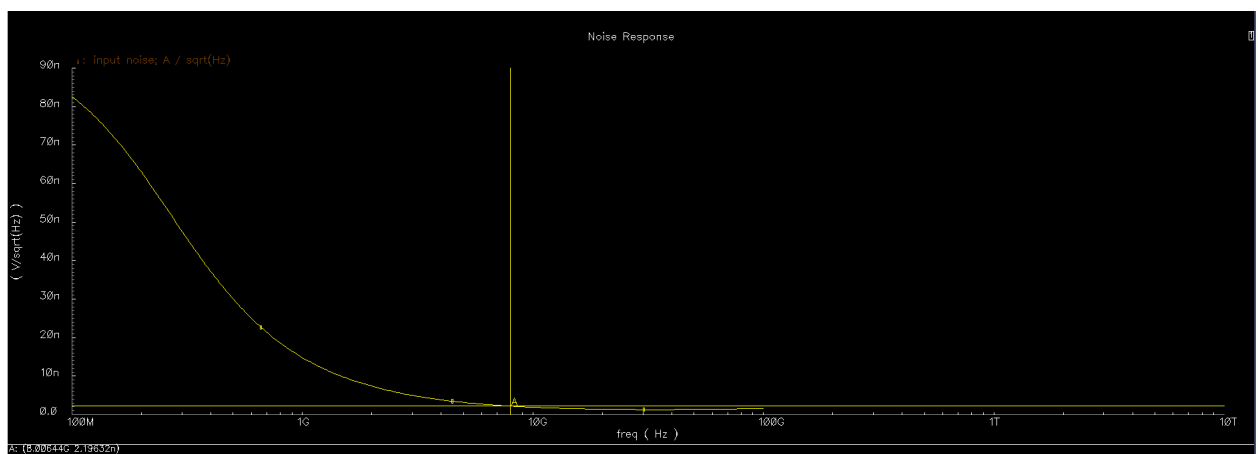


Figure 43: The input-referred noise

3.3 SS

3.3.1 -40°

The analysis results are shown in the following figures and in table 10.

Power comsumption (mW)	8.380
Gain (@ 8GHz, dB)	26.467
Bandwidth (GHz)	1.253
1dB compression point (dBm)	-7.744
3rd input intercept point (dBm)	-1.449
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.992

Table 10: The circuit properties at -40°

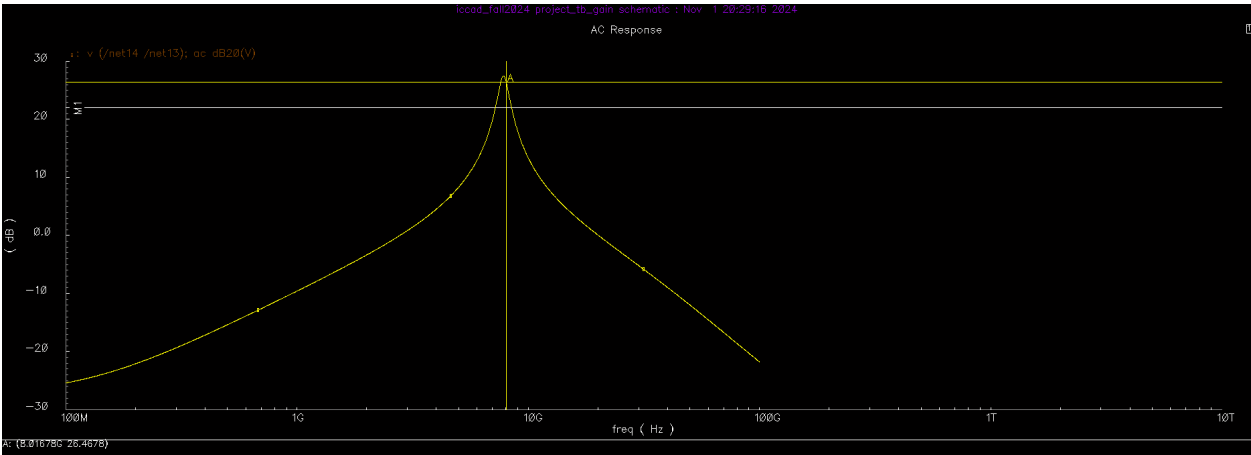


Figure 44: The gain plot

Curve name map:		

Curve1	- v (/net14 /net13); ac dB20(V)	
Curve table:		

	Y value	Curve1
M1	22	7.2057388196 8.4639582042G

Figure 45: The bandwidth, using a horizontal marker

signal	OP("I0_M3" "??")	signal	OP("I0_M5" "??")
betaeff	197.696m	betaeff	197.696m
cbb	14.8156f	cbb	14.8156f
cbd	42.1271a	cbd	42.1271a
cbg	-10.1132f	cbg	-10.1132f
cbs	-4.74453f	cbs	-4.74453f
cdb	-6.82882a	cdb	-6.82882a
cdd	17.3613f	cdd	17.3613f
cdg	-17.3869f	cdg	-17.3869f
cds	32.4952a	cds	32.4952a
cgb	-6.31007f	cgb	-6.31007f
cgd	-16.9346f	cgd	-16.9346f
cgg	86.5989f	cgg	86.5989f
cgs	-63.3542f	cgs	-63.3542f
cjd	19.4895f	cjd	19.4895f
cjs	26.8418f	cjs	26.8418f
csb	-8.49866f	csb	-8.49866f
csd	-468.818a	csd	-468.818a
csg	-59.0988f	csg	-59.0988f
css	68.0663f	css	68.0663f
gds	621.276u	gds	621.276u
gm	18.4592m	gm	18.4592m
gmbs	3.78907m	gmbs	3.78907m
gmoverid	9.0127	gmoverid	9.0127
ibulk	-743.113p	ibulk	-743.113p
id	2.04813m	id	2.04813m
ids	2.04813m	ids	2.04813m
is	-2.04813m	is	-2.04813m
pwr	1.94287m	pwr	1.94287m
region	2	region	2
reversed	0	reversed	0
ron	463.158	ron	463.158
type	0	type	0
vbs	-844.37m	vbs	-844.37m
vds	948.606m	vds	948.606m
vdsat	112.706m	vdsat	112.706m
vgs	955.63m	vgs	955.63m
vth	822.617m	vth	822.617m
signal	OP("I0_M6" "??")	signal	OP("I0_M0" "??")
betaeff	419.572m	betaeff	41.9548m
cbb	39.898f	cbb	3.99118f
cbd	31.1189a	cbd	2.73266a
cbg	-22.0729f	cbg	-2.20632f
cbs	-17.8563f	cbs	-1.78759f
cdb	-26.9635a	cdb	-3.55153a
cdd	34.7303f	cdd	3.47415f
cdg	-34.7916f	cdg	-3.48183f
cds	88.2271a	cds	11.2322a
cgb	-13.4111f	cgb	-1.33424f
cgd	-34.5187f	cgd	-3.45224f
cgg	174.573f	cgg	17.4567f
cgs	-126.643f	cgs	-12.6703f
cjd	46.6724f	cjd	4.78364f
cjs	66.4984f	cjs	10.5031f
csb	-26.4599f	csb	-2.65339f
csd	-242.799a	csd	-24.6488a
csg	-117.709f	csg	-11.7686f
css	144.411f	css	14.4466f
gds	956.886u	gds	99.2742u
gm	36.5177m	gm	3.60001m
gmbs	11.5939m	gmbs	1.14673m
gmoverid	8.9149	gmoverid	9.00004
ibulk	-209.643p	ibulk	-1.58585p
id	4.09626m	id	400u
ids	4.09626m	ids	400u
is	-4.09626m	is	-400u
pwr	3.45876m	pwr	298.167u
region	2	region	2
reversed	0	reversed	0
ron	206.132	ron	1.86354K
type	0	type	0
vbs	0	vbs	0
vds	844.37m	vds	745.418m
vdsat	106.461m	vdsat	106.351m
vgs	745.418m	vgs	745.418m
vth	610.195m	vth	610.476m

Figure 46: The DC operating points of all transistors

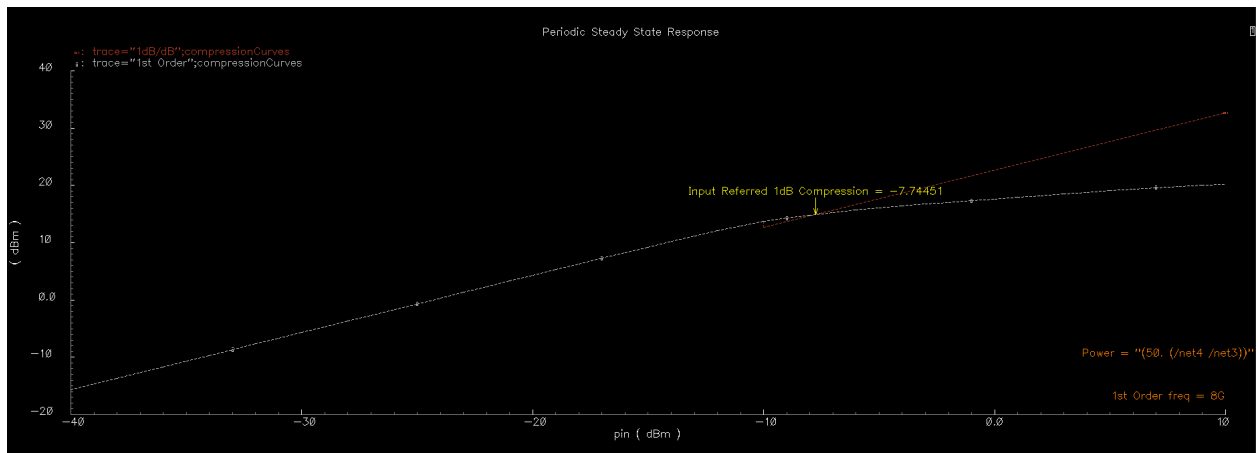


Figure 47: The 1dB compression point

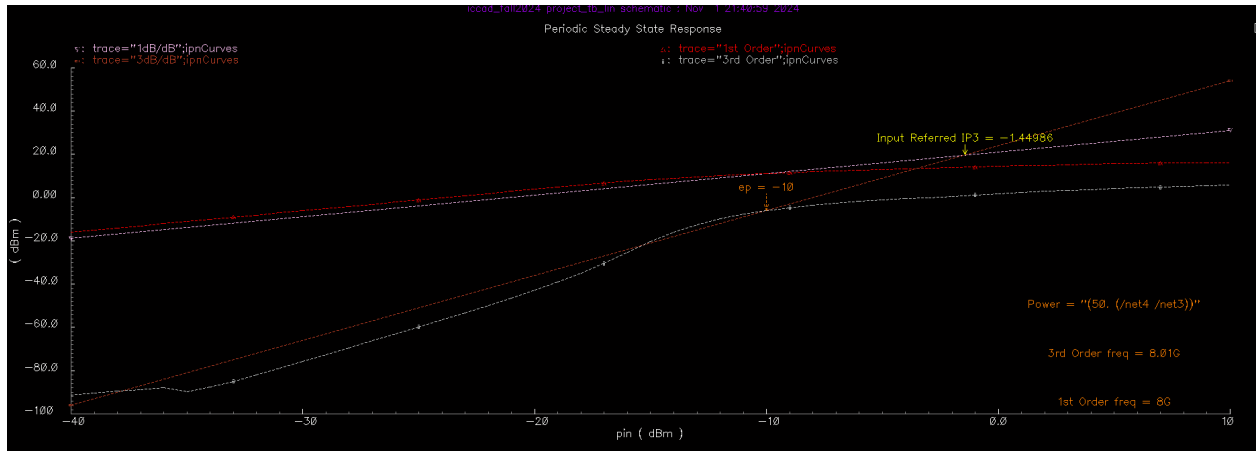


Figure 48: The IIP3 plot

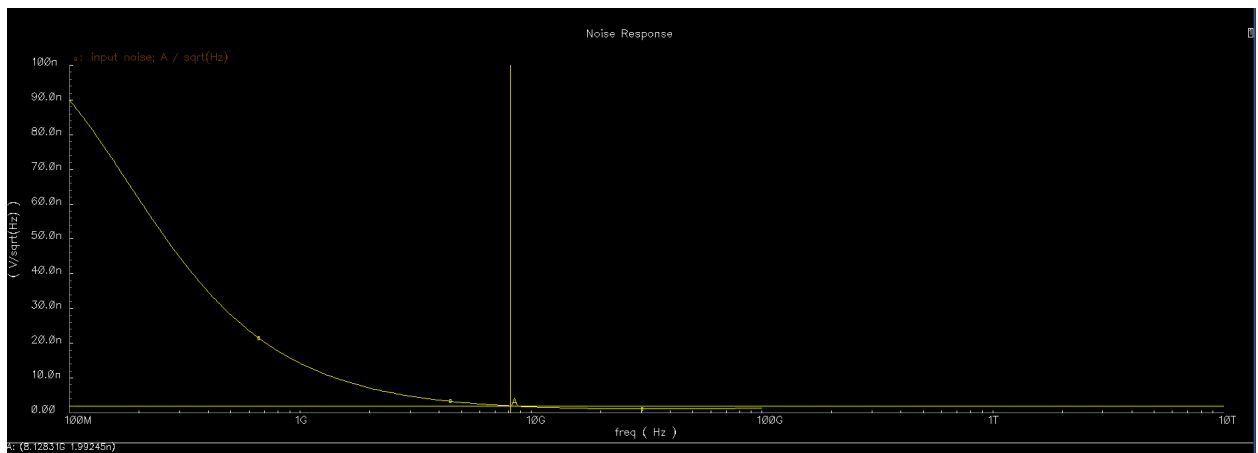


Figure 49: The input-referred noise

3.3.2 27°

The analysis results are shown in the following figures and in table 11.

Power consumption (mW)	8.1
Gain (@ 8GHz, dB)	25.154
Bandwidth (GHz)	0.966
1dB compression point (dBm)	-6.582
3rd input intercept point (dBm)	-0.507
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.1

Table 11: The circuit properties at 27°

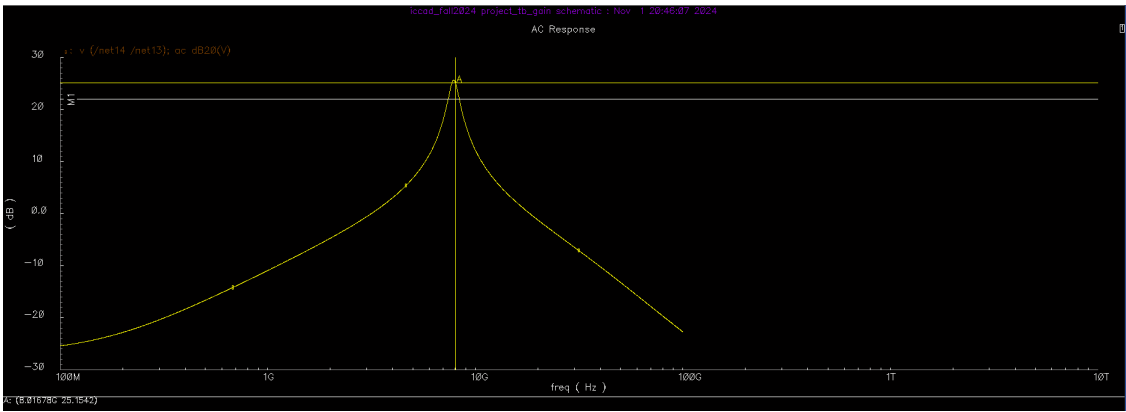


Figure 50: The gain plot

Curve name map:		

Curve1	- v (/net14 /net13); ac dB20(V)	
Curve table:		

	Y value	Curve1
M1	22	7.39779910146 8.36375637386

Figure 51: The bandwidth, using a horizontal marker

signal	OP("I0.M3" "??")	signal	OP("I0.M5" "??")
betaeff	124.059m	betaeff	124.059m
cbb	14.7976f	cbb	14.7976f
cbd	41.5613a	cbd	41.5613a
cbg	-10.0796f	cbg	-10.0796f
cbs	-4.75961f	cbs	-4.75961f
cdb	-7.53739a	cdb	-7.53739a
cdd	17.3641f	cdd	17.3641f
cdg	-17.3895f	cdg	-17.3895f
cds	32.9475a	cds	32.9475a
cgb	-6.10544f	cgb	-6.10544f
cgd	-16.9367f	cgd	-16.9367f
cgg	86.3856f	cgg	86.3856f
cgs	-63.3434f	cgs	-63.3434f
cjd	19.8356f	cjd	19.8356f
cjs	27.5767f	cjs	27.5767f
csb	-8.68466f	csb	-8.68466f
csd	-468.969a	csd	-468.969a
csg	-58.9165f	csg	-58.9165f
css	68.0701f	css	68.0701f
gds	558.068u	gds	558.068u
gm	15.6063m	gm	15.6063m
gmbs	3.28377m	gmbs	3.28377m
gmoverid	7.61092	gmoverid	7.61092
ibulk	-346.864p	ibulk	-346.864p
id	2.05051m	id	2.05051m
ids	2.05051m	ids	2.05051m
is	-2.05051m	is	-2.05051m
pwr	1.93353m	pwr	1.93353m
region	2	region	2
reversed	0	reversed	0
ron	459.861	ron	459.861
type	0	type	0
vbs	-848.051m	vbs	-848.051m
vds	942.949m	vds	942.949m
vdsat	148.54m	vdsat	148.54m
vgs	951.949m	vgs	951.949m
vth	784.505m	vth	784.505m

signal	OP("I0.M6" "??")	signal	OP("I0.M0" "??")
betaeff	263.641m	betaeff	26.3624m
cbb	39.9134f	cbb	3.99294f
cbd	28.8332a	cbd	2.2936a
cbg	-22.0124f	cbg	-2.20006f
cbs	-17.9298f	cbs	-1.79518f
cdb	-28.8943a	cdb	-3.98256a
cdd	34.7377f	cdd	3.47551f
cdg	-34.7965f	cdg	-3.48302f
cds	87.6078a	cds	11.4904a
cgb	-13.0911f	cgb	-1.3013f
cgd	-34.5226f	cgd	-3.45283f
cgg	174.149f	cgg	17.4146f
cgs	-126.535f	cgs	-12.6605f
cjd	47.8885f	cjd	4.93308f
cjs	70.3437f	cjs	11.1164f
csb	-26.7934f	csb	-2.68766f
csd	-244.017a	csd	-24.978a
csg	-117.34f	csg	-11.7316f
css	144.377f	css	14.4442f
gds	881.033u	gds	93.9219u
gm	31.1495m	gm	3.06245m
gmbs	10.0051m	gmbs	987.33u
gmoverid	7.59554	gmoverid	7.65613
ibulk	-105.87p	ibulk	-425.191f
id	4.10102m	id	400u
ids	4.10102m	ids	400u
is	-4.10102m	is	-400u
pwr	3.47788m	pwr	294.688u
region	2	region	2
reversed	0	reversed	0
ron	206.79	ron	1.8418K
type	0	type	0
vbs	0	vbs	0
vds	848.051m	vds	736.72m
vdsat	139.823m	vdsat	139.687m
vgs	736.72m	vgs	736.72m
vth	566.388m	vth	566.704m

Figure 52: The DC operating points of all transistors

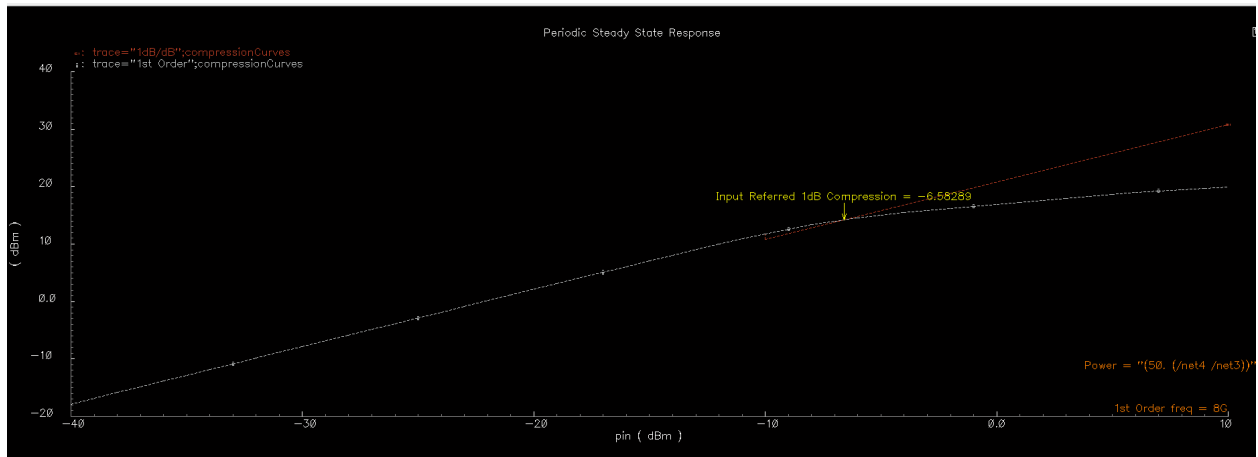


Figure 53: The 1dB compression point

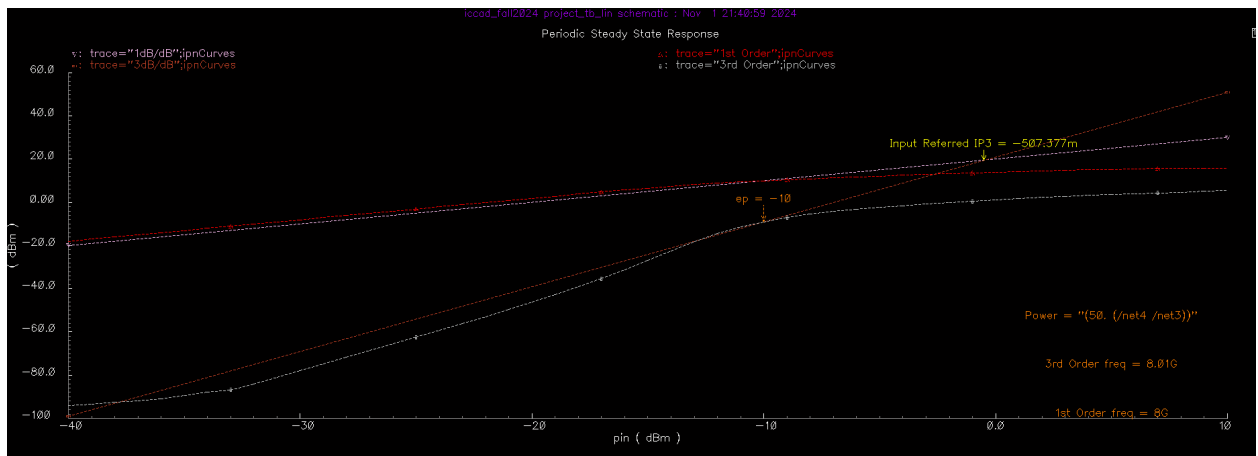


Figure 54: The IIP3 plot

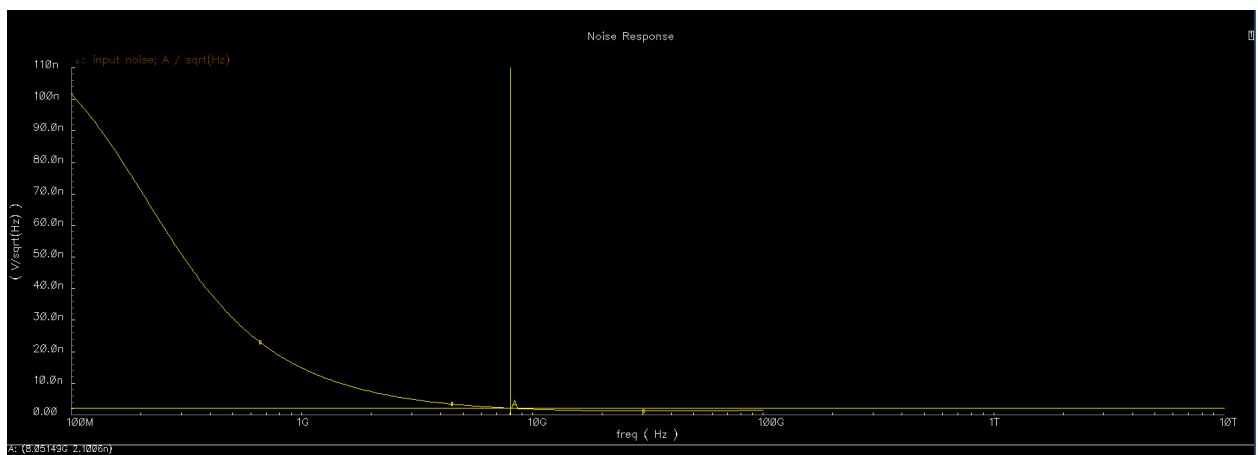


Figure 55: The input-referred noise

3.3.3 120°

The analysis results are shown in the following figures and in table 12.

Power consumption (mW)	8.931
Gain (@ 8GHz, dB)	23.48
Bandwidth (GHz)	0.611
1dB compression point (dBm)	-5.041
3rd input intercept point (dBm)	0.928
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	2.27

Table 12: The circuit properties at 120°

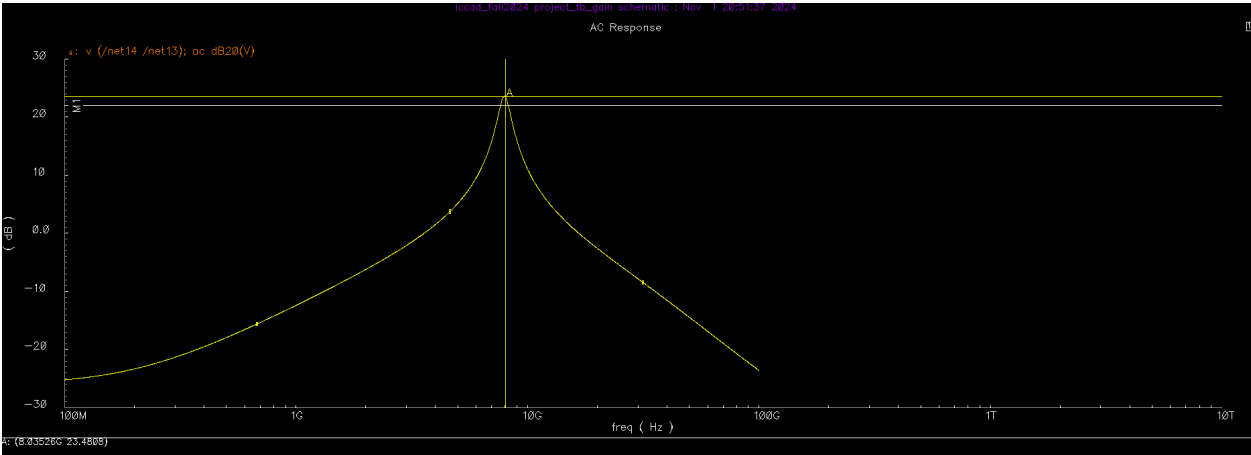


Figure 56: The gain plot

Curve name map:		

Curve1	-	v (/net14 /net13); ac dB20(V)
Curve table:		

	Y value	Curve1
M1	22	7.64864177246
		8.25935705446

Figure 57: The bandwidth, using a horizontal marker

signal	OP("I0.M3" "?")	signal	OP("I0.M5" "?")
betaeff	74.5036m	betaeff	74.5036m
cbb	14.7846f	cbb	14.7846f
cbd	40.283a	cbd	40.283a
cbg	-10.0476f	cbg	-10.0476f
cbs	-4.77728f	cbs	-4.77728f
cdb	-8.68012a	cdb	-8.68012a
cdd	17.3691f	cdd	17.3691f
cdg	-17.3937f	cdg	-17.3937f
cds	33.3042a	cds	33.3042a
cgb	-5.79479f	cgb	-5.79479f
cgd	-16.9404f	cgd	-16.9404f
cgg	86.2364f	cgg	86.2364f
cgs	-63.5013f	cgs	-63.5013f
cjd	19.9348f	cjd	19.9348f
cjs	28.1734f	cjs	28.1734f
csb	-8.98111f	csb	-8.98111f
csd	-468.984a	csd	-468.984a
csg	-58.7951f	csg	-58.7951f
css	68.2452f	css	68.2452f
gds	508.763u	gds	508.763u
gm	12.9803m	gm	12.9803m
gmbs	2.81979m	gmbs	2.81979m
gmoverid	6.32868	gmoverid	6.32868
ibulk	-149.653p	ibulk	-149.653p
id	2.05103m	id	2.05103m
ids	2.05103m	ids	2.05103m
is	-2.05103m	is	-2.05103m
pwr	1.92771m	pwr	1.92771m
region	2	region	2
reversed	0	reversed	0
ron	458.245	ron	458.245
type	0	type	0
vbs	-848.391m	vbs	-848.391m
vds	939.874m	vds	939.874m
vdsat	199.783m	vdsat	199.783m
vgs	951.609m	vgs	951.609m
vth	730.581m	vth	730.581m

signal	OP("I0.M6" "?")	signal	OP("I0.M0" "?")
betaeff	158.966m	betaeff	15.8955m
cbb	39.9093f	cbb	3.99271f
cbd	24.4056a	cbd	1.49771a
cbg	-21.9388f	cbg	-2.19241f
cbs	-17.9949f	cbs	-1.80179f
cdb	-32.7215a	cdb	-4.66498a
cdd	34.7516f	cdd	3.47794f
cdg	-34.8067f	cdg	-3.48492f
cds	87.8147a	cds	11.6367a
cgb	-12.5259f	cgb	-1.24424f
cgd	-34.5282f	cgd	-3.45374f
cgg	173.888f	cgg	17.3892f
cgs	-126.834f	cgs	-12.6912f
cjd	48.8158f	cjd	5.05299f
cjs	75.6808f	cjs	11.9676f
csb	-27.3507f	csb	-2.7438f
csd	-247.774a	csd	-25.7023a
csg	-117.143f	csg	-11.7119f
css	144.741f	css	14.4814f
gds	835.106u	gds	91.8474u
gm	26.1982m	gm	2.56833m
gmbs	8.51202m	gmbs	837.812u
gmoverid	6.3866	gmoverid	6.42082
ibulk	-64.8883p	ibulk	-3.39178p
id	4.10206m	id	400u
ids	4.10206m	ids	400u
is	-4.10206m	is	-400u
pwr	3.48015m	pwr	292.611u
region	2	region	2
reversed	0	reversed	0
ron	206.821	ron	1.82882K
type	0	type	0
vbs	0	vbs	0
vds	848.391m	vds	731.528m
vdsat	187.336m	vdsat	187.176m
vgs	731.528m	vgs	731.528m
vth	505.595m	vth	505.926m

Figure 58: The DC operating points of all transistors

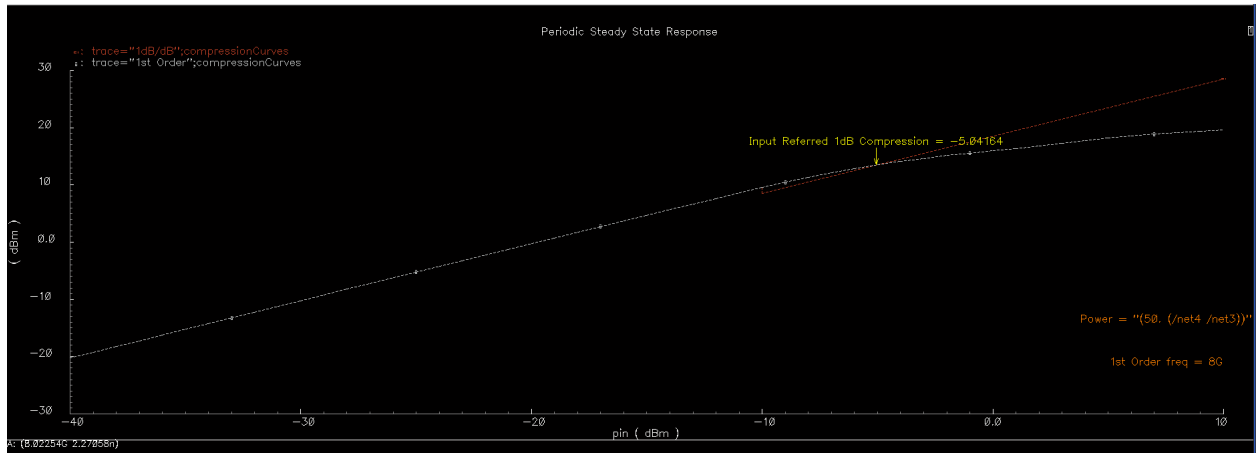


Figure 59: The 1dB compression point

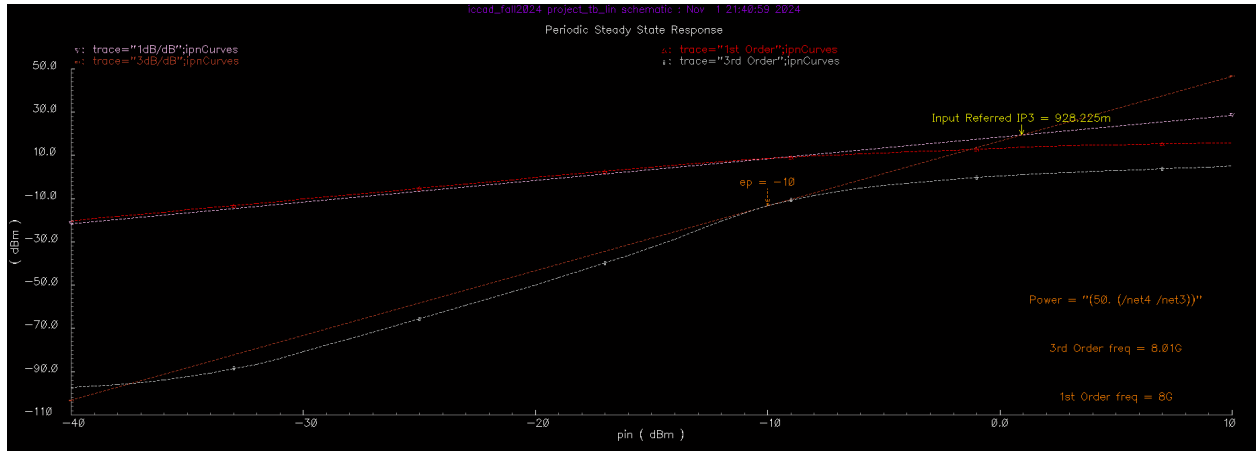


Figure 60: The IIP3 plot

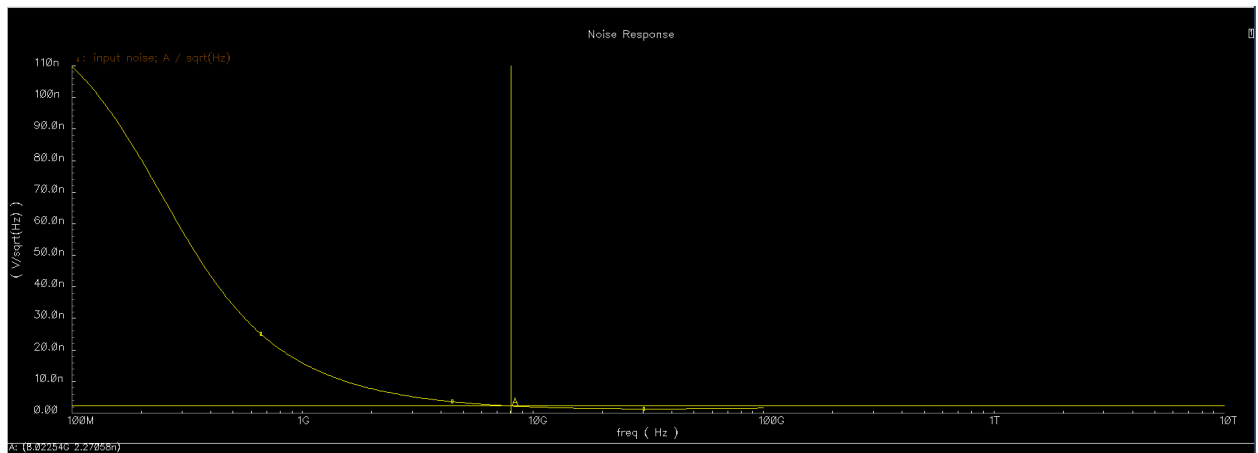


Figure 61: The input-referred noise

3.4 Final Results

	TT (-40)	TT (27)	TT (120)	FF (-40)	FF (27)	FF (120)	SS (-40)	SS (27)	SS (120)
Power consumption (mW)	8.380	8.378	8.377	9.003	11.152	10.839	8.380	8.1	8.931
Gain (@ 8GHz, dB)	27.543	25.891	23.792	26.227	24.509	22.223	26.467	25.154	23.48
Bandwidth (GHz)	1.463	1.153	0.794	1.651	1.308	0.877	1.253	0.966	0.611
1dB compression point (dBm)	-6.004	-4.359	-2.411	-1.160	-0.485	0.425	-7.744	-6.582	-5.041
3rd input intercept point (dBm)	-3.563	4.668	1.201	-1.343	1.647	4.604	-1.449	-0.507	0.928
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.964	2.071	2.232	1.906	2.017	2.196	1.992	2.1	2.27

Table 13: The circuit properties at all corners and temperatures

4 Layout

The first step was to lay the floor plan by putting down every element. As the amplifier is differential, keeping the design as symmetric as possible is important, so I put the components in a symmetric pattern, as shown in fig. 62. Then, I made the connections as short and sparse

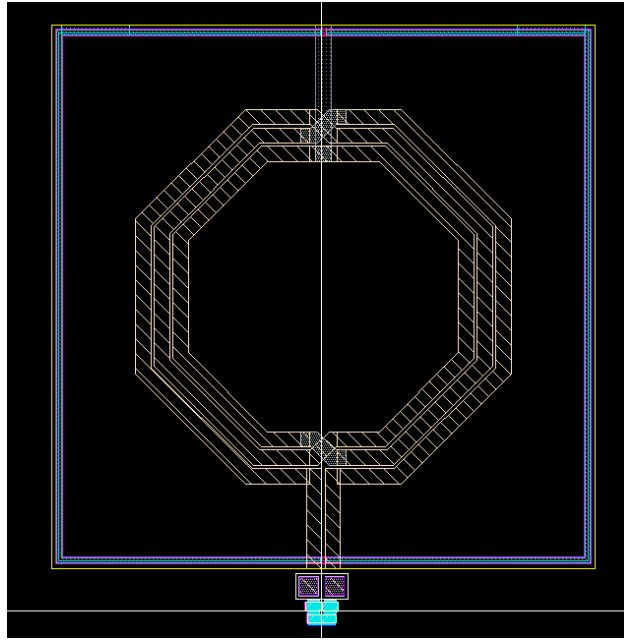


Figure 62: The floor plan

as possible. The following figures show every connection starting from the bottom of the circuit and moving to the top.

The bias transistors are shown in fig.63. The bulks are connected to the sources of both transistors which are connected on M1 and then brought to M2 to jump over the gate of the tail transistor and connected to the ground pin, GRND. The gates are also connected on both top and bottom, the two sides are connected to each other, and the bias current pin, IB. The drain of the current mirror transistor is directly connected to the top connection of the gate and the drains of the tail transistor are brought to M2 with VIAs and connected to the sources of the input transistors, as shown in fig. 64.

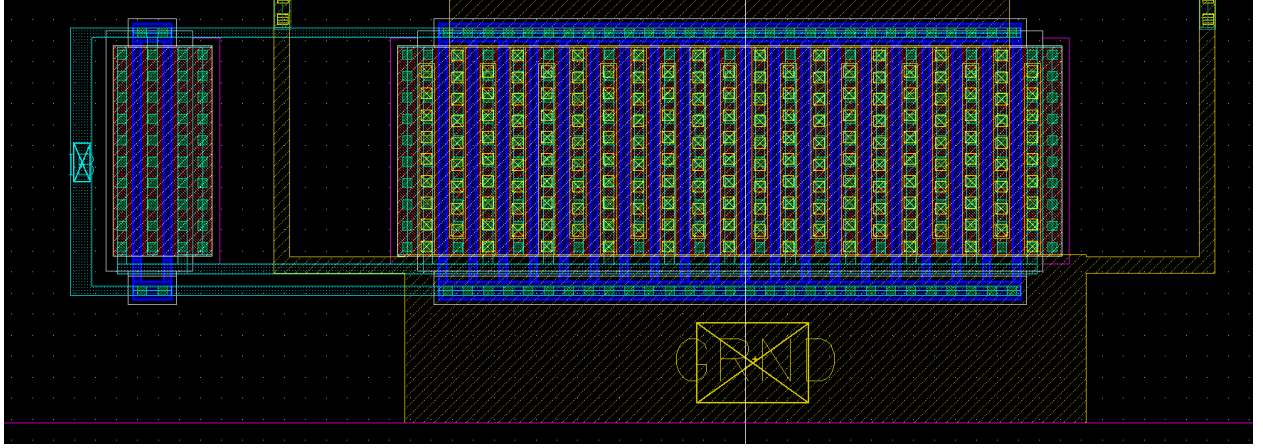


Figure 63: The bias circuit

The bulk connections of the input transistor are visible on both sides but detached from the sources. The bulk connection closer to the middle of both transistors is brought to M3 with VIAs to jump over the source connection, and then it is brought back to M2 to connect with the bulk connections on the outer edge and the GRND pin.

The gate connections of each transistor are connected on the outer edge and the input pins, IN1 and IN2, are placed there on M1. The drains of each transistor are connected and then brought to M6 with VIAs to connect to the top layer of the capacitors, as shown in fig. 65.

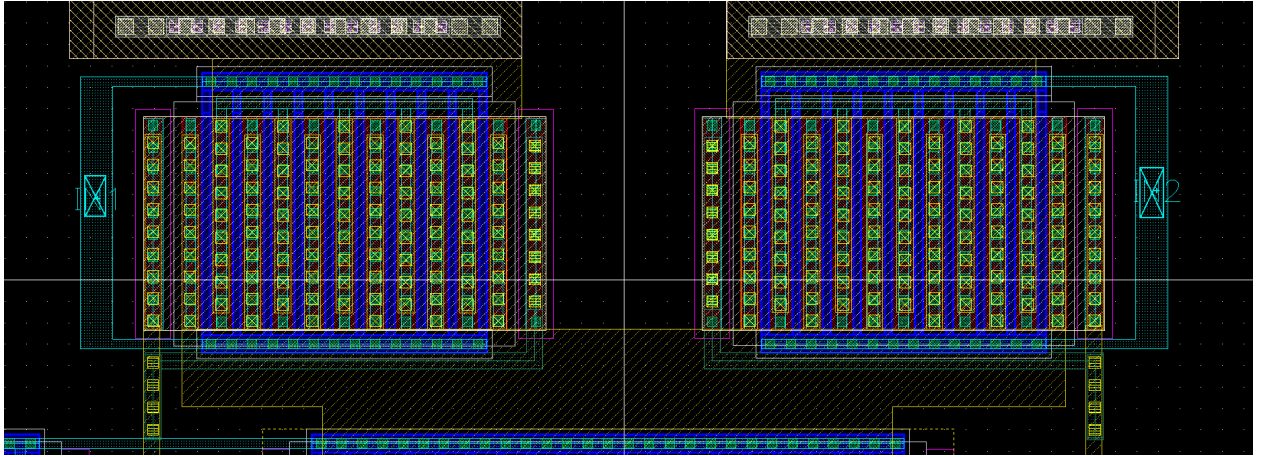


Figure 64: The input transistors

The top layer of the capacitors connects directly to the PLUS and MINUS terminals of the inductor and the output pins, OUT1 and OUT2, are placed on their connection. The bottom layer is dragged to the sides of the inductor and brought down to M4 with VIAs to connect to the center tap of the inductor, as shown in fig. 66. Fig. 67 shows the complete layout, while fig. 68 and fig. 69 show the results of the DRC and LVS runs.

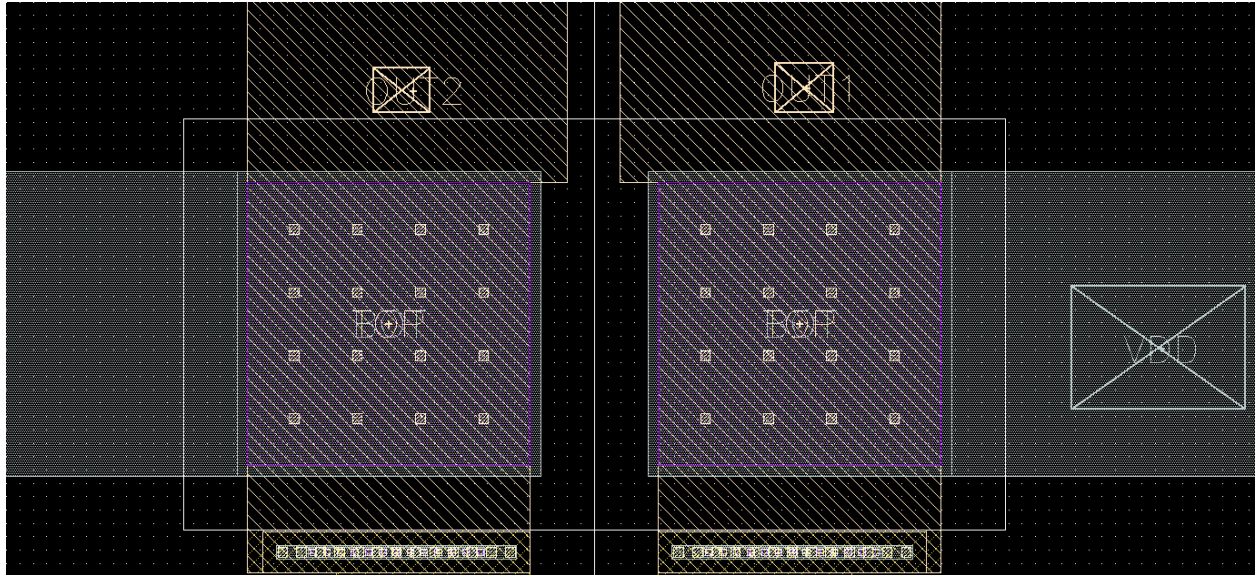


Figure 65: The capacitors

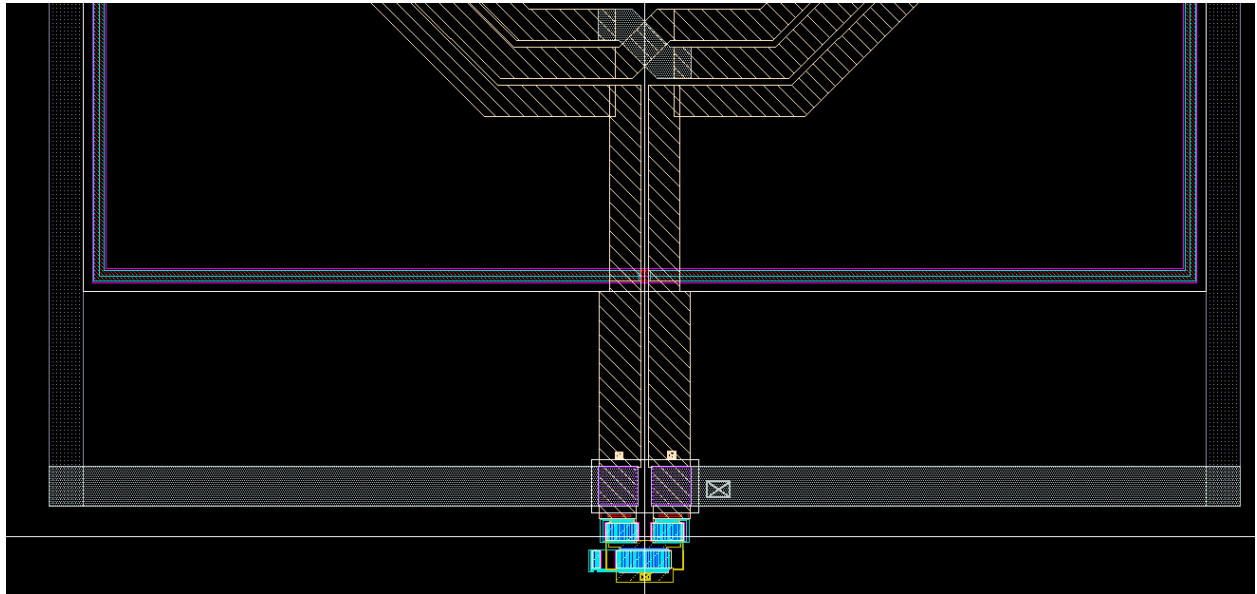


Figure 66: Overview of the capacitor connections

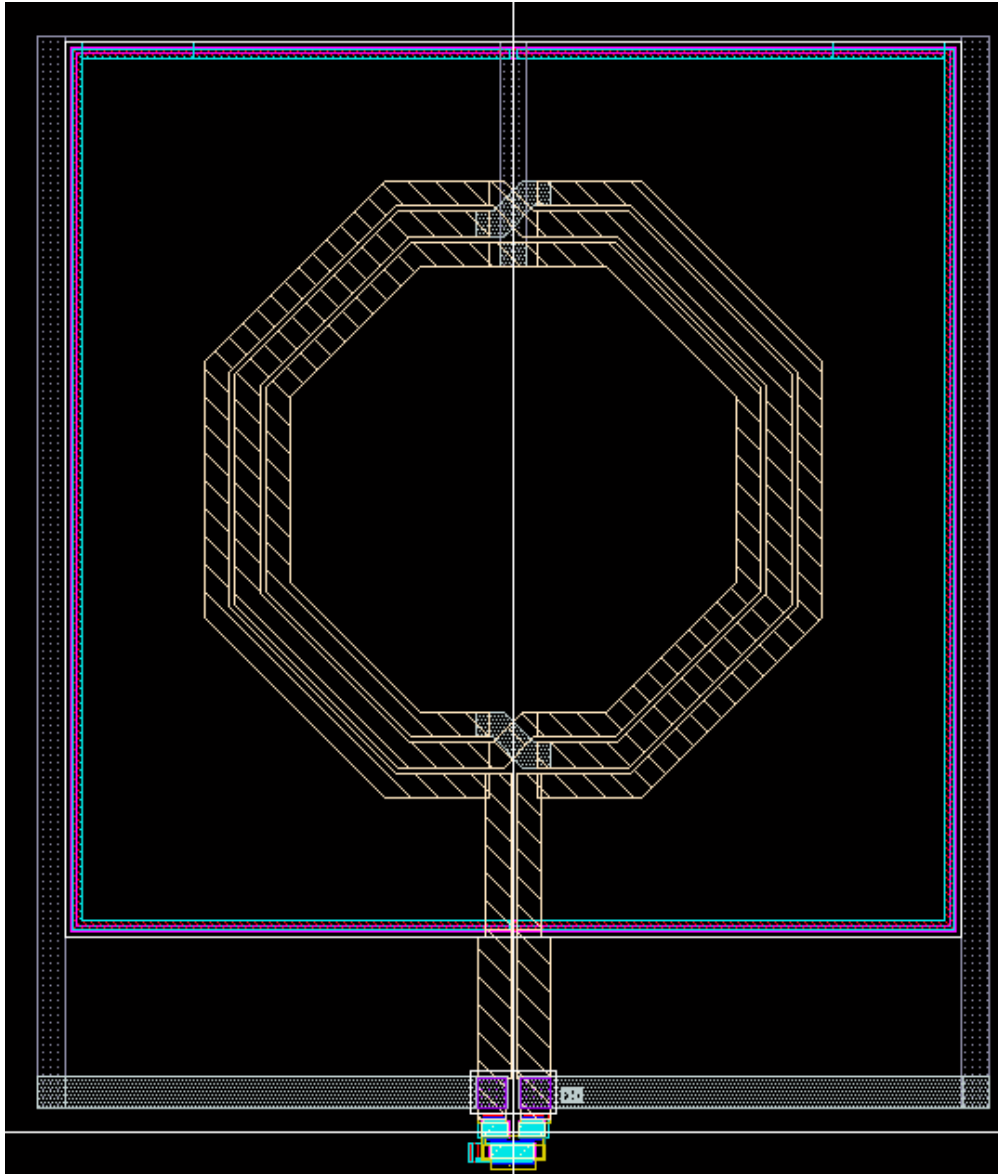


Figure 67: The layout

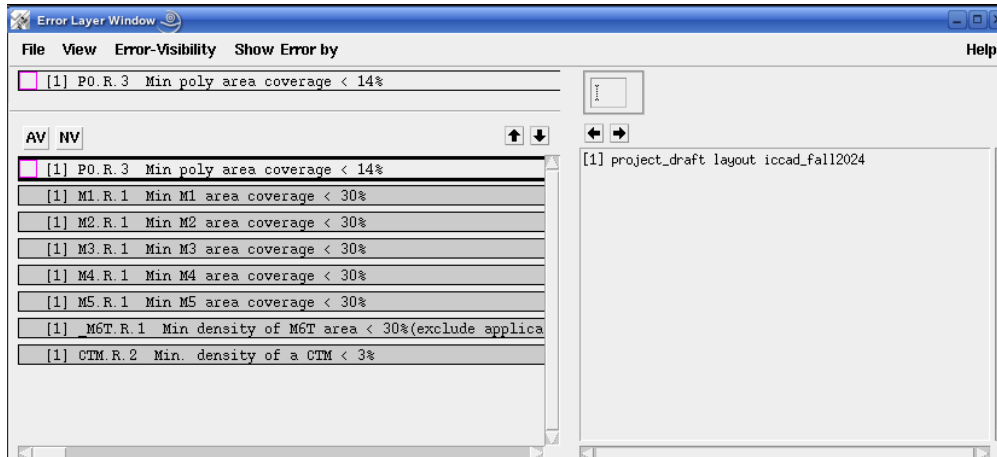


Figure 68: The DRC results

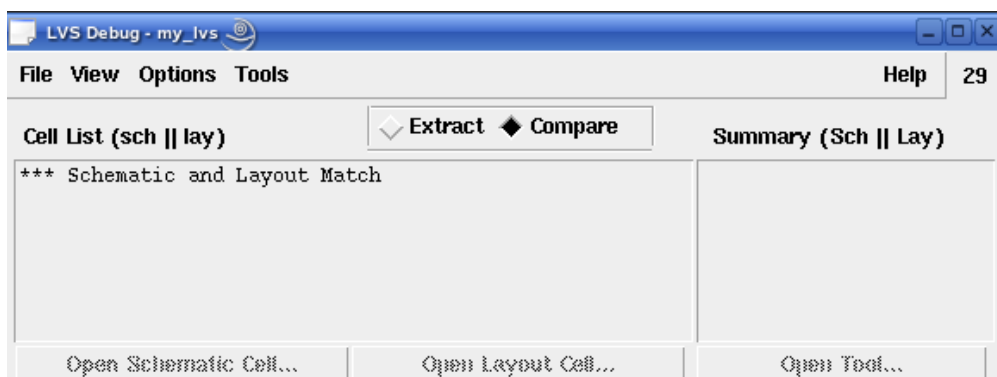


Figure 69: The LVS results

5 Parasitics

I ran the RCX three times: for R only, RC, and RLC. The settings each time were similar and only the name of the file and the extracted parameters in the Extraction tab were changed, a sample of which is shown in fig. 70. Also, fig. 71 shows a view of the RC parasitic file, zoomed in on the tail transistor to show the parasitics.

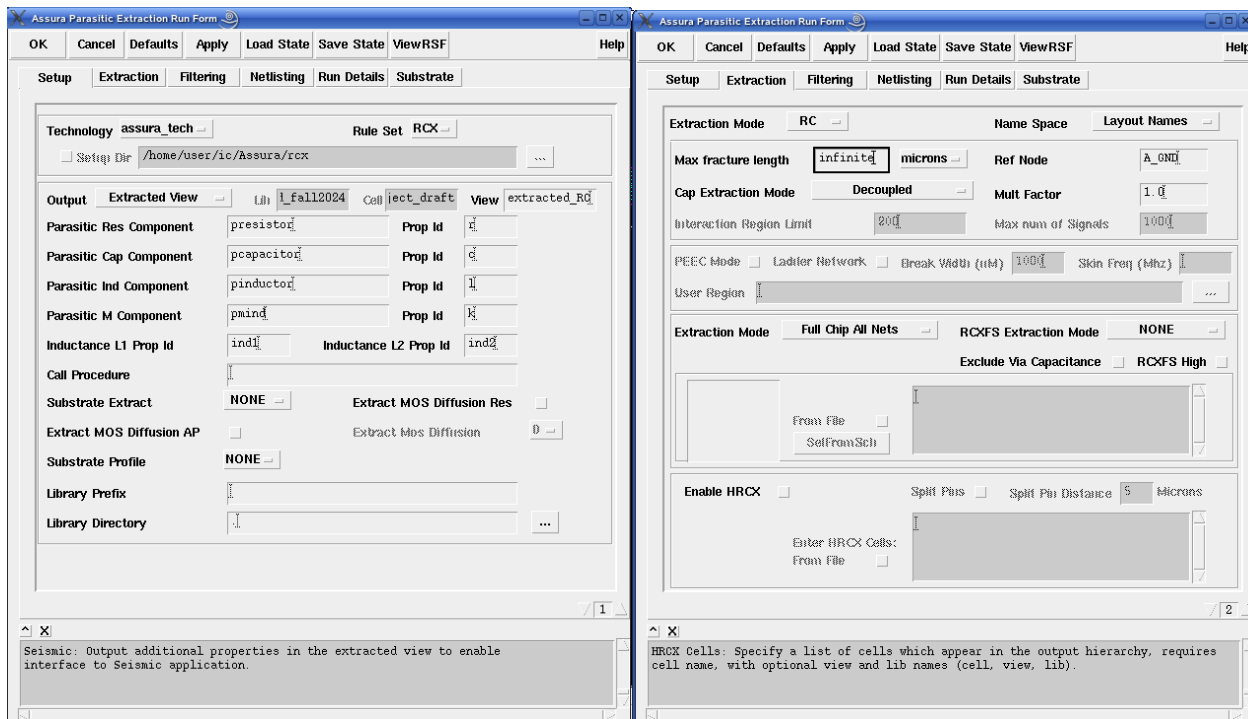


Figure 70: The RCX setup

Then, I made a config cell view of my test benches and ran the same analyses as the first section, for the TT corner at 27 degrees, the results of which are shown in the following subsections. The results for the AC and noise plots are drawn on the schematic results for ease of comparison, with the yellow line as the results from the parasitic view. Also, fig. 72 shows the hierarchy editor view of one of the test benches.

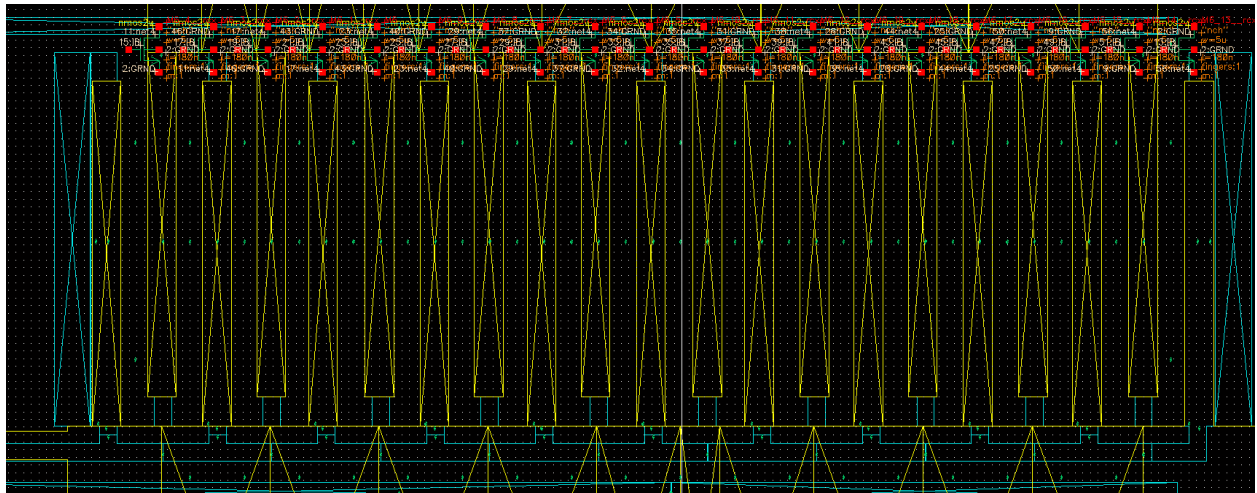


Figure 71: The parasitics

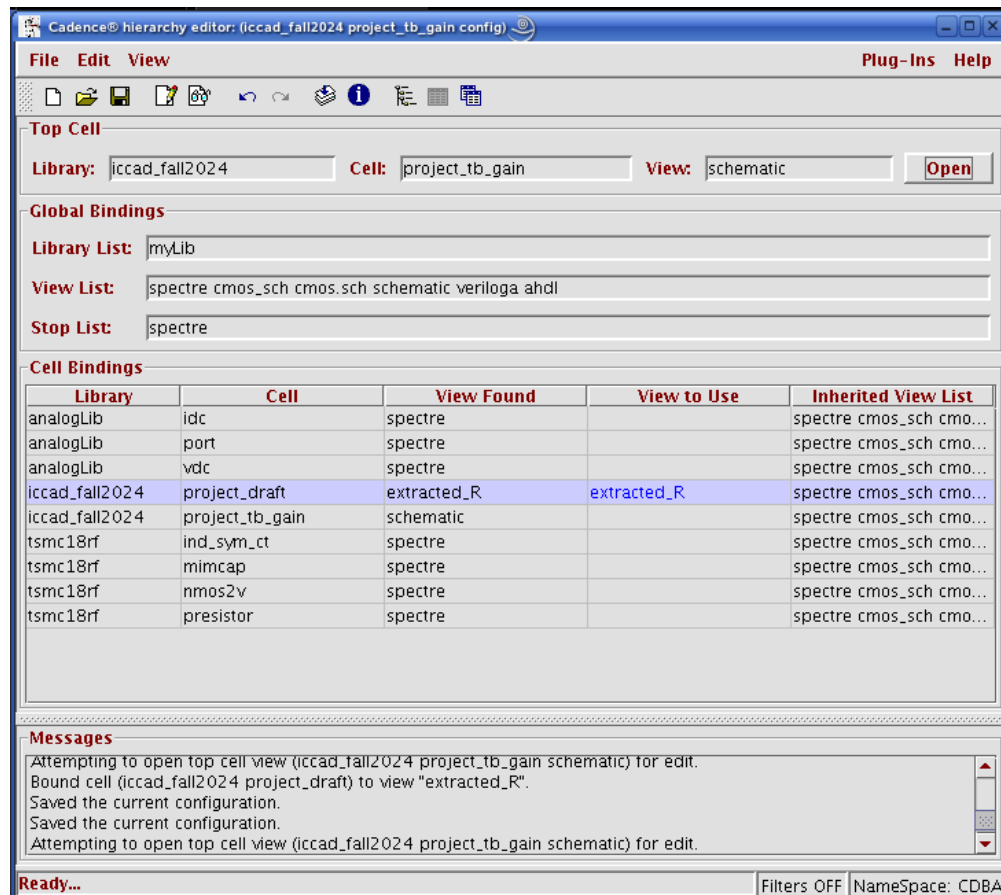


Figure 72: Hierarchy editor

5.1 R Only

The analysis results are shown in the following figures and in table 14. I could not run the PSS analysis to find IP3 because the error shown in fig. 77 occurred.

Power consumption (mW)	8.368
Gain (@ 8GHz, dB)	22.821
Bandwidth (GHz)	0.63
1dB compression point (dBm)	-5.348
3rd input intercept point (dBm)	-
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.992

Table 14: The circuit properties for the R-only parasitic view

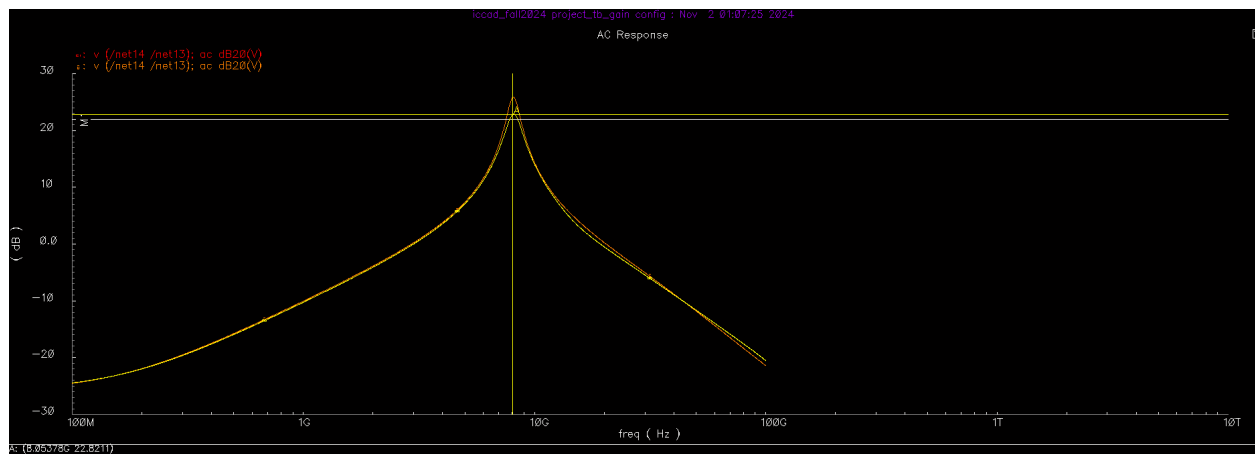


Figure 73: The gain plot

```
Curve name map:
-----

Curve2      - v (/net14 /net13); ac dB20(V)
Curve1      - v (/net14 /net13); ac dB20(V)


Curve table:
-----
```

	Y value	Curve2	Curve1
M1	22	7.8134714363G 8.4438019972G	7.535350187G 8.6887684874G

Figure 74: The bandwidth, using a horizontal marker

signal	OP("V0" "??")
i	-4.64941m
pwr	-8.36894m
v	1.8

Figure 75: The DC operating point of the supply voltage

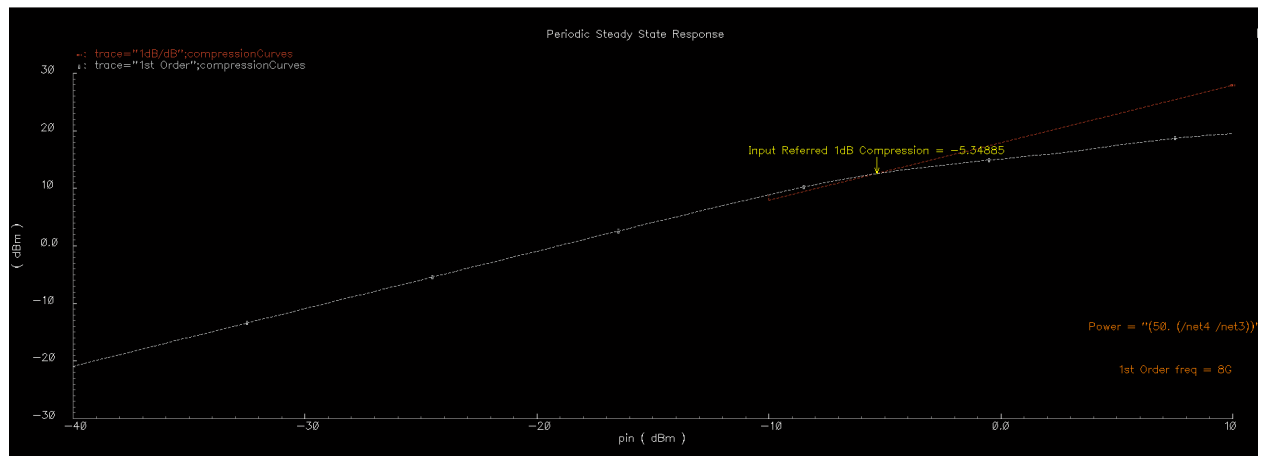


Figure 76: The 1dB compression point

```

Simulating 'input.scs' on linux at 1:57:03 AM, Sat Nov 2, 2024.
Compiling ahdldcmi module library.
Failed to compile ahdldcmi module library, see input.ahdldcmi/ahdldcmi.ov
details
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory

```

Figure 77: The error shown after running PSS analysis for IIP3

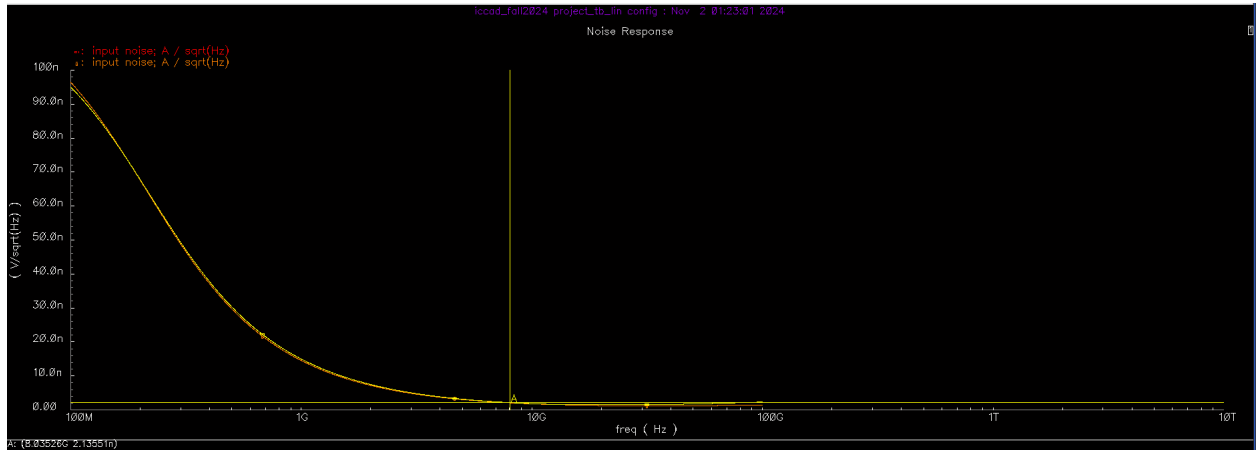


Figure 78: The input-referred noise

5.2 RC

The analysis results are shown in the following figures and in table 15. Again, I encountered the same error when running PSS for IP3.

Power consumption (mW)	8.368
Gain (@ 8GHz, dB)	22.989
Bandwidth (GHz)	0.697
1dB compression point (dBm)	-8.001
3rd input intercept point (dBm)	-
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.86

Table 15: The circuit properties for the RC parasitic view

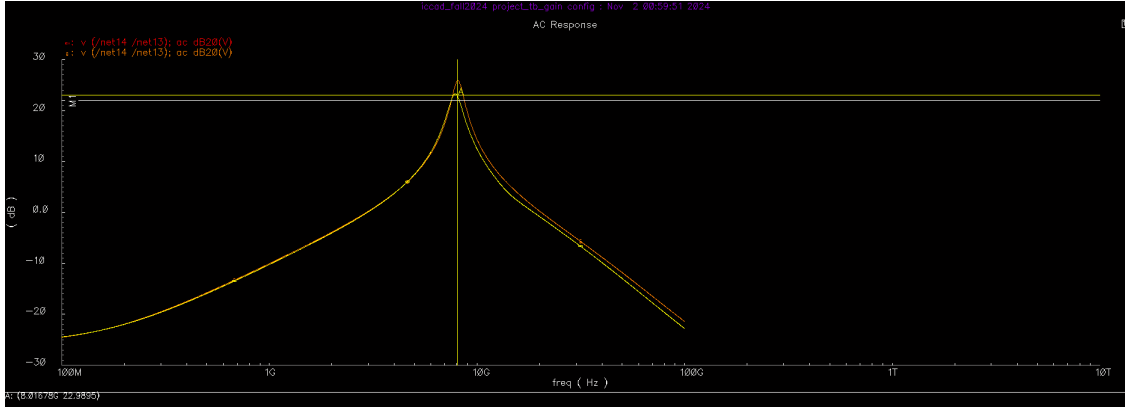


Figure 79: The gain plot

Curve name map:			

Curve2	- v (/net14 /net13); ac dB20(V)		
Curve1	- v (/net14 /net13); ac dB20(V)		
Curve table:			

	Y value	Curve2	Curve1
M1	22	7.51409018736	7.5353501876
		8.21116574356	8.68876848746

Figure 80: The bandwidth, using a horizontal marker

signal	OP("V0" "??")
i	-4.64941m
pwr	-8.36894m
v	1.8

Figure 81: The DC operating point of the supply voltage

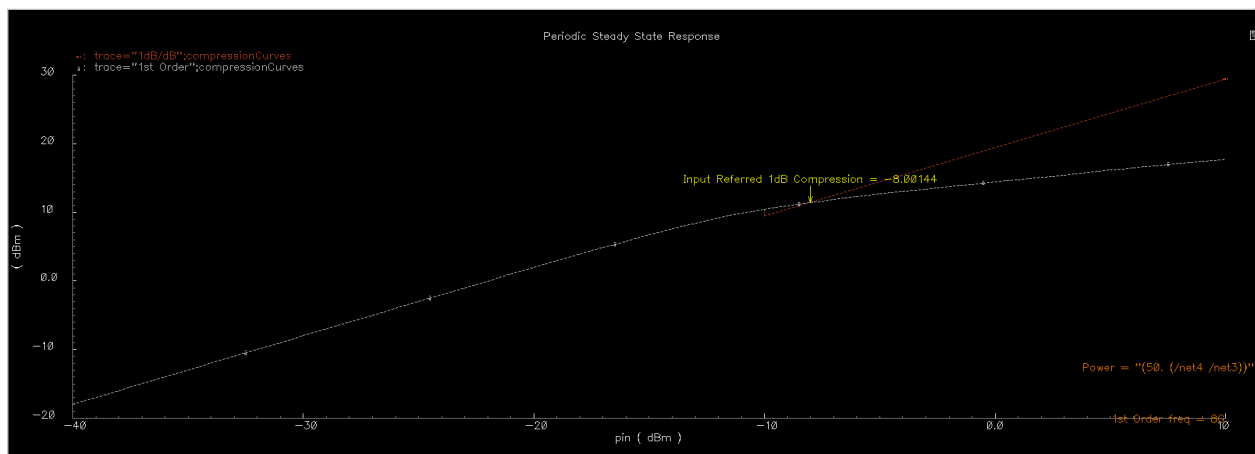


Figure 82: The 1dB compression point

```

Simulating 'input.scs' on linux at 1:59:28 AM, Sat Nov 2, 2024.
Compiling ahdldcmi module library.
Failed to compile ahdldcmi module library, see input.ahdldcmi/ahdldcmi.o:
details
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory

```

Figure 83: The error shown after running PSS analysis for IIP3

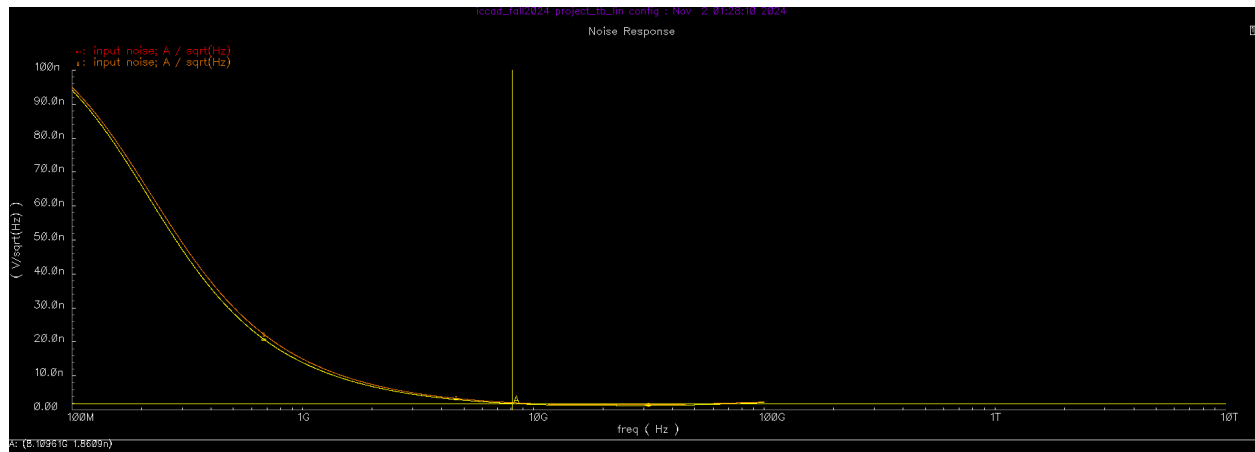


Figure 84: The input-referred noise

5.3 RLC

The analysis results are shown in the following figures and in table 16. Once again, I could not determine the IP3 due to the same error as the last ones.

Power consumption (mW)	8.368
Gain (@ 8GHz, dB)	22.989
Bandwidth (GHz)	0.611
1dB compression point (dBm)	-8.001
3rd input intercept point (dBm)	-
Input-referred noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.86

Table 16: The circuit properties for the RLC parasitic view

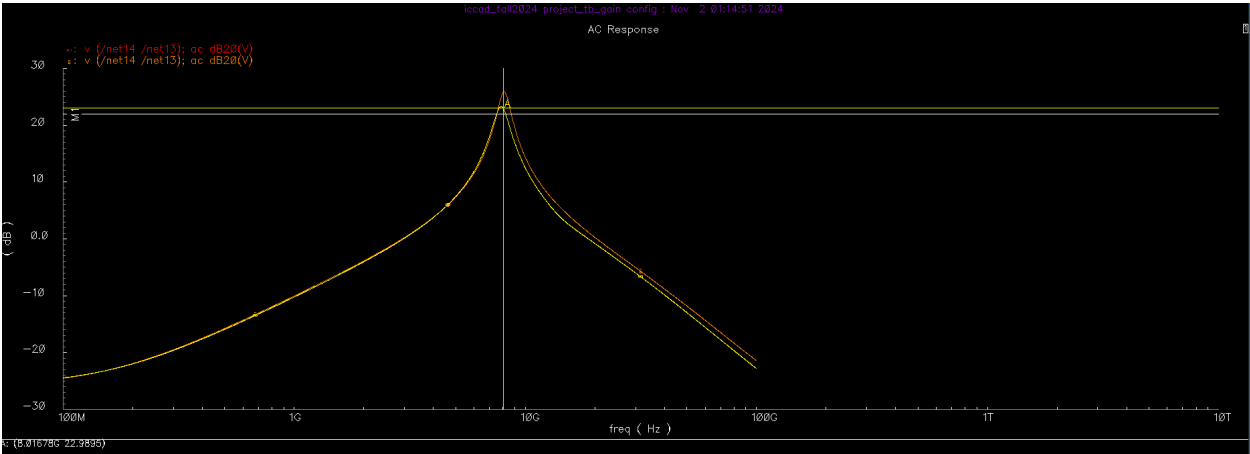


Figure 85: The gain plot

Curve name map:			

Curve2	- v (/net14 /net13); ac	dB20(V)	
Curve1	- v (/net14 /net13); ac	dB20(V)	
Curve table:			

	Y value	Curve2	Curve1
M1	22	7.5140901929G	7.535350187G
		8.211165745G	8.6887684874G

Figure 86: The bandwidth, using a horizontal marker

signal	OP("V0" "??")
i	-4.64941m
pwr	-8.36894m
v	1.8

Figure 87: The DC operating point of the supply voltage

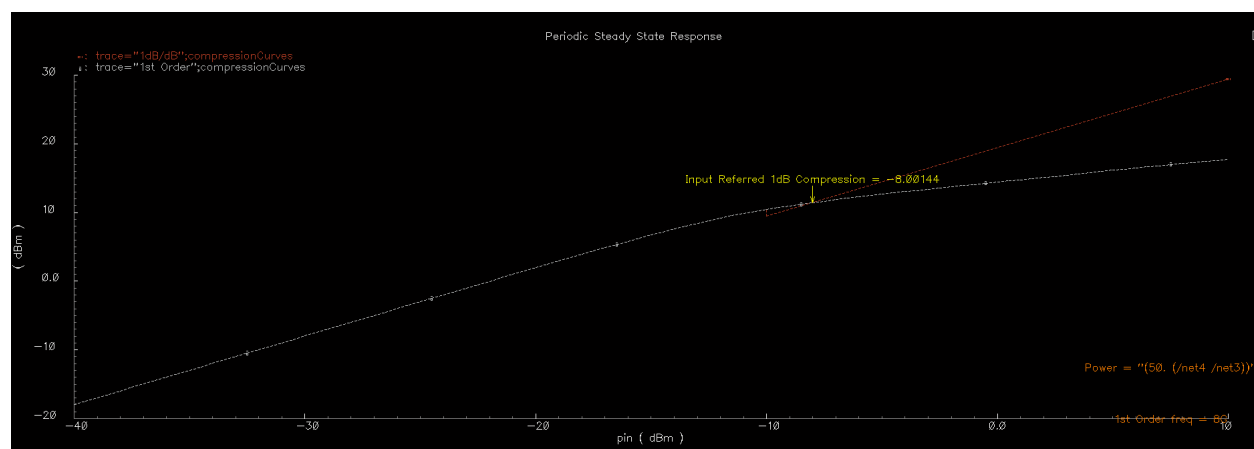


Figure 88: The 1dB compression point

```

Simulating 'input.scs' on linux at 2:04:00 AM, Sat Nov 2, 2024.
Compiling ahdldcmi module library.
Failed to compile ahdldcmi module library, see input.ahdldcmi/ahdldcmi.ov
details
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.
cannot open shared object file: No such file or directory
Could not open ahdldcmi module library input.ahdldcmi/obj/Linux2.6.11.4-
smp+gcc/optimize/libinput.so
input.ahdldcmi/obj/Linux2.6.11.4-20a-smp+gcc/optimize/libinput.

```

Figure 89: The error shown after running PSS analysis for IIP3

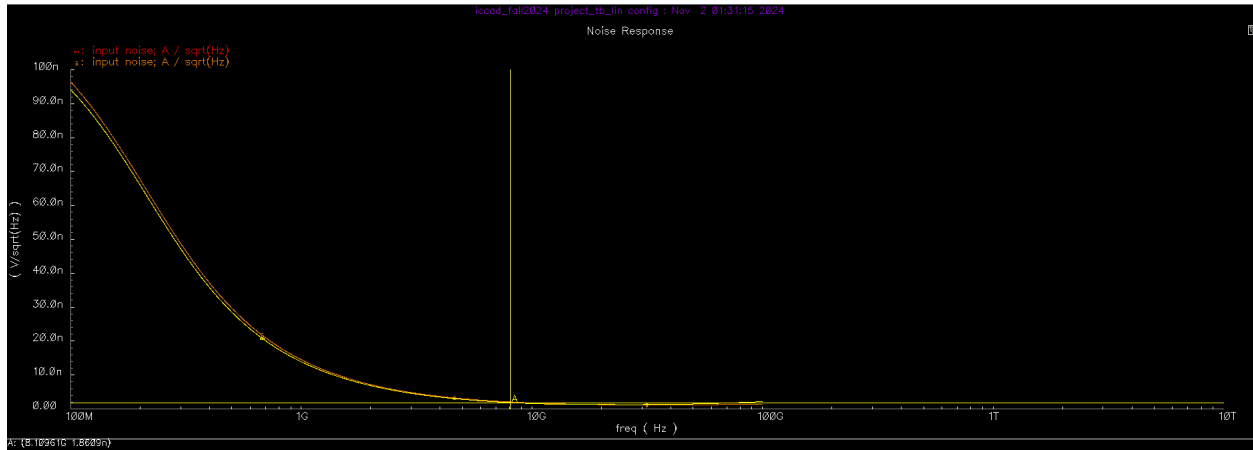


Figure 90: The input-referred noise

6 Final Comparison

The following table compares all the simulation results gathered from this project.

	TT (-40)	TT (27)	TT (120)	FF (-40)	FF (27)	FF (120)	SS (-40)	SS (27)	SS (120)	R	RC	RLC
Power (mW)	8.380	8.378	8.377	9.003	11.152	10.839	8.380	8.1	8.931	8.368	8.368	8.368
Frequency (GHz)	8.058	8.111	8.194	8.297	8.359	8.458	7.834	7.88	7.953	8.128	7.862	7.862
Gain (@ 8GHz, dB)	27.543	25.891	23.792	26.227	24.509	22.223	26.467	25.154	23.48	22.821	22.989	22.989
Bandwidth (GHz)	1.463	1.153	0.794	1.651	1.308	0.877	1.253	0.966	0.611	0.63	0.697	0.611
P_{idB} (dBm)	-6.004	-4.359	-2.411	-1.160	-0.485	0.425	-7.744	-6.582	-5.041	-5.348	-8.001	-8.001
IIP3(dBm)	-3.563	4.668	1.201	-1.343	1.647	4.604	-1.449	-0.507	0.928	-	-	-
noise (@ 8GHz, $\frac{nV}{\sqrt{Hz}}$)	1.964	2.071	2.232	1.906	2.017	2.196	1.992	2.1	2.27	1.992	1.86	1.86

Table 17: The circuit properties at all corners and temperatures