

NETWORK

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graph TD; NETWORK[NETWORK] --- Node1[Node]; NETWORK --- Node2[Node]; NETWORK --- Node3[Node]; subgraph Node1 [Node]; direction TB; M1[MEMORY]; C1_1(Core); C1_2(Core); C1_3(Core); C1_4(Core); end; subgraph Node2 [Node]; direction TB; M2[MEMORY]; C2_1(Core); C2_2(Core); C2_3(Core); C2_4(Core); end; subgraph Node3 [Node]; direction TB; M3[MEMORY]; C3_1(Core); C3_2(Core); C3_3(Core); C3_4(Core); end;
```

The diagram illustrates a network architecture. At the top, a green rectangular box labeled "NETWORK" is connected by three black lines to three identical nodes below. Each node is a white rectangle with a black border. Inside each node, at the top, is a brown rectangular box labeled "MEMORY". Below the "MEMORY" box, there are four olive-green oval shapes, each labeled "Core", arranged in a 2x2 grid. The entire diagram is set against a white background.

MEMORY

Core

Core

Core

Core

Node

MEMORY

Core

Core

Core

Core

Node

MEMORY

Core

Core

Core

Core

Node