DESIGNED WITH OPEN SOURCE Hardware hardware R12 G17 U131 R7 R6 DS3231N wyolum.com — 8n — 60 - +10 U13 U12 N78 U119 CLK\_IN GND DAT\_IN 5٧ 139 - 540 950 750 ogn \_\_\_ 124 1051 — 69n 020 120 122 \_ rru 820 — 890 172 — <sub>620</sub> \_\_\_\_ 660 1010 0109 0110 00100 U103 U108

https://github.com	m/wyolum/ClockIOT	
www.wyolum.com		
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