



Note

1. NRST is the reset pin. The default is high. It is reset when the input is low.
2. PB00 and PB01 default to the TX and RX interfaces of UART1 after power-on. Functions can be changed in the program.
3. When programming with UART, you need to force PB14 to be high.
4. If DCDC is not used to reduce power consumption, replace L1 with 0 ohm resistor, C4 capacitor can be omitted, PIN8 must be connected with PIN24
5. When using TX power above 10dBm, a 1uF capacitor must be added around VDD_PAHP(PIN23).

Title		
LE5010_SIMPLE_SCH		
Size	Number	Revision
A4	1	v0.4
Date:	2020/9/21	Sheet of
File:	D:\work_file\...\LE5010_QFN32_EN.Sch	Drawn By: xwcai