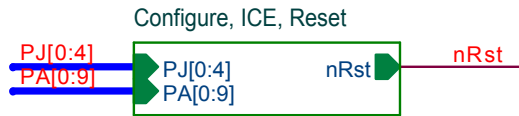
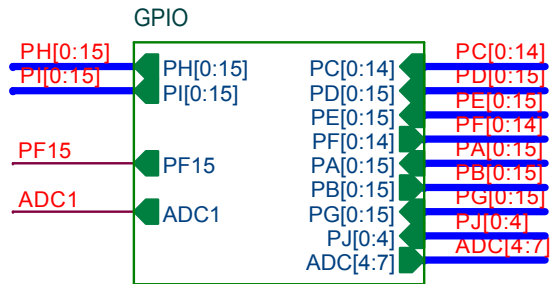


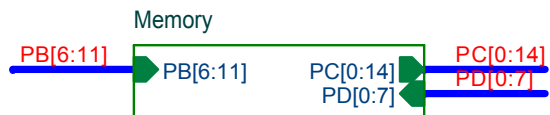
Schematic01



Schematic04



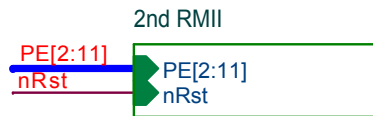
Schematic06



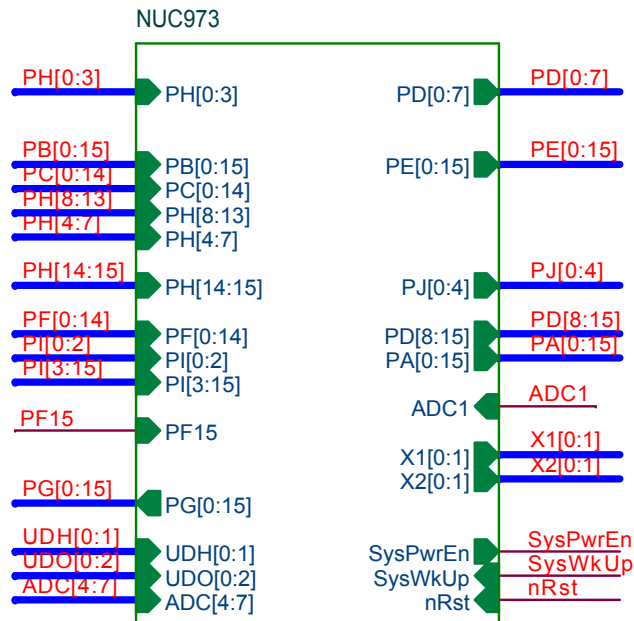
Schematic08



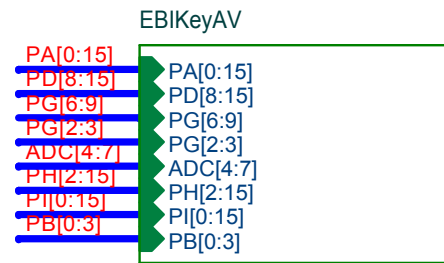
Schematic09



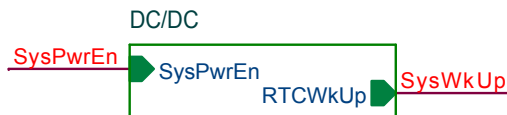
Schematic14



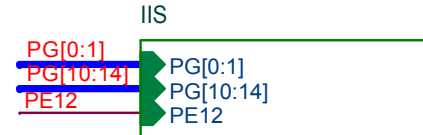
SCHEMATIC03



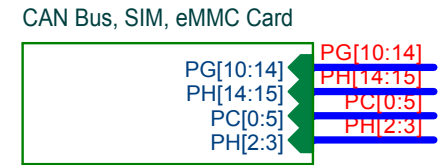
Schematic07



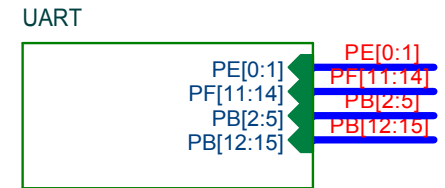
Schematic02



Schematic10



Schematic11



Schematic12



Schematic13



Schematic05

**NHS-973-1-YS-1M51**

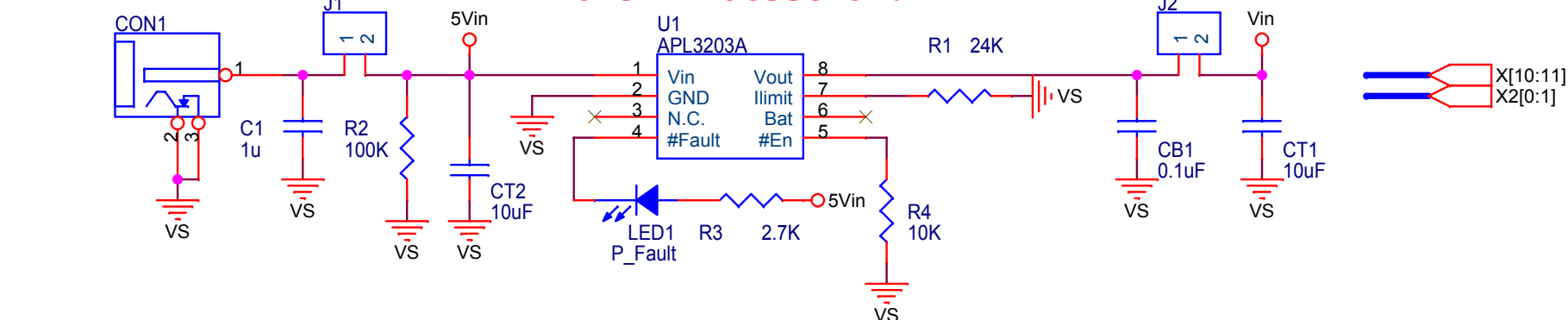
Size	Document Number	Rev
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5V DC-PLUG

5VIN

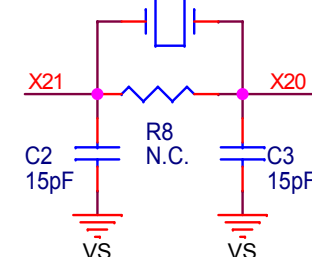
Power Protection.

VIN

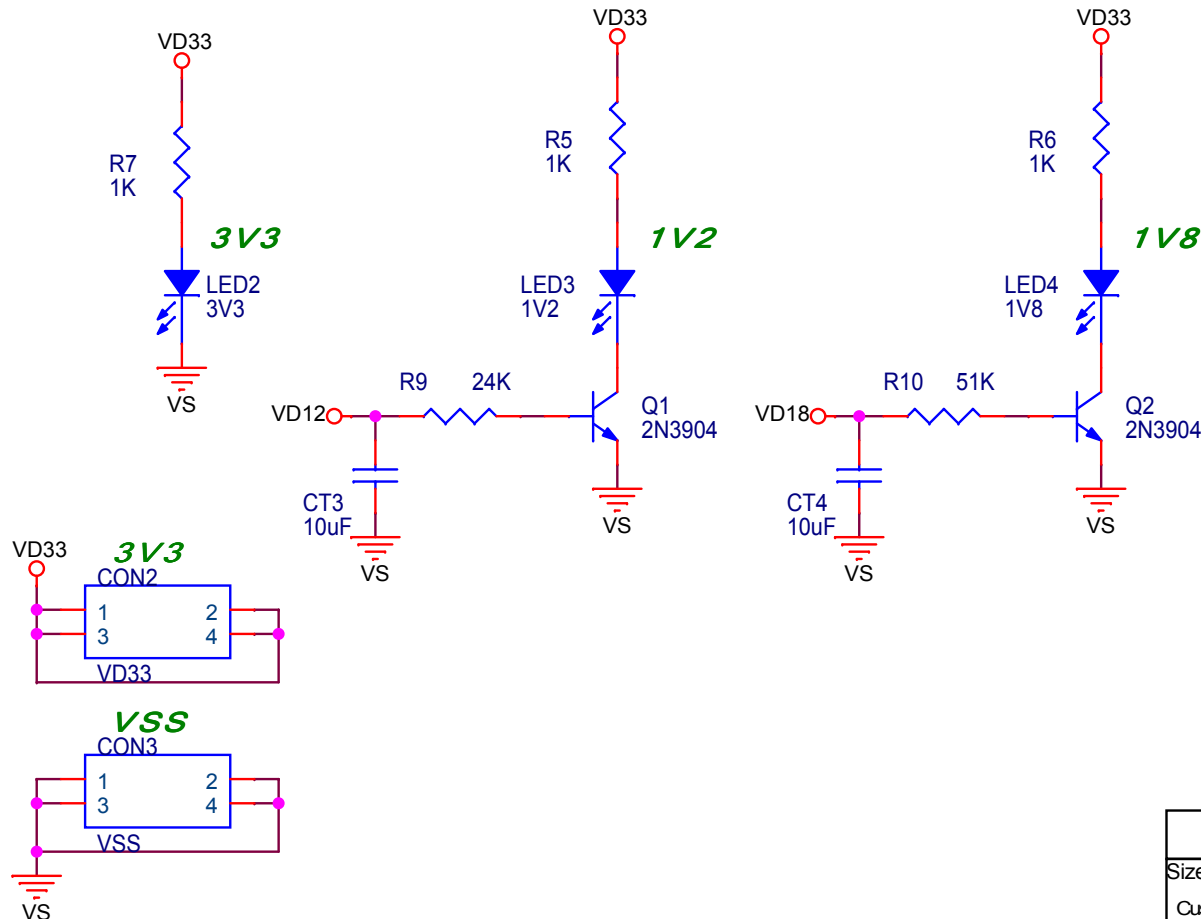
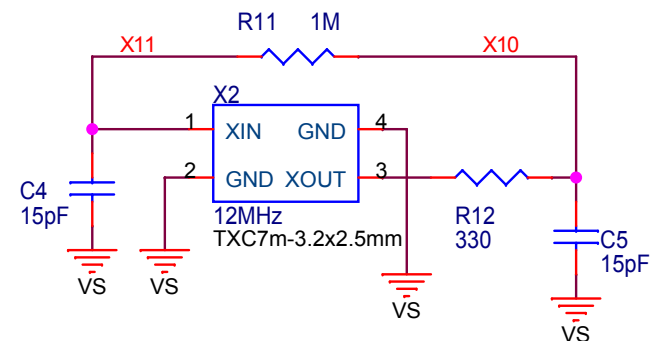


RTC Crystal

X1  
TXC-9H T9 SMD 32.768KHz 30ppm



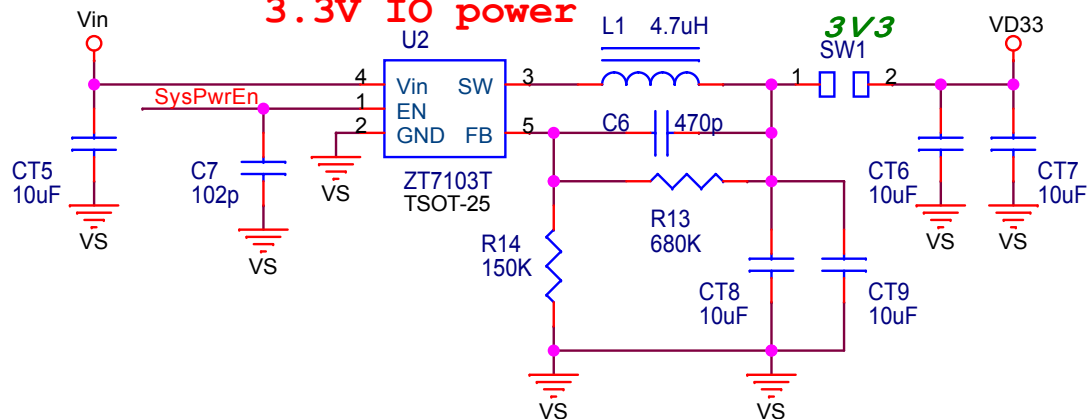
System Main Crystal



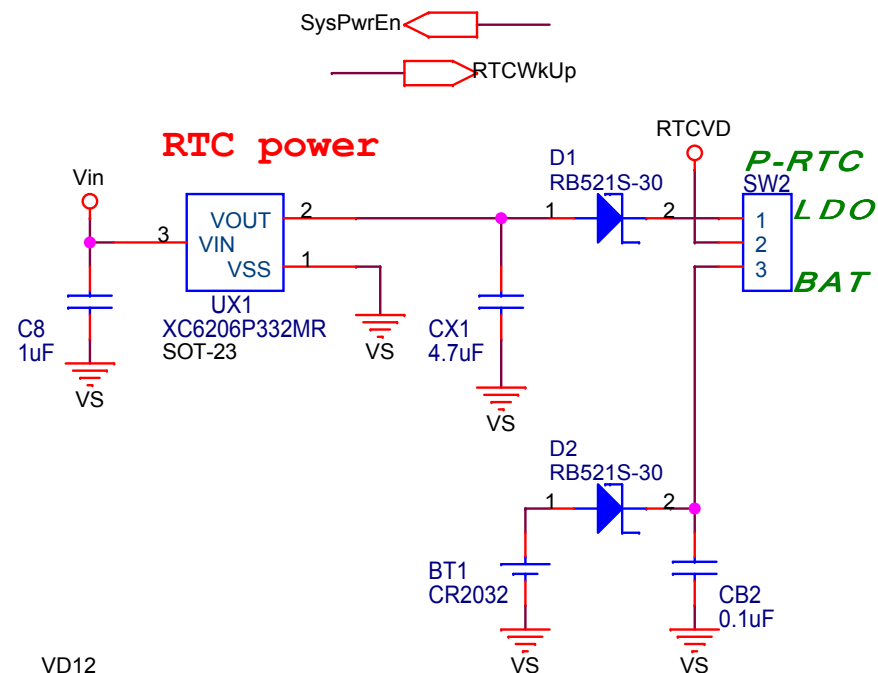
NHS-973-1-YS-1M51

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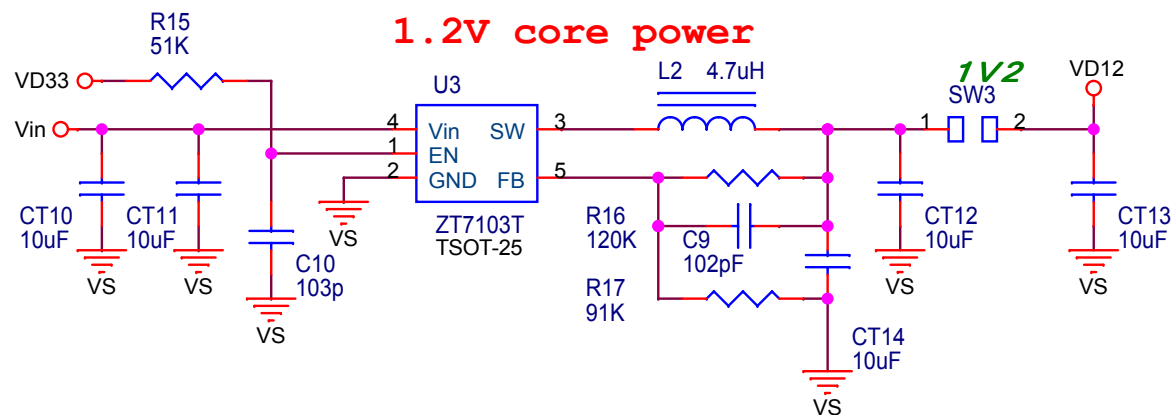
### 3.3V IO power



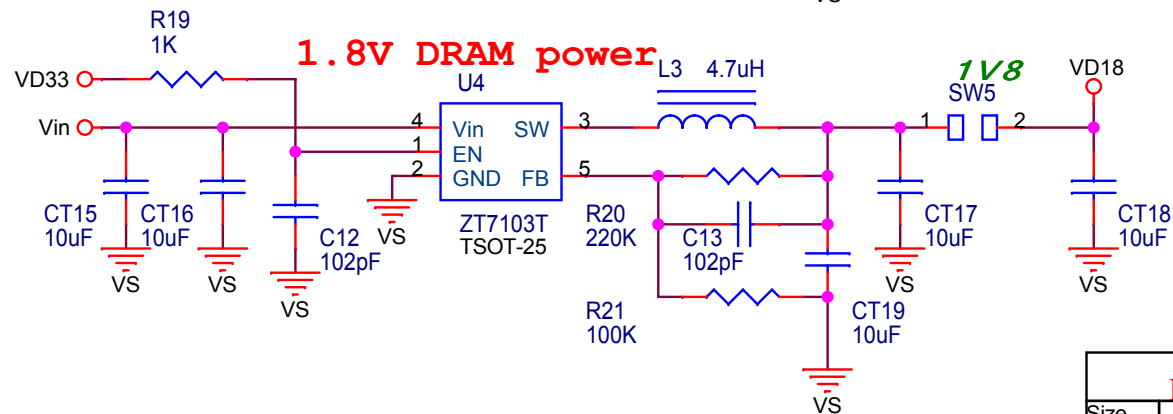
### RTC power



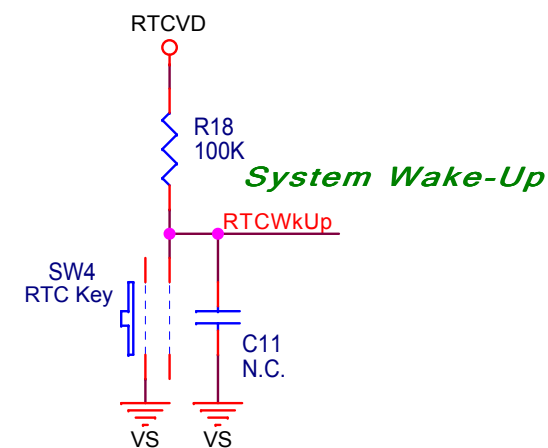
### 1.2V core power



### 1.8V DRAM power

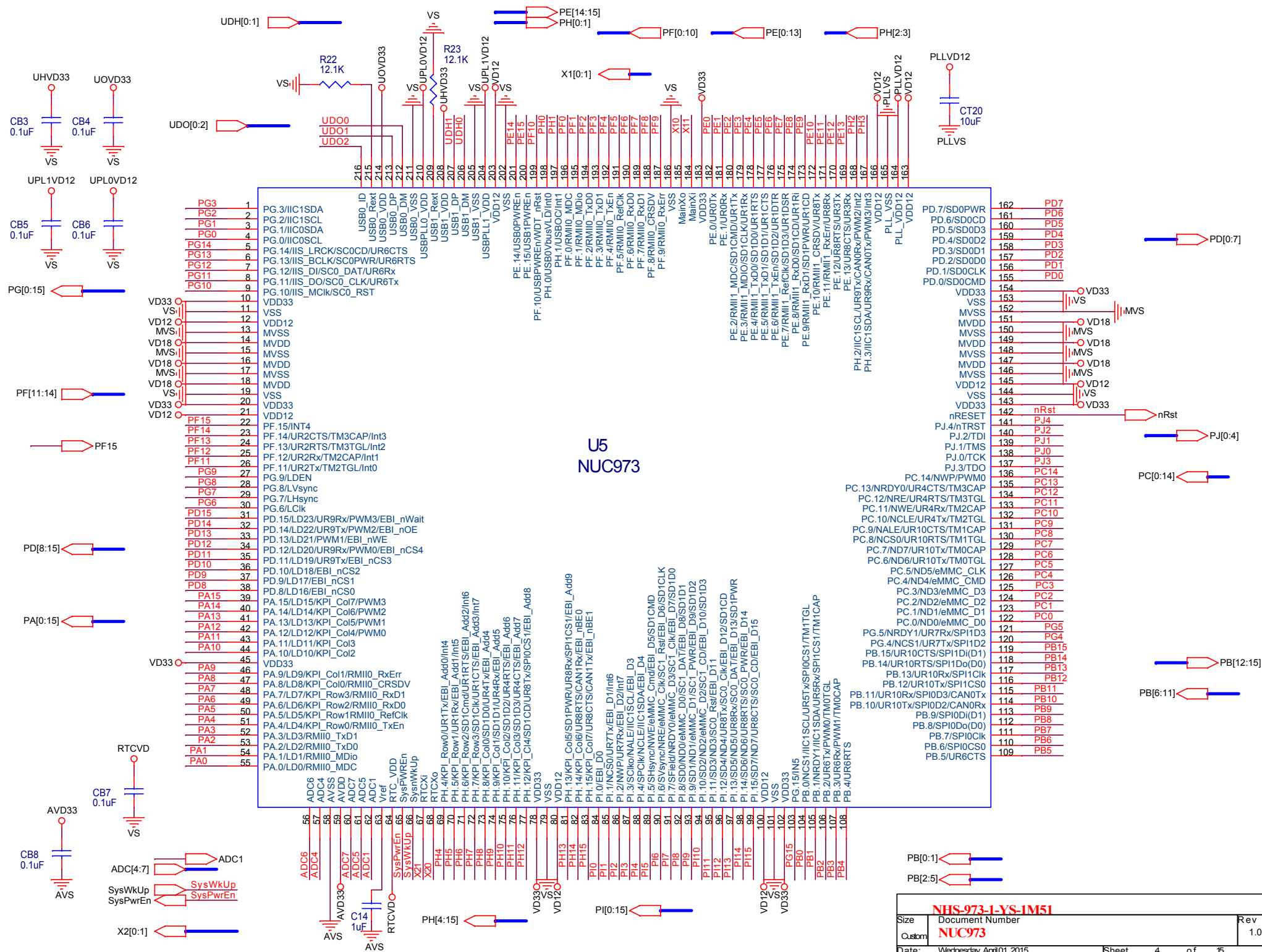


### Sys. Power Switch

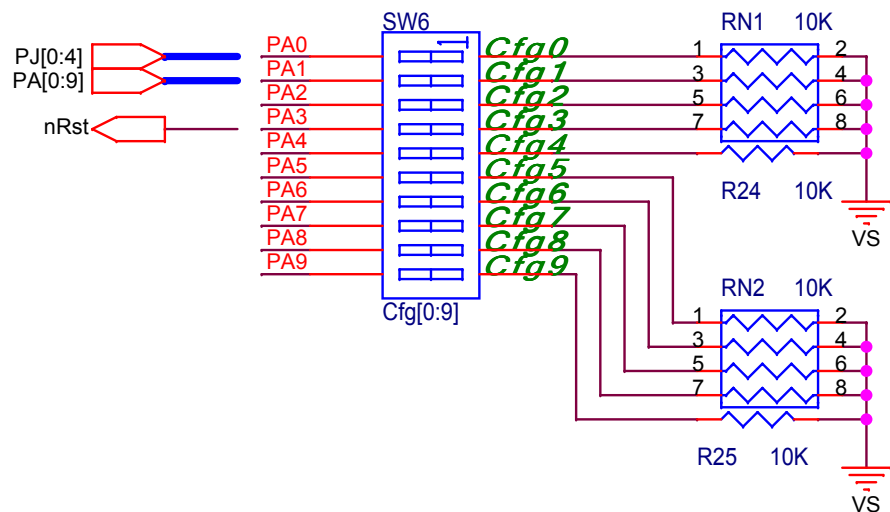


**NHS-973-1-YS-1M51**

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Custom	<b>System Power</b>	1.0
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## Power On Setting



Cfg1	Cfg0	Bootting from
ON	ON	USB.
ON	OFF	eMMC Flash.
OFF	ON	NAND Flash.
OFF	OFF	SPI Flash.

Cfg2	System Clock from
ON	12MHz crystal.
OFF	UPLL output.

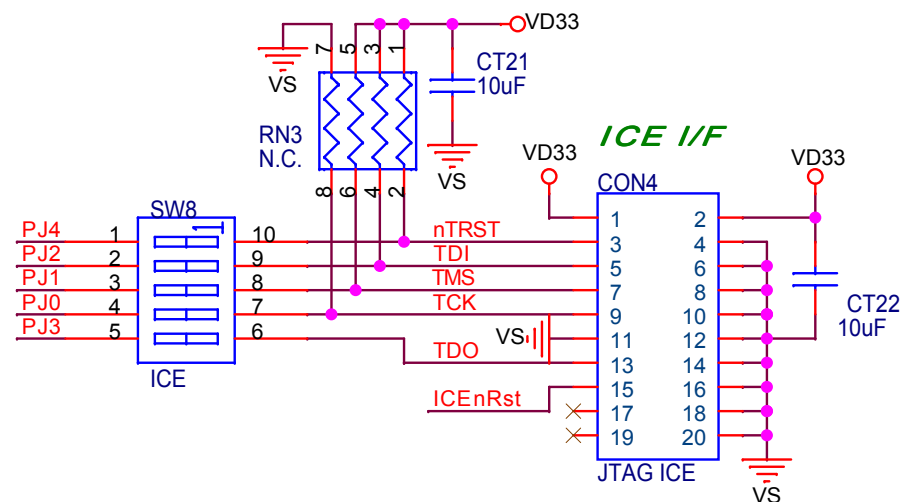
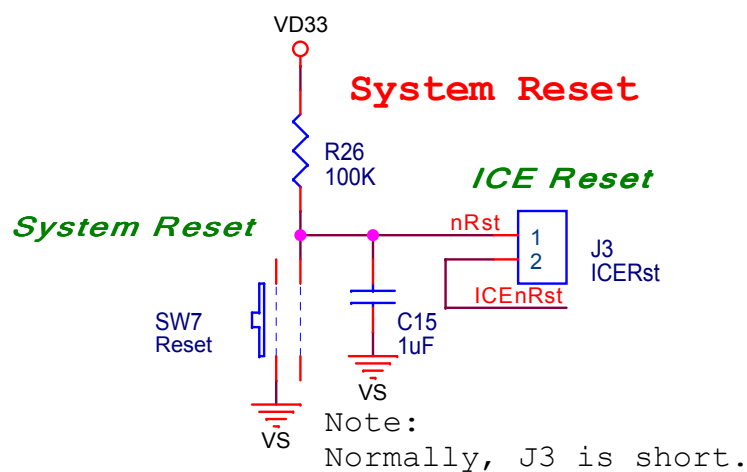
Cfg3	Watch Dog Setting
ON	OFF after power on.
OFF	ON after power on.

Cfg4	Setting PJ[4:0]
ON	As GPIO pin.
OFF	As JTAG interface.

Cfg5	UART Debuq Message
ON	Output turn ON.
OFF	Output turn OFF.

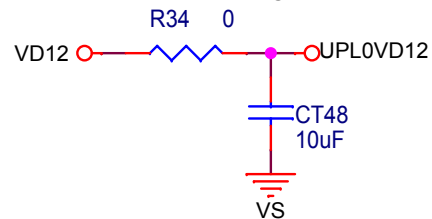
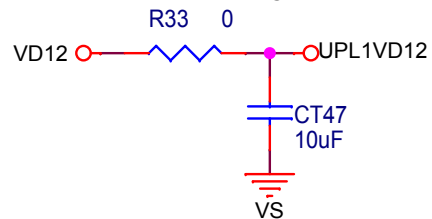
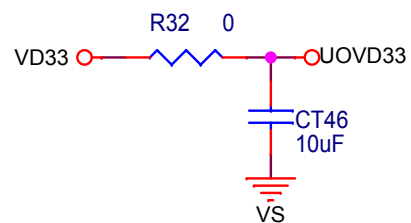
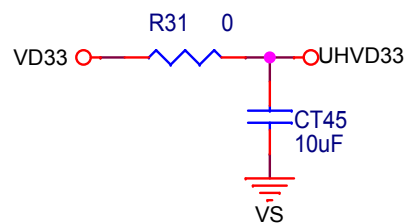
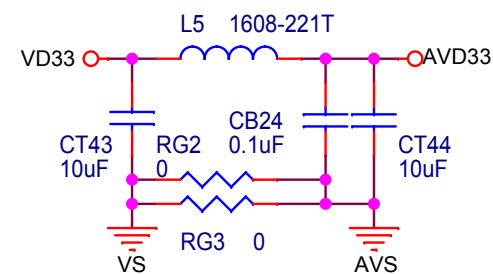
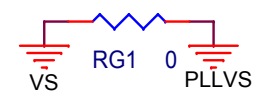
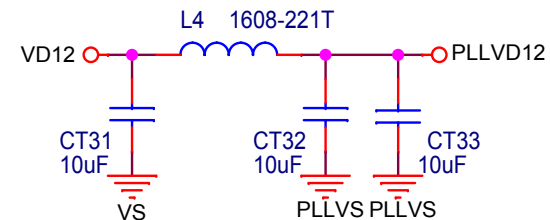
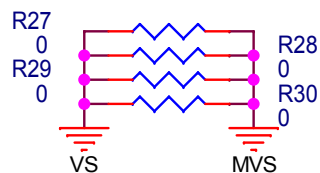
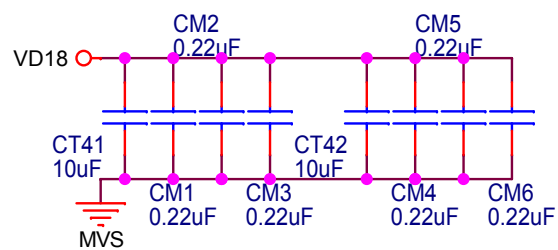
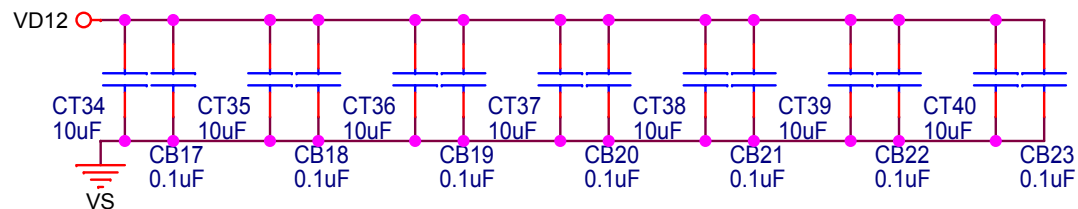
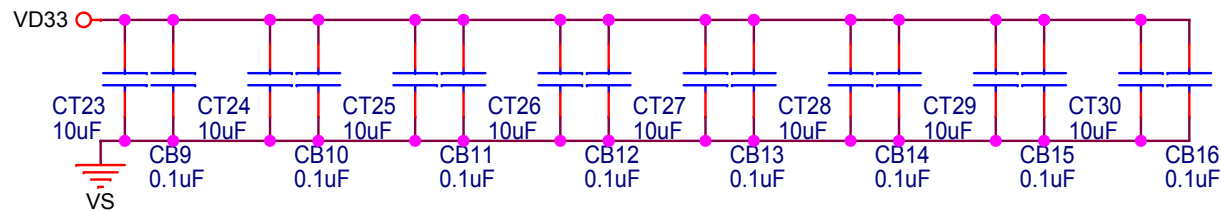
Cfg7	Cfg6	NAND Flash Page size
ON	ON	2KB
ON	OFF	4KB
OFF	ON	8KB
OFF	OFF	None

Cfg9	Cfg8	NAND Flash ECC type
ON	ON	BCH T12.
ON	OFF	BCH T15.
OFF	ON	BCH T24.
OFF	OFF	None



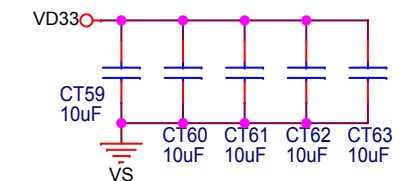
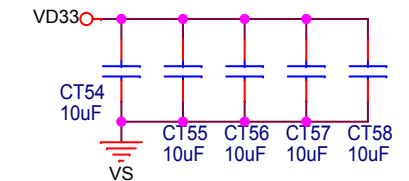
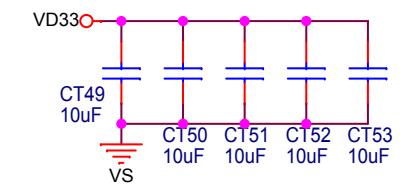
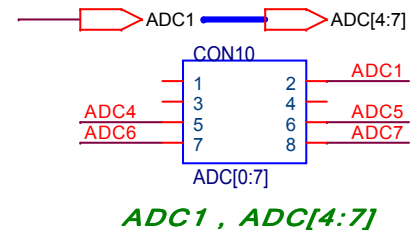
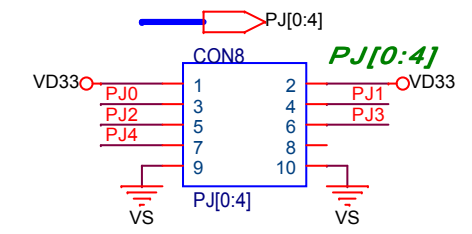
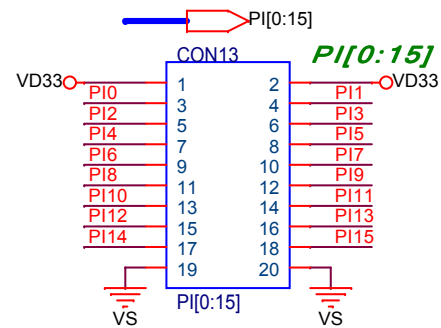
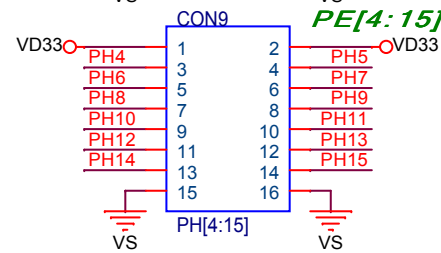
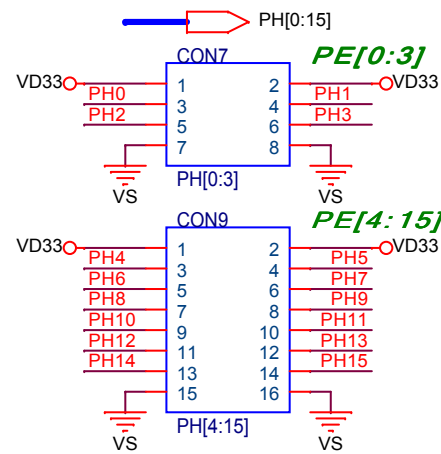
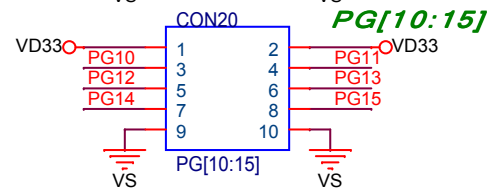
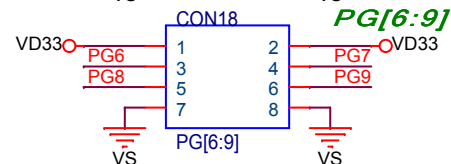
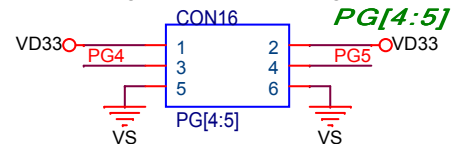
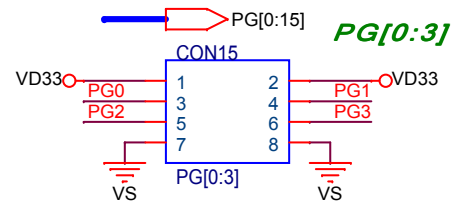
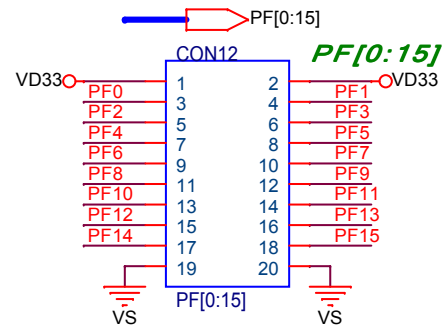
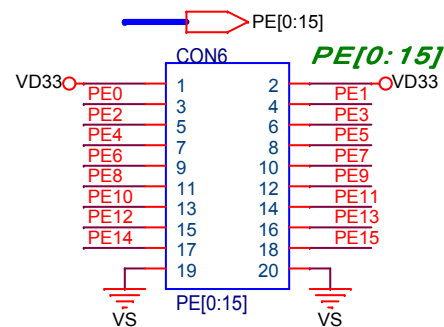
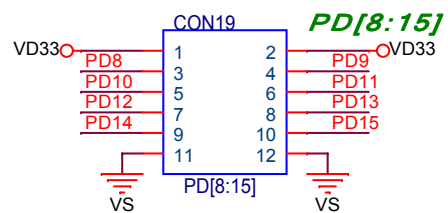
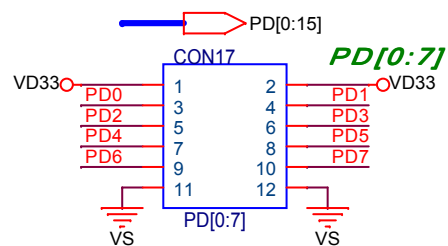
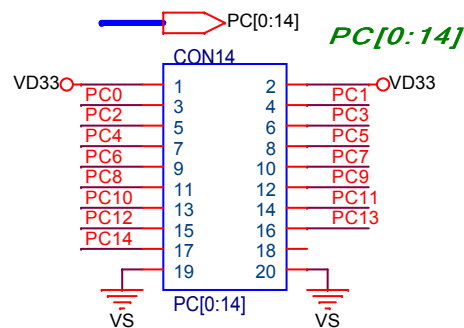
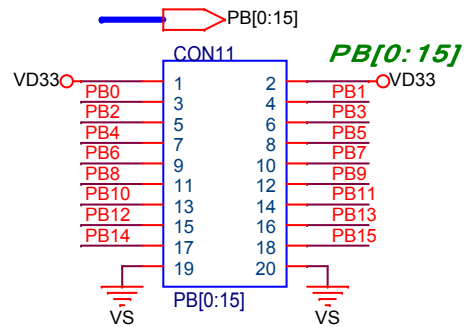
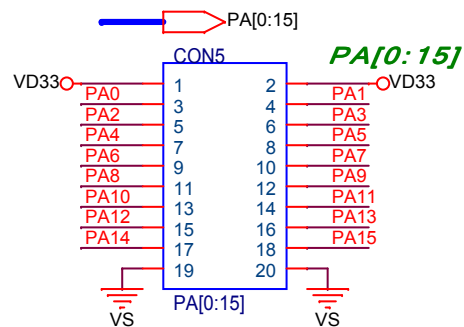
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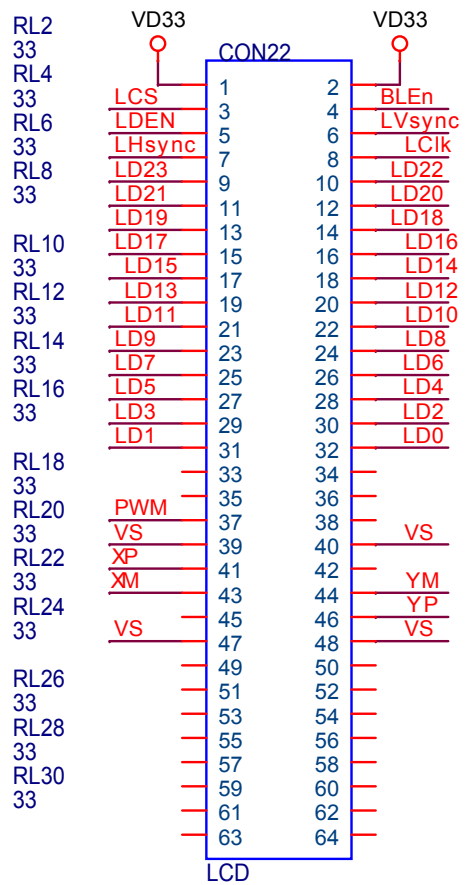
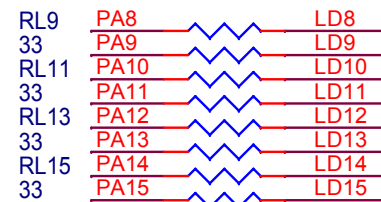
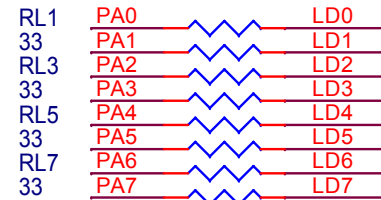
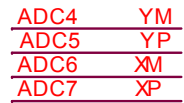
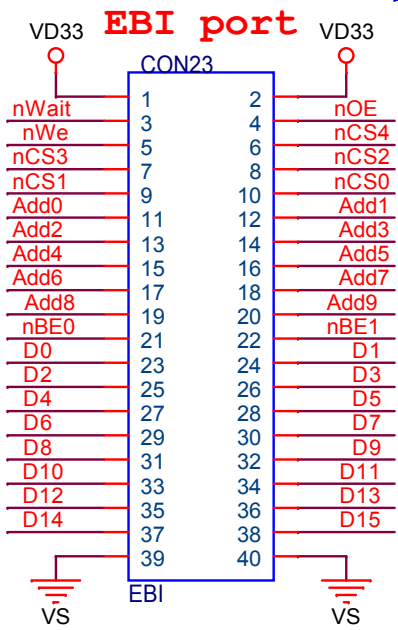
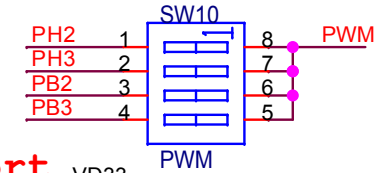
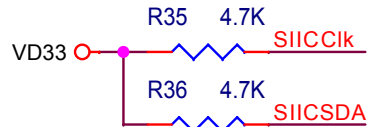
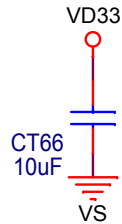
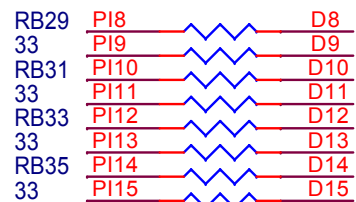
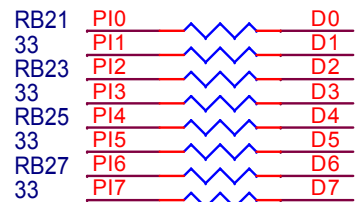
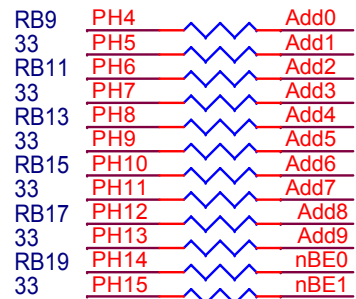
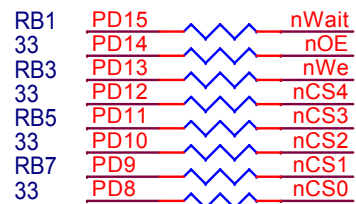
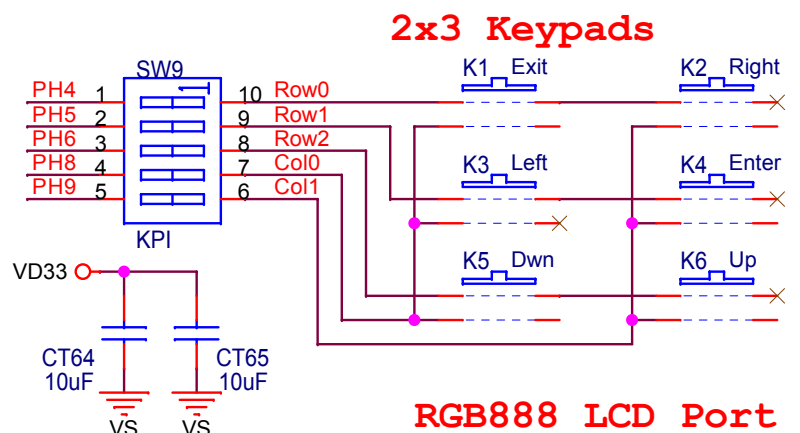
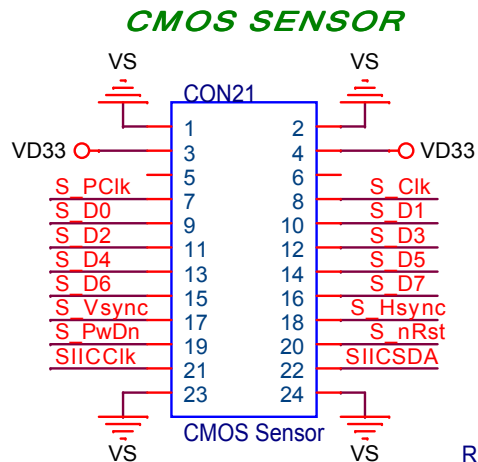
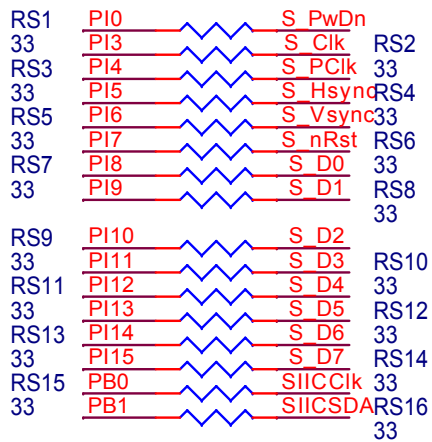
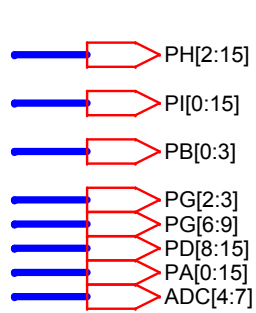


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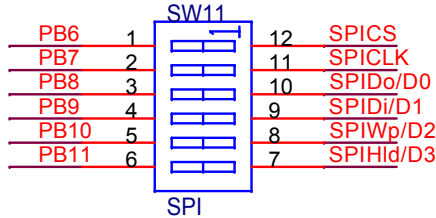
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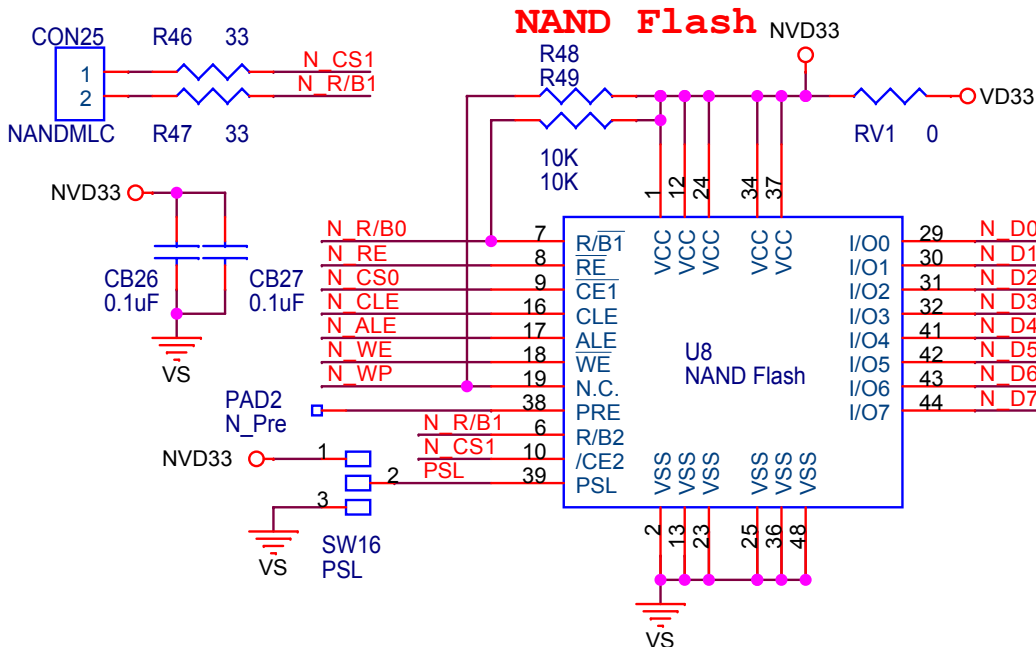
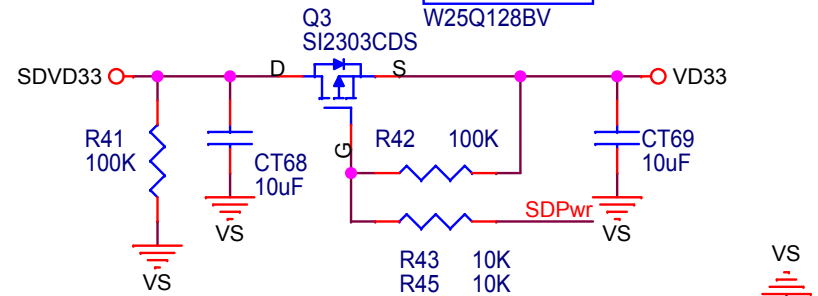
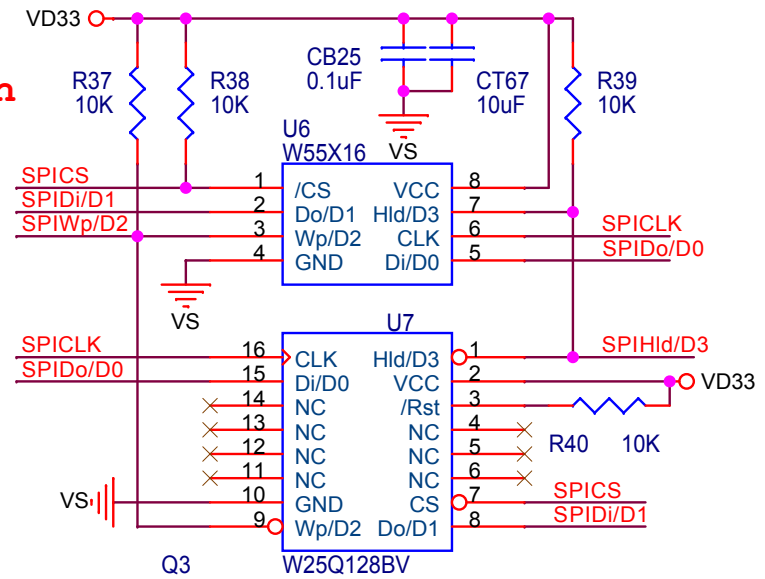
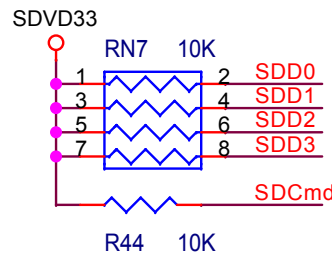
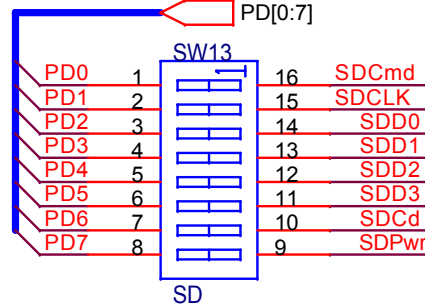
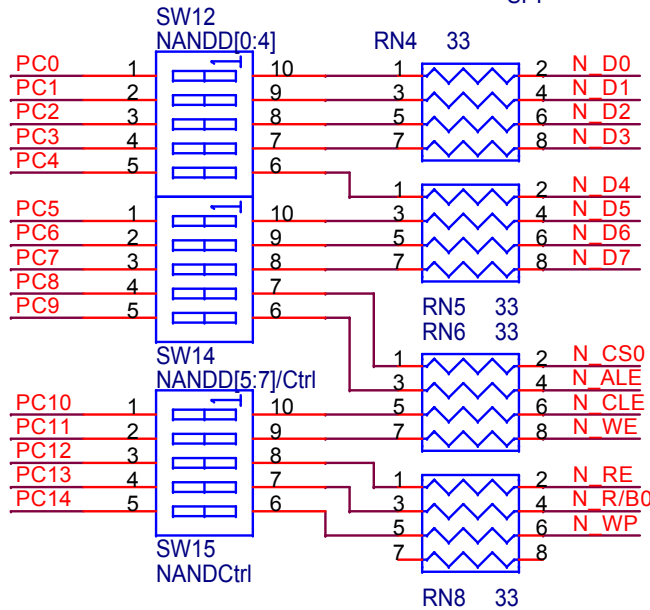
PB[6:11]

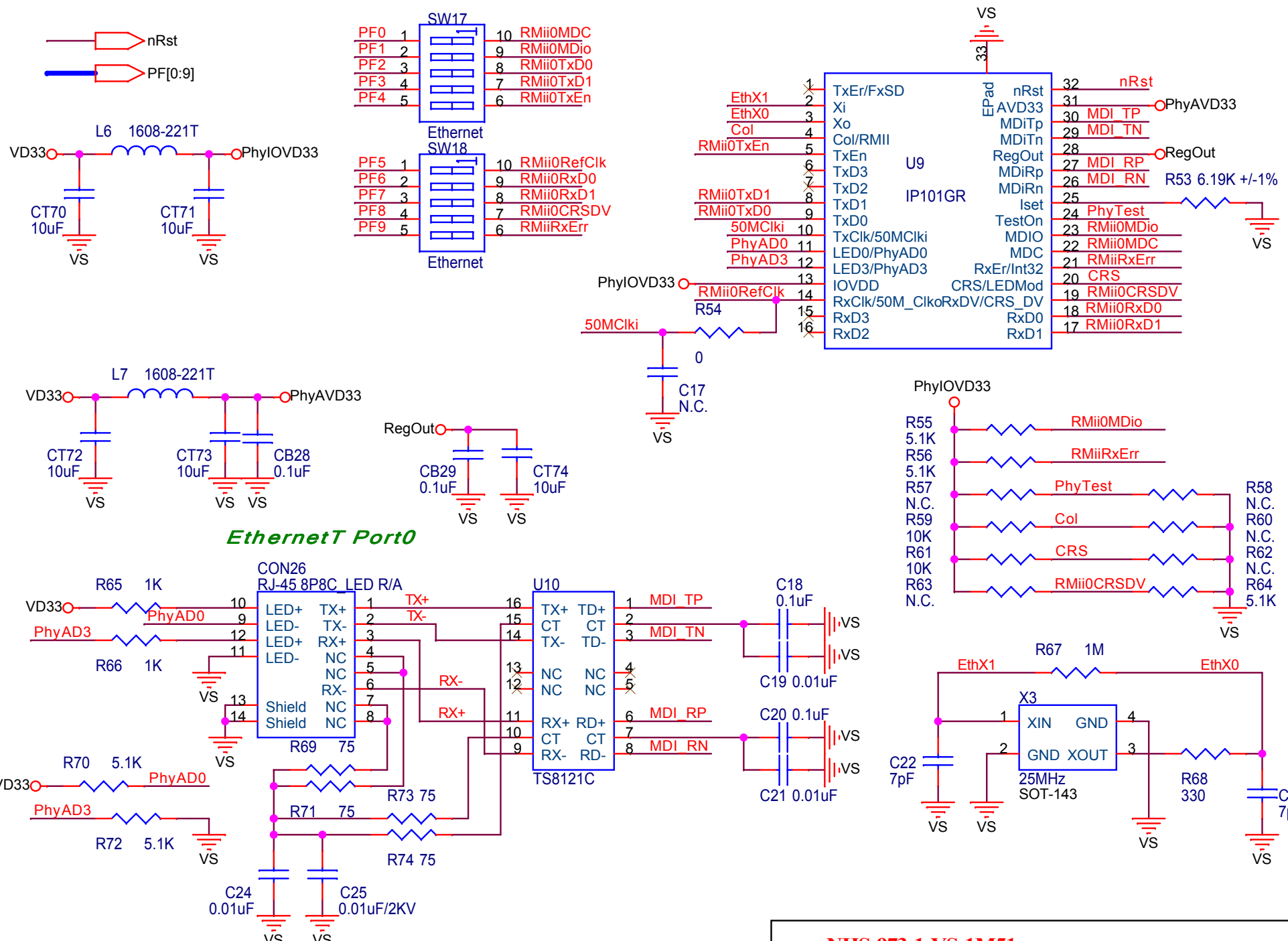
PC[0:14]



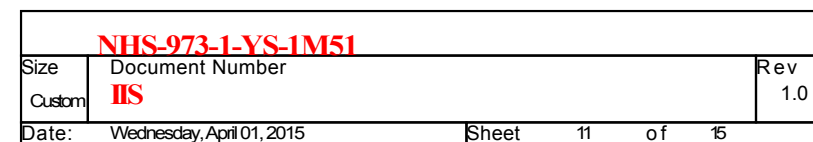
## SPI Falsh

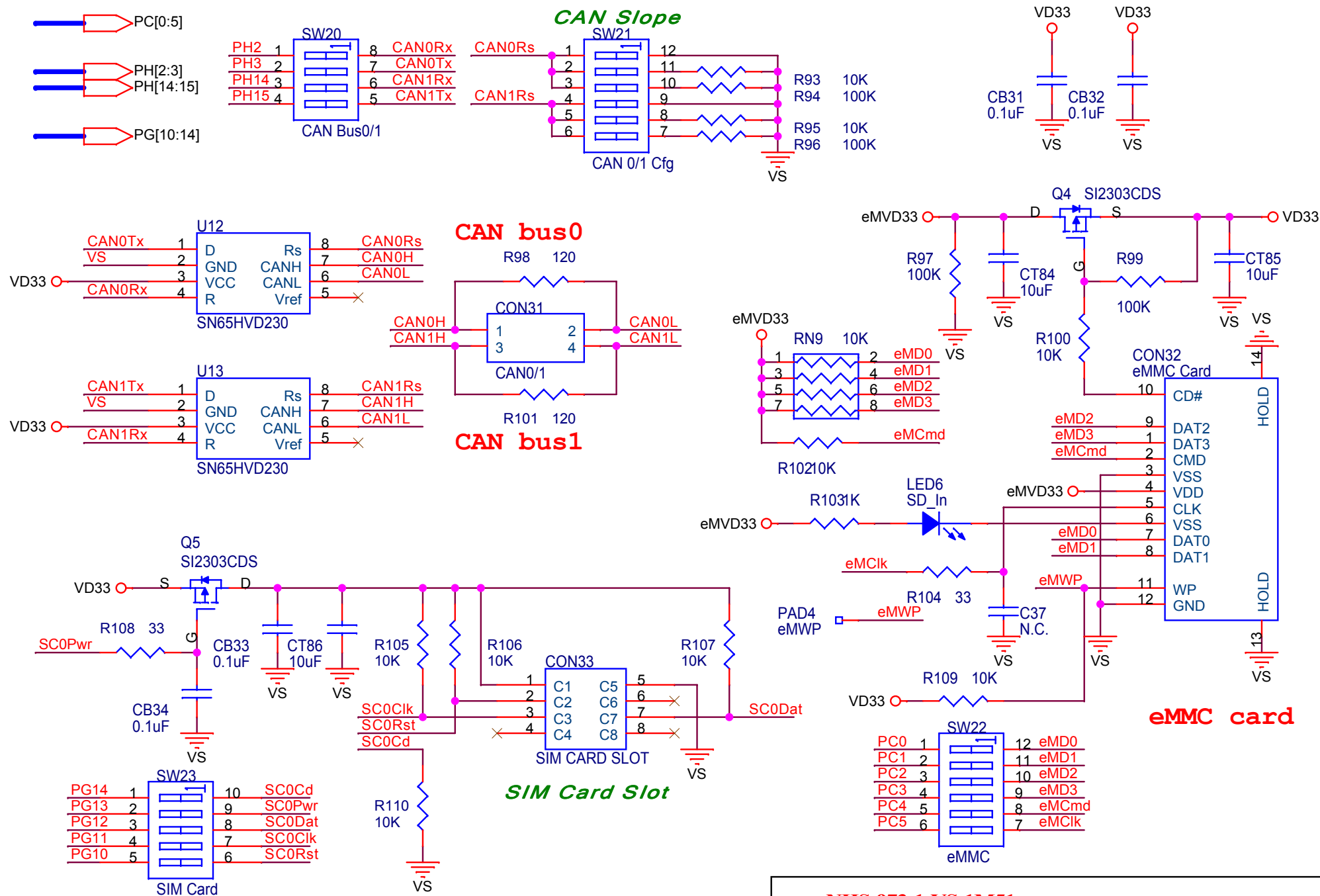
Note:  
Only one SPI  
Flash is used.





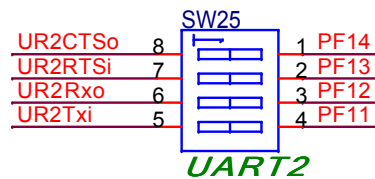
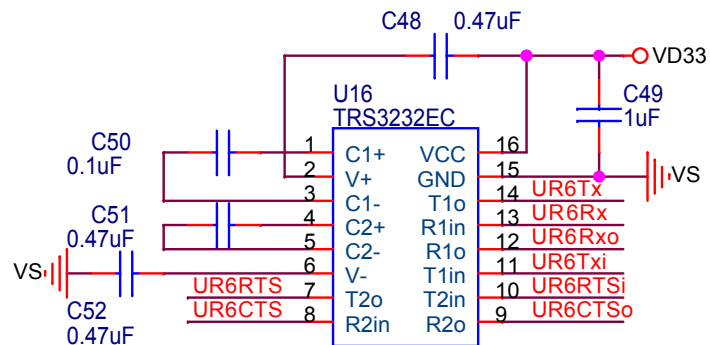
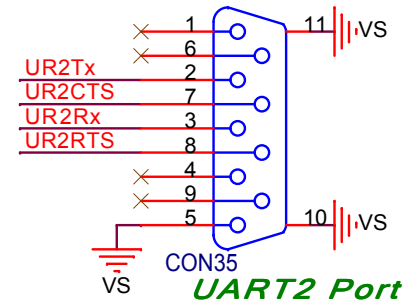
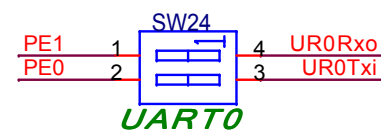
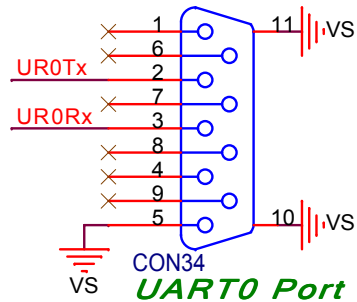
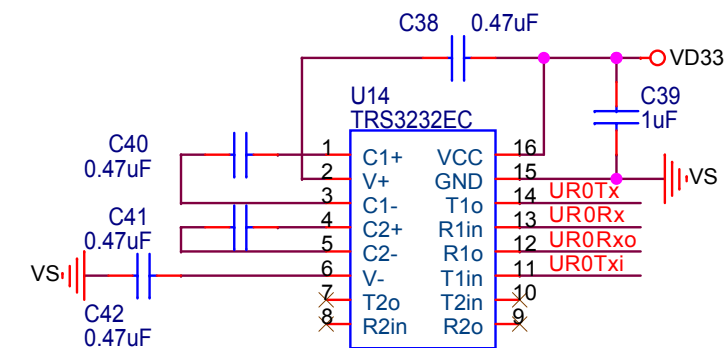
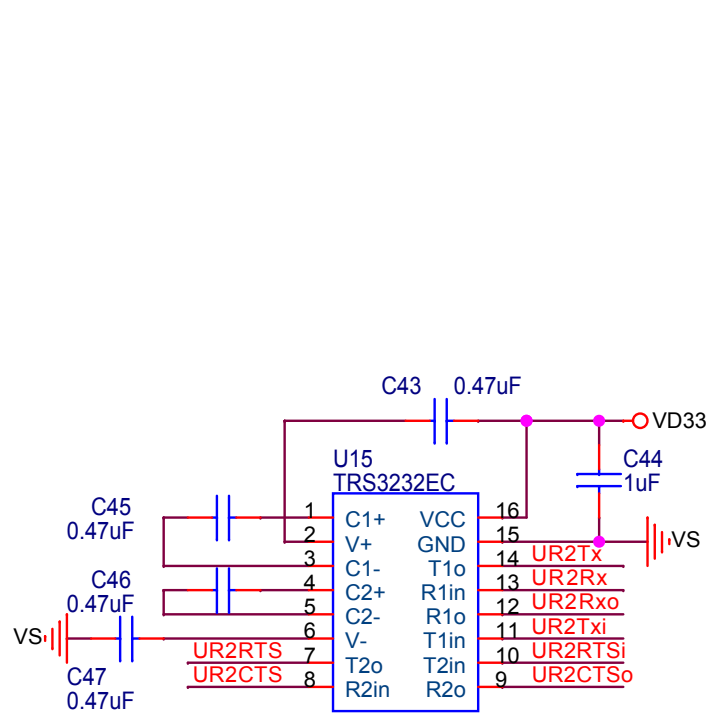
### EthernetT Port0





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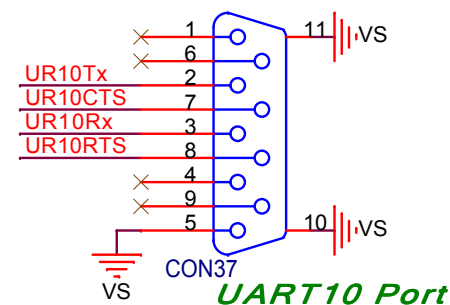
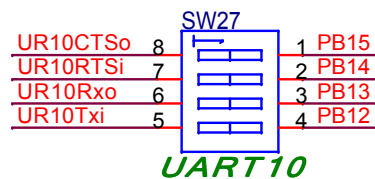
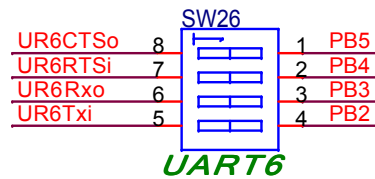
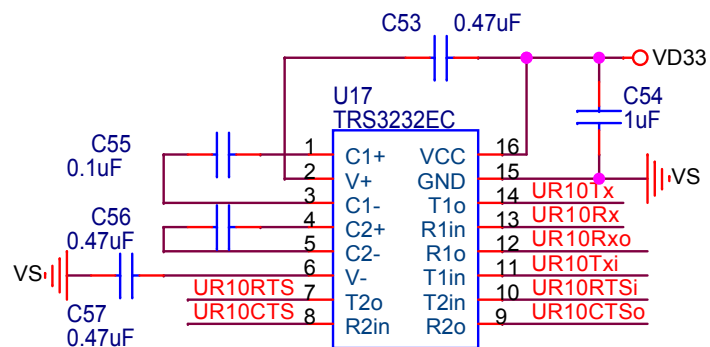
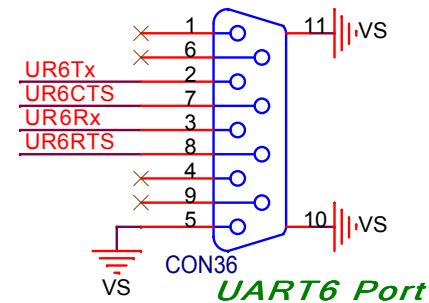


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