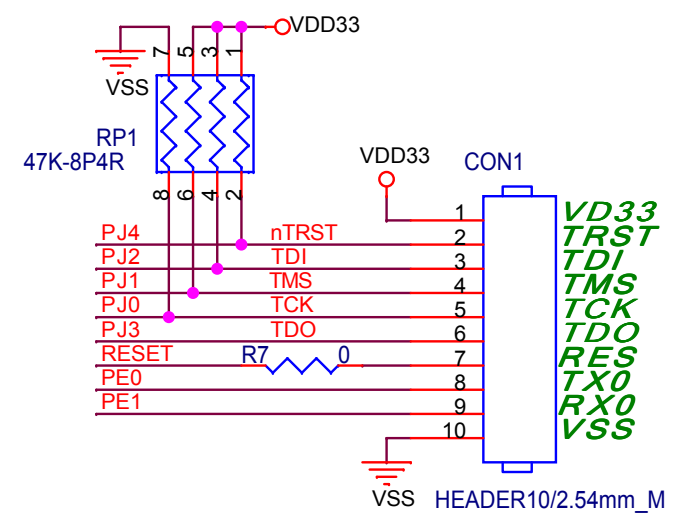
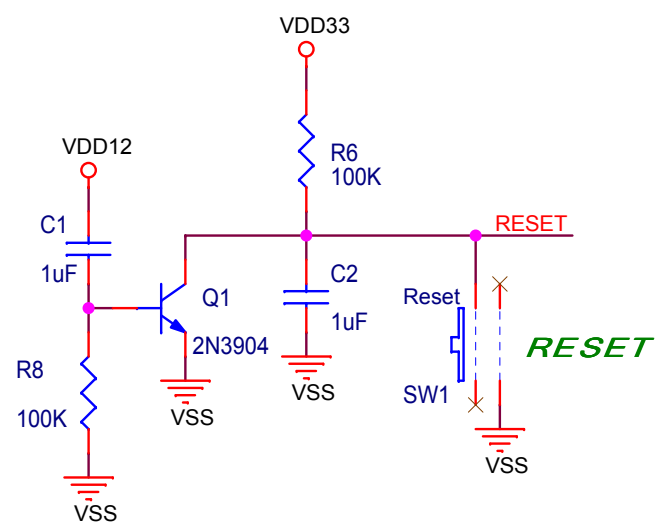
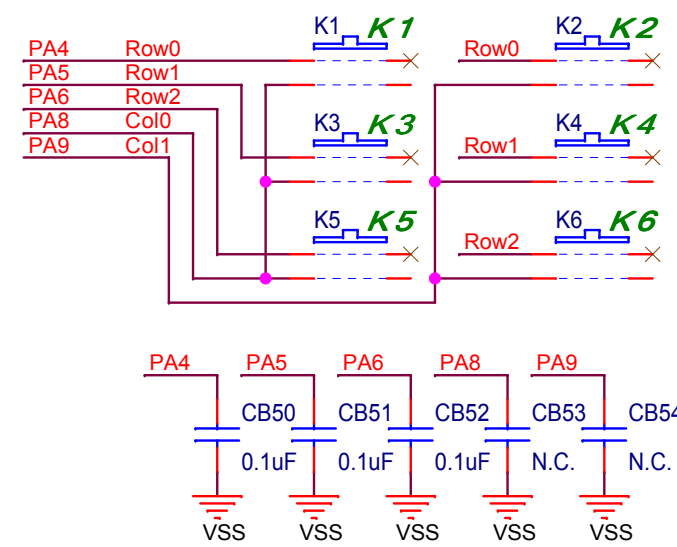
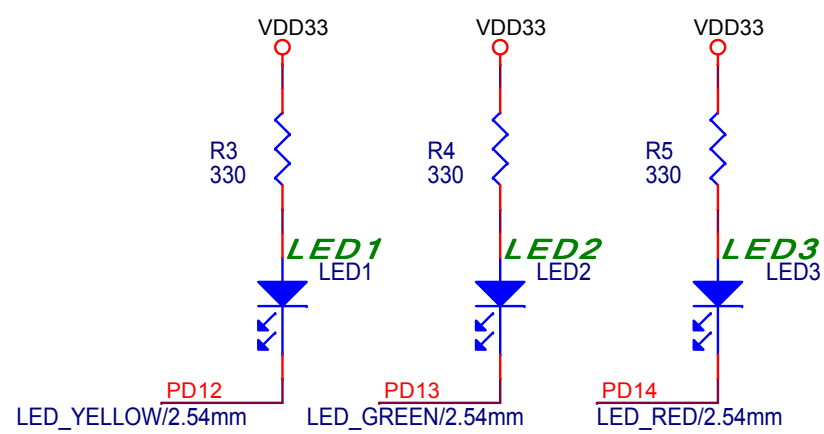
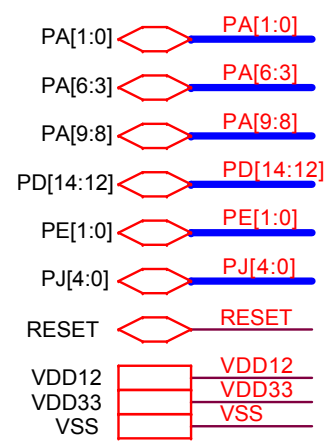
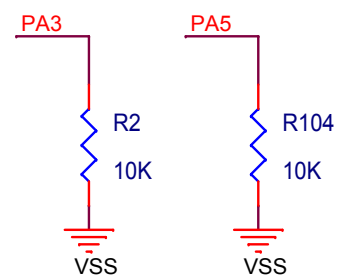
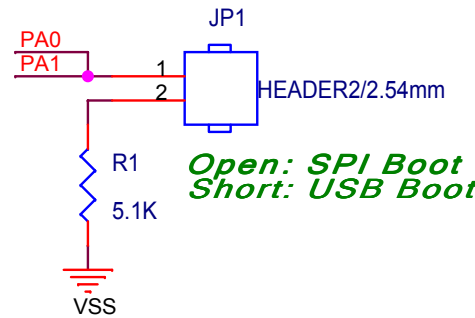
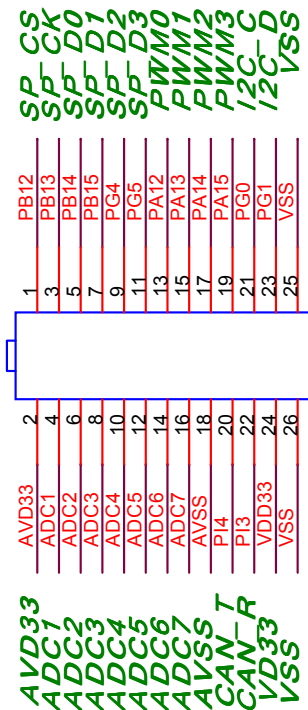
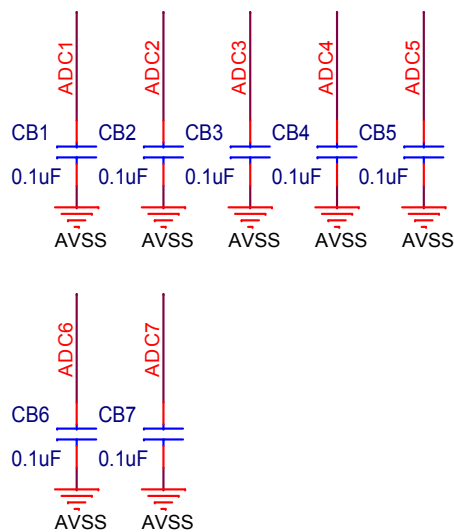
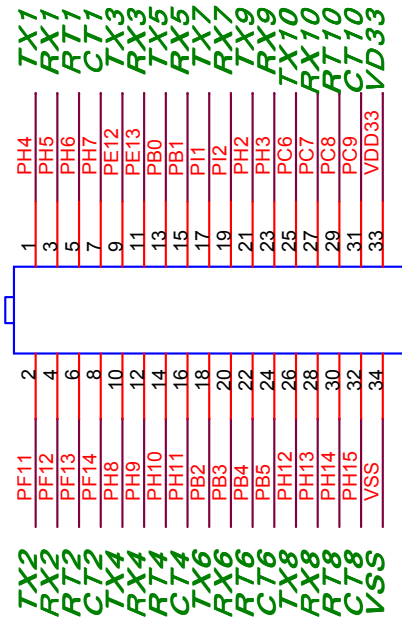


nuvoTon
NuDesign EtherU
P/N: NK-UAR2ETH-U V1.0

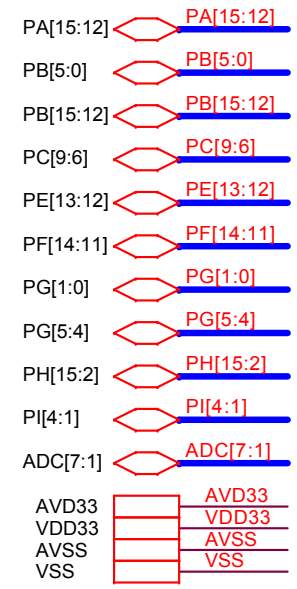


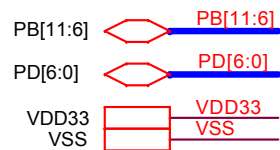


J2
HEADER13X2/2.54mm_M



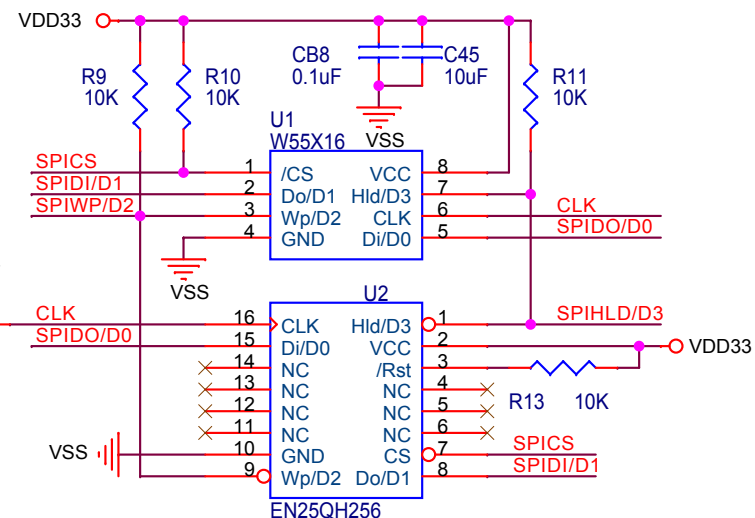
J1
HEADER17X2/2.54mm_F



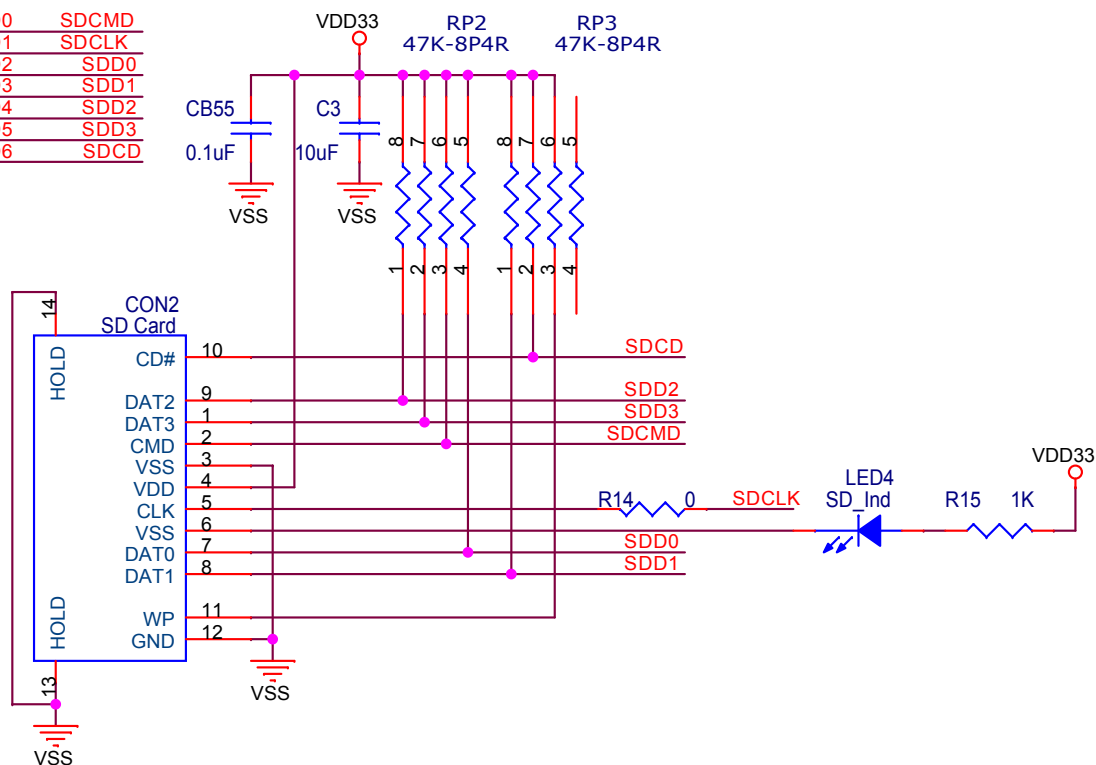


PB6	SPICS
PB7	SPICLK
PB8	SPIDO/D0
PB9	SPIDI/D1
PB10	SPIWP/D2
PB11	SPIHLD/D3

Note:
Only one SPI
Flash is used.

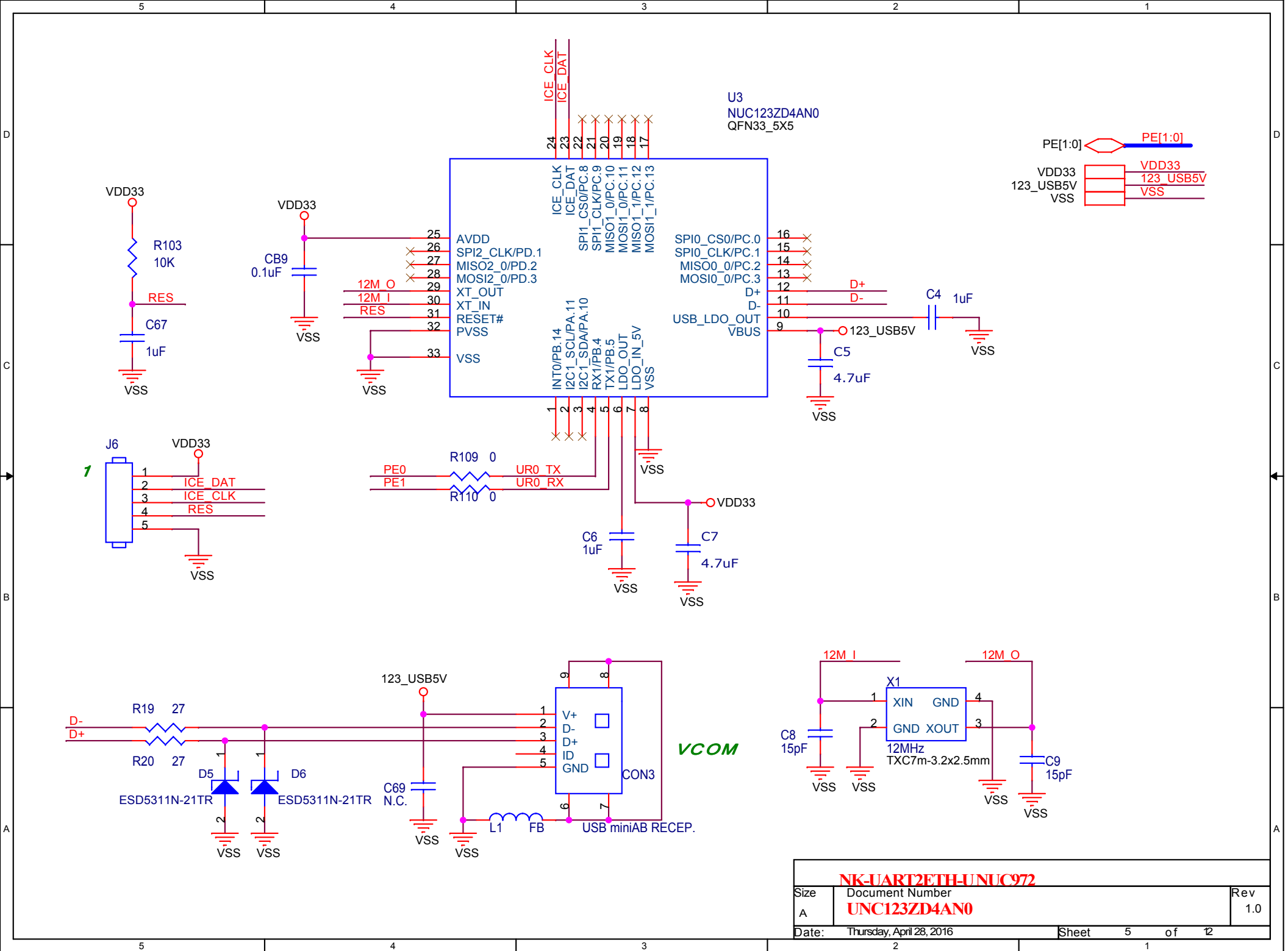


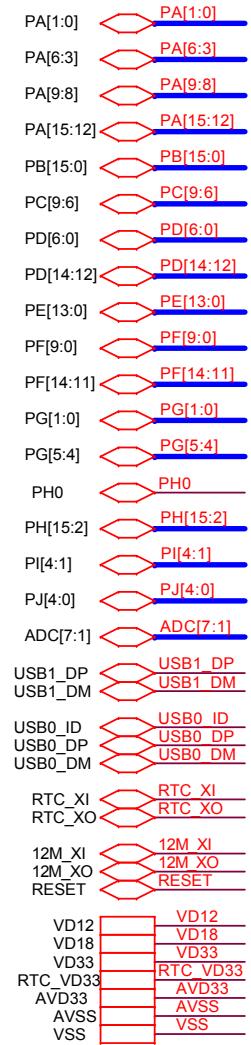
PD0	SDCMD
PD1	SDCLK
PD2	SDD0
PD3	SDD1
PD4	SDD2
PD5	SDD3
PD6	SDCD

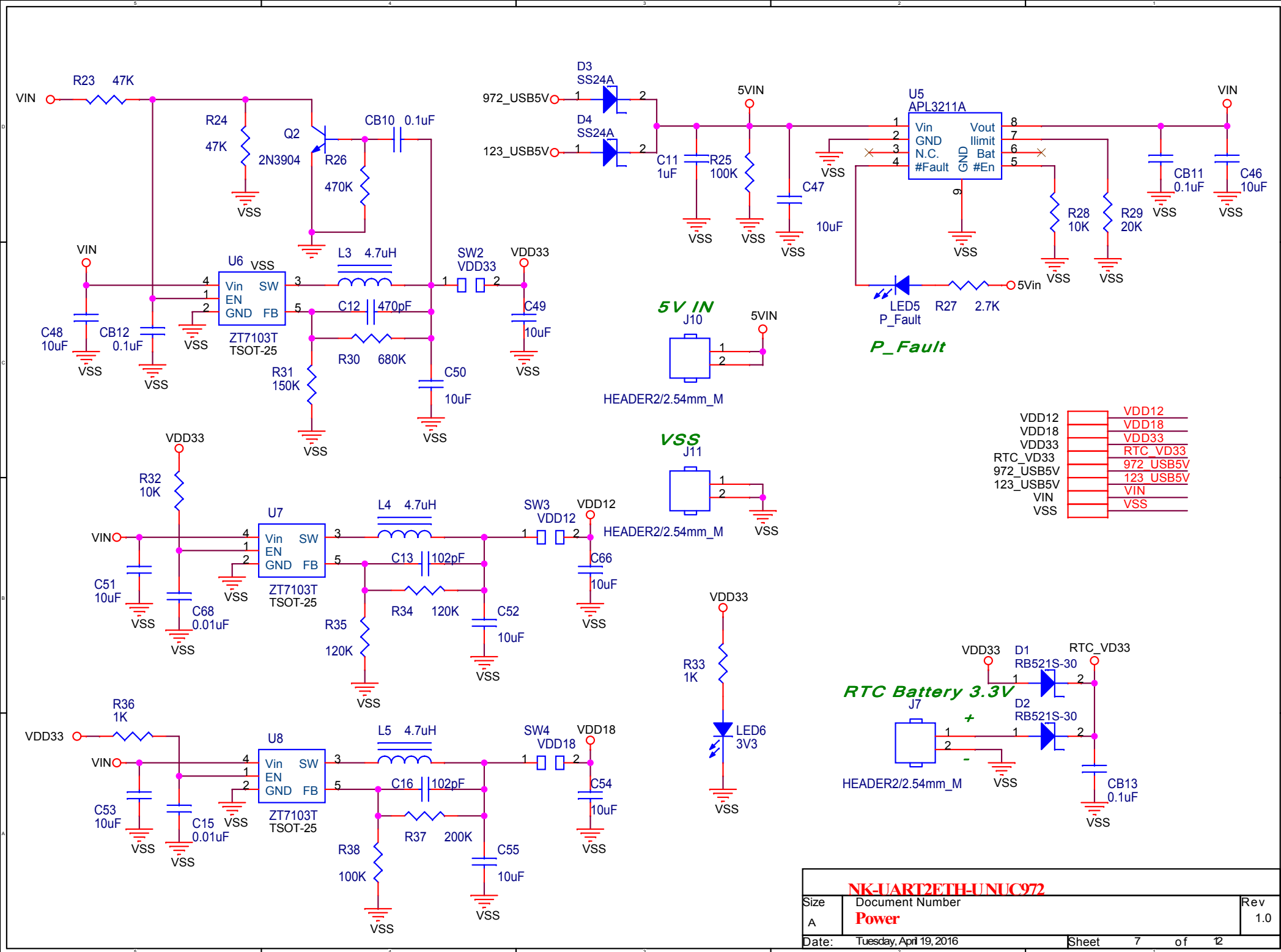


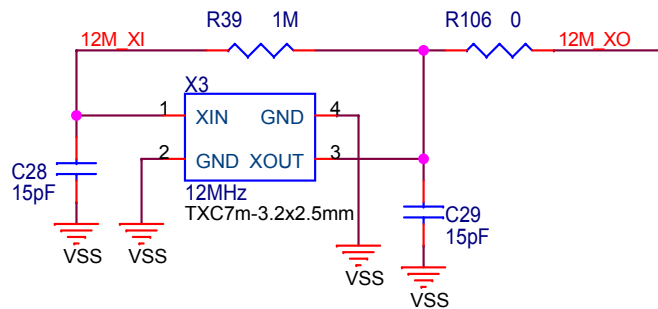
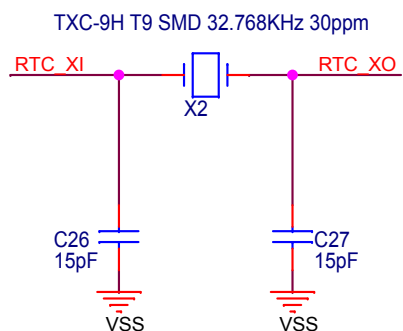
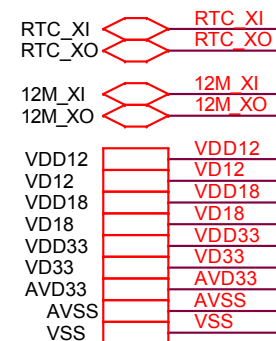
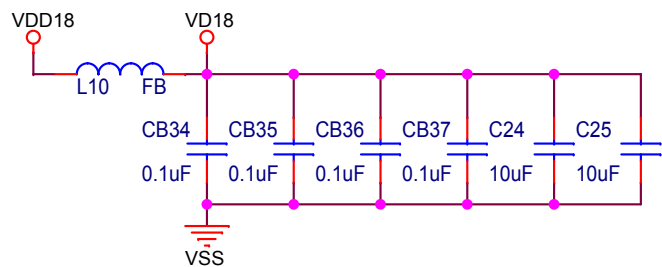
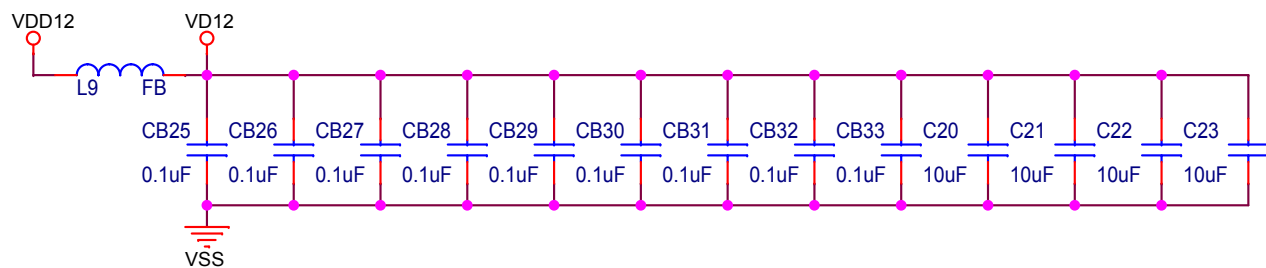
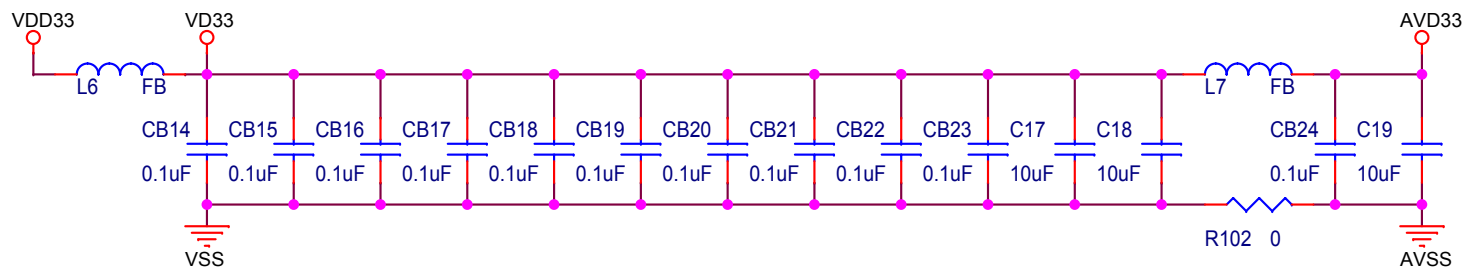
NK-UART2ETH-UNUC972

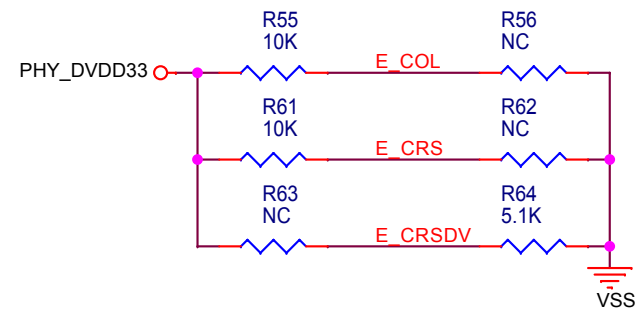
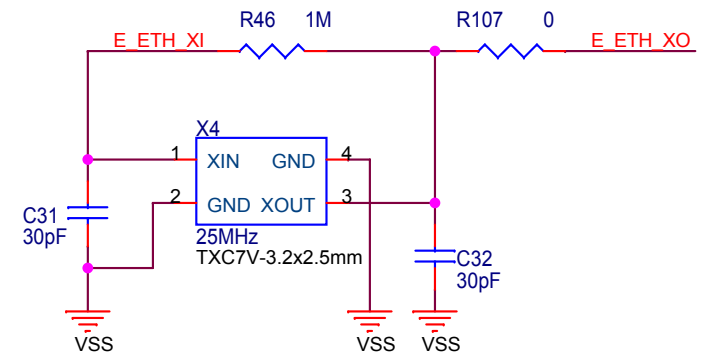
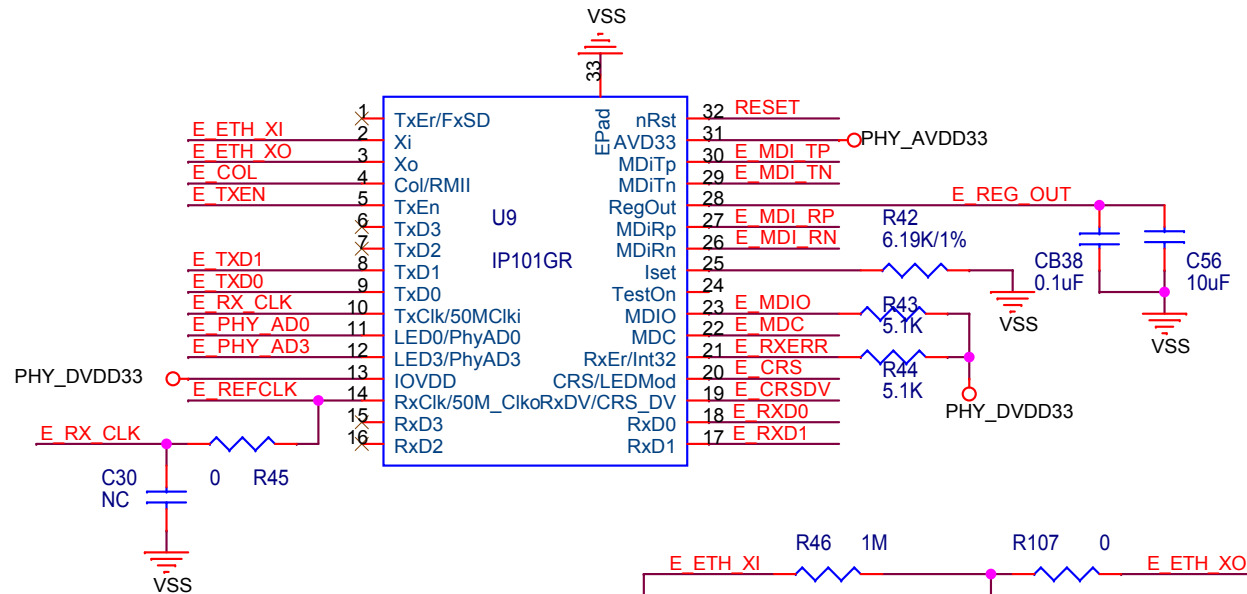
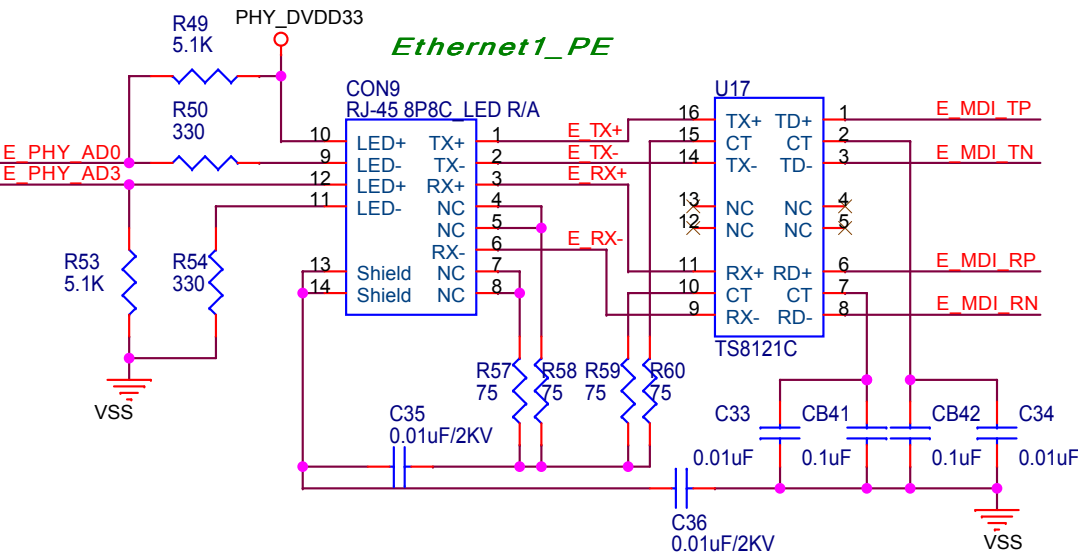
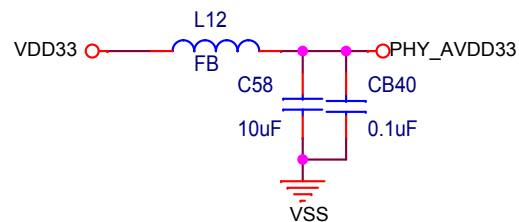
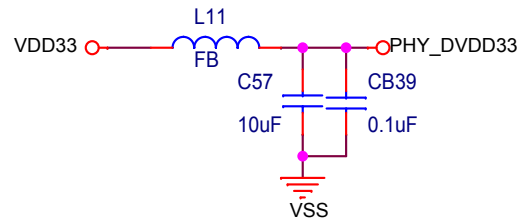
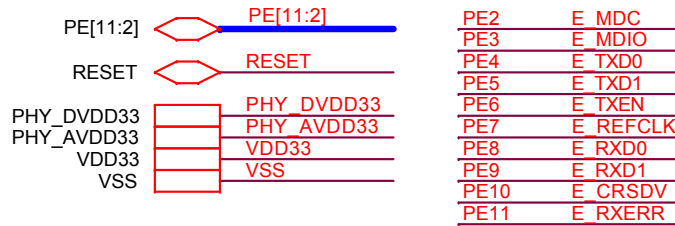
Size	Document Number	Rev
A	Memory	1.0
Date:	Monday, July 04, 2016	Sheet 4 of 12



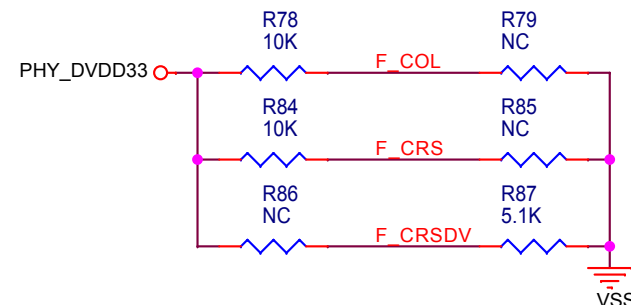
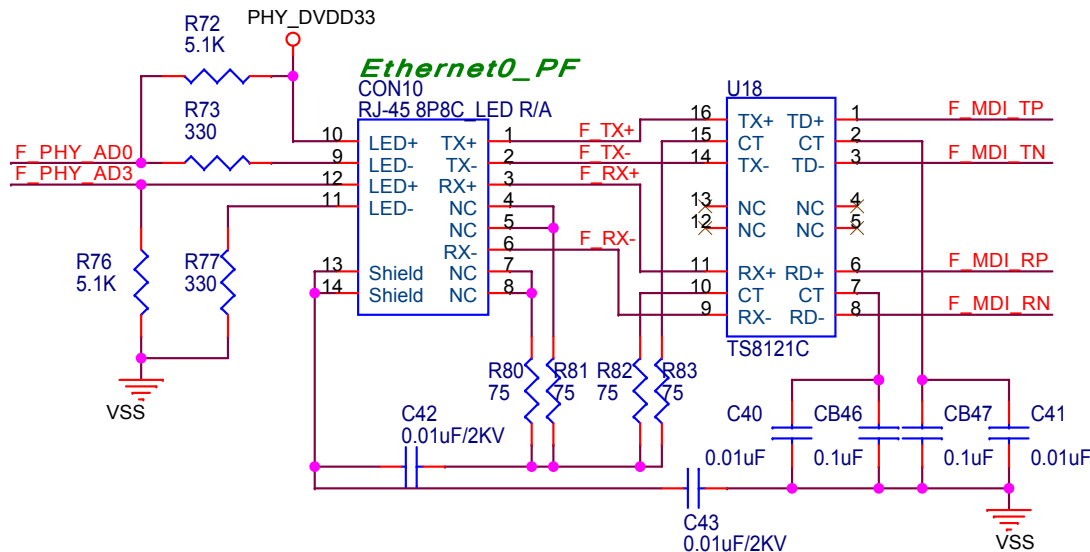
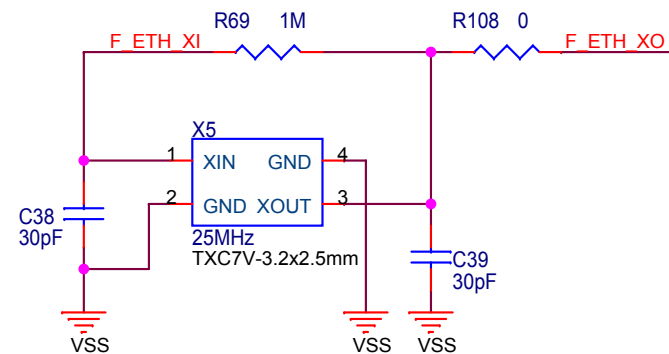
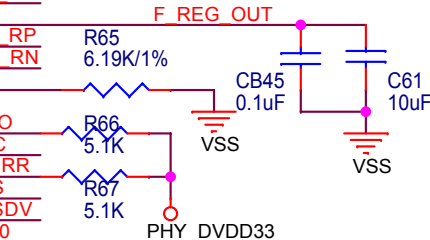
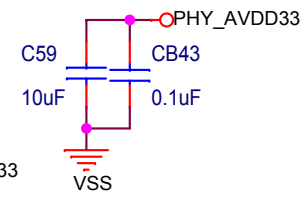
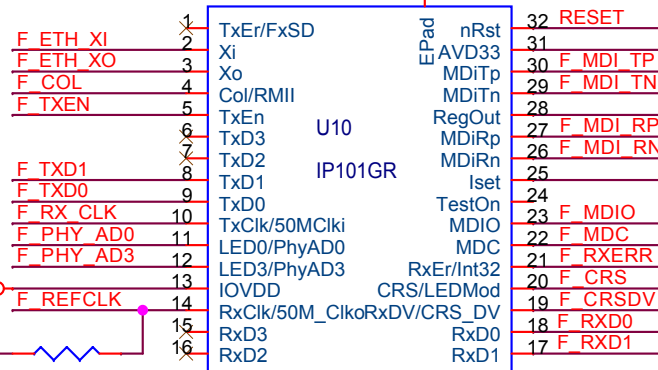
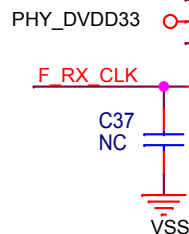
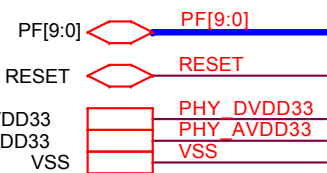
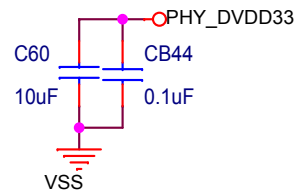


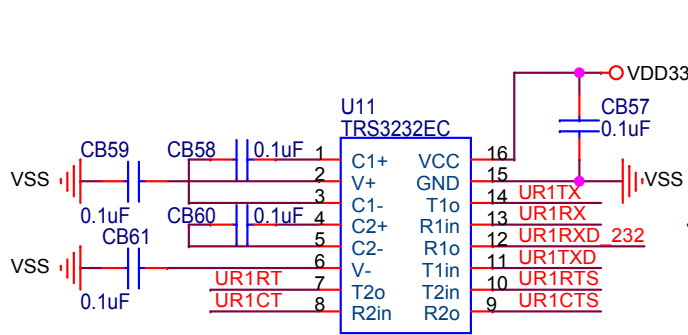




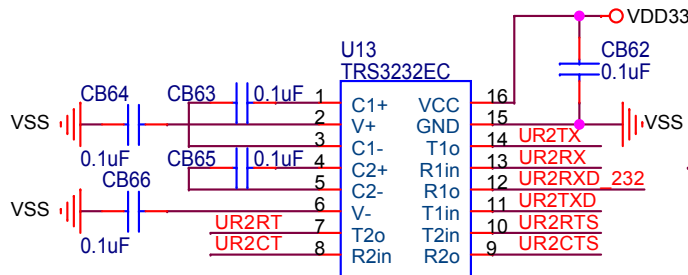
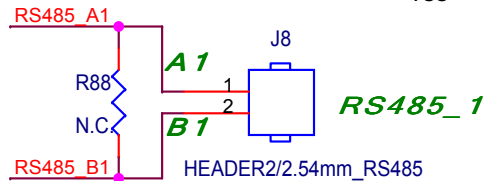
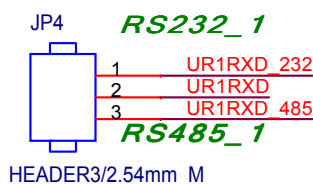
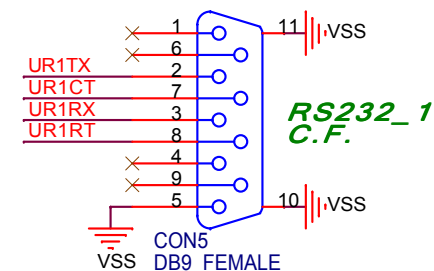
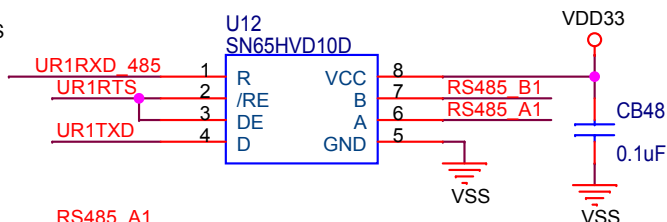


PF0	F MDC
PF1	F MDIO
PF2	F TXD0
PF3	F TXD1
PF4	F TXEN
PF5	F REFCLK
PF6	F RXD0
PF7	F RXD1
PF8	F CRSDV
PF9	F RXERR





UR1CTS PH7
UR1RTS PH6
UR1RXD PH5
UR1TXD PH4



UR2CTS PF14
UR2RTS PF13
UR2RXD PF12
UR2TXD PF11

