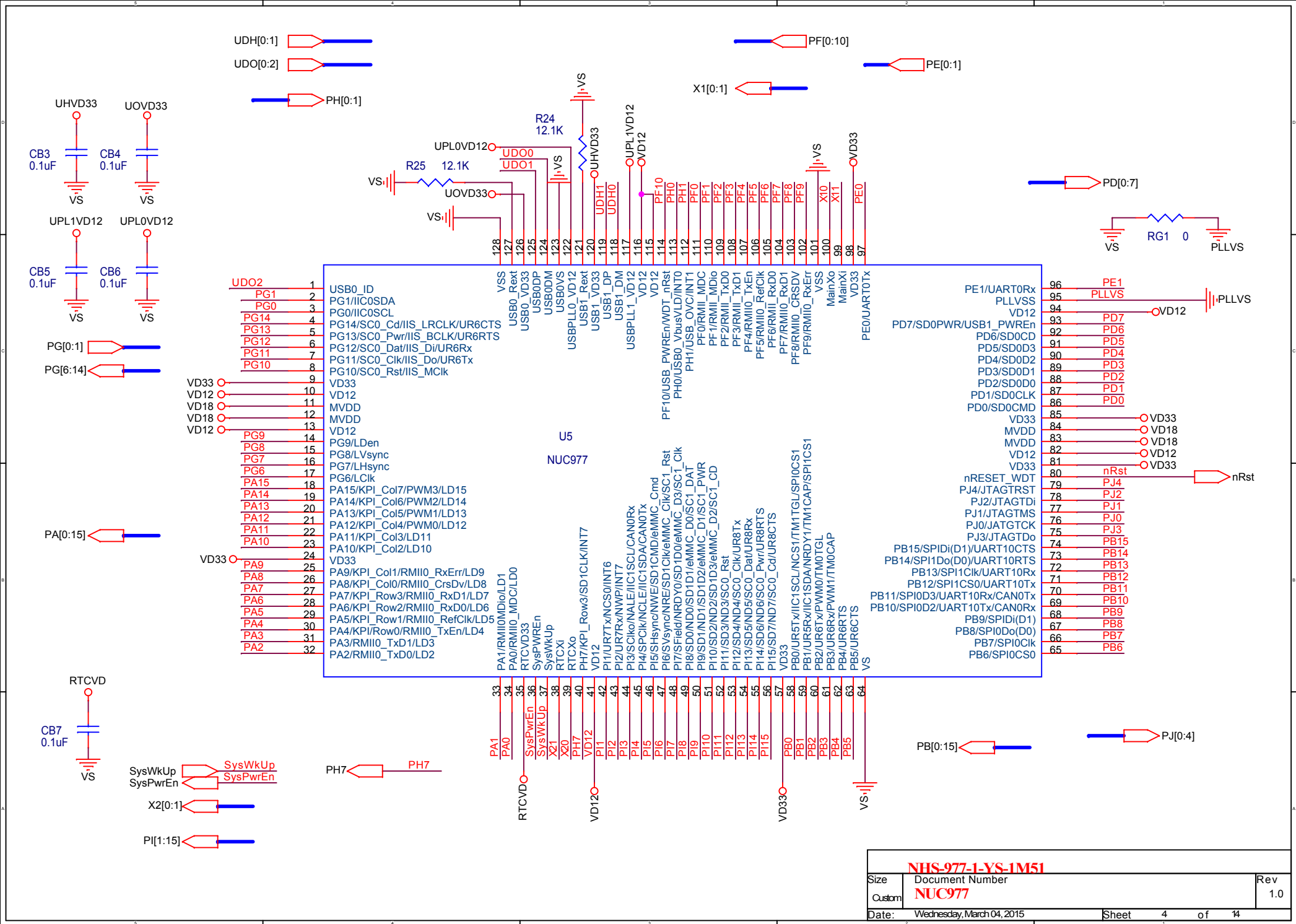
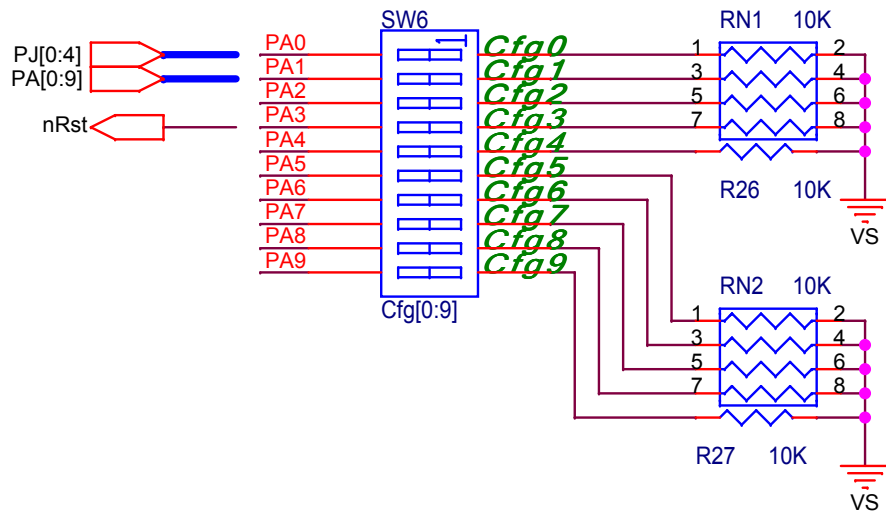


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Power On Setting



Cfg1	Cfg0	Bootting from
ON	ON	USB.
ON	OFF	eMMC Flash.
OFF	ON	NAND Flash.
OFF	OFF	SPI Flash.

Cfg2	System Clock from
ON	12MHz crystal.
OFF	UPLL output.

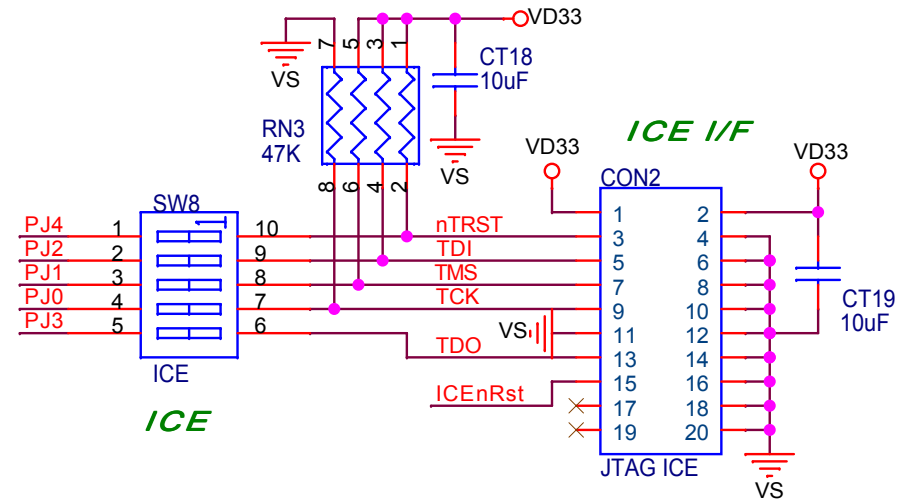
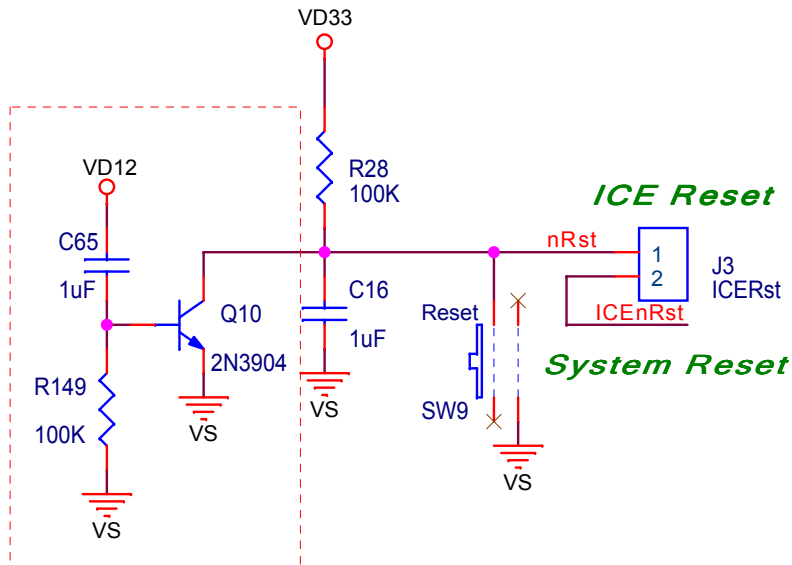
Cfg3	Watch Dog Setting
ON	OFF after power on.
OFF	ON after power on.

Cfg4	Setting PJ[4:0]
ON	As GPIO pin.
OFF	As JTAG interface.

Cfg5	UART Debug Message
ON	Output turn ON.
OFF	Output turn OFF.

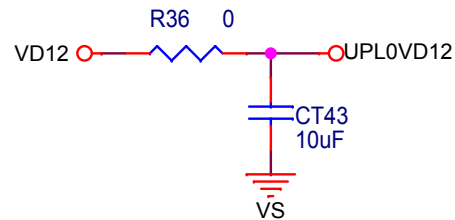
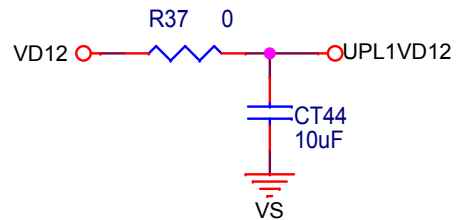
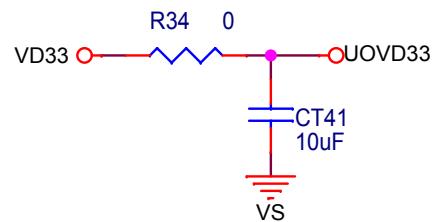
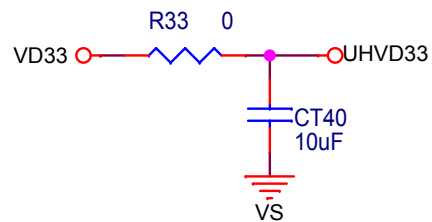
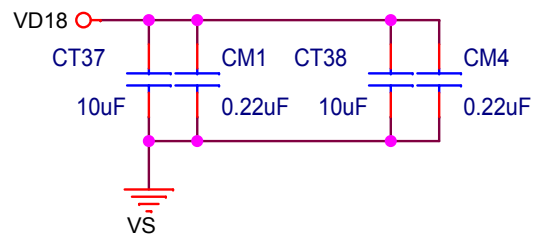
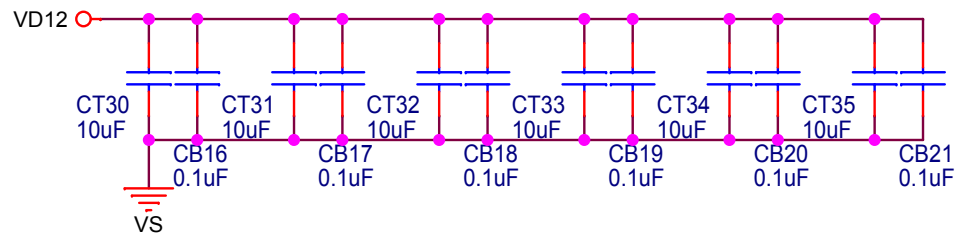
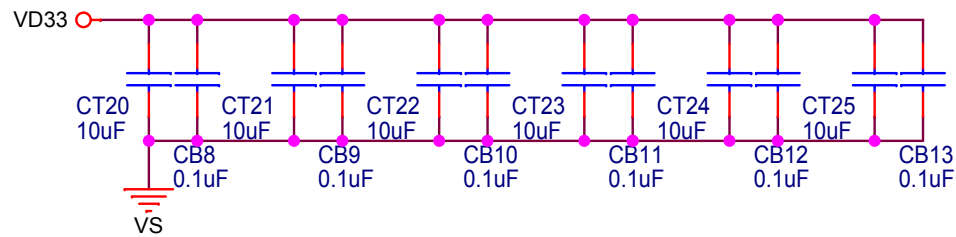
Cfg7	Cfg6	NAND Flash Page size
ON	ON	2KB
ON	OFF	4KB
OFF	ON	8KB
OFF	OFF	None

Cfg9	Cfg8	NAND Flash ECC type
ON	ON	BCH T12.
ON	OFF	BCH T15.
OFF	ON	BCH T24.
OFF	OFF	None



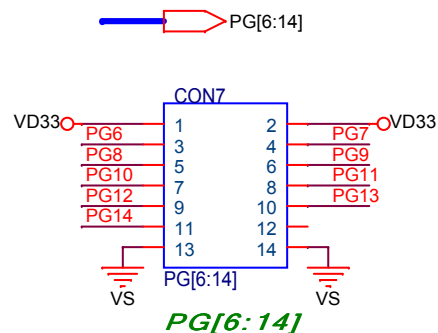
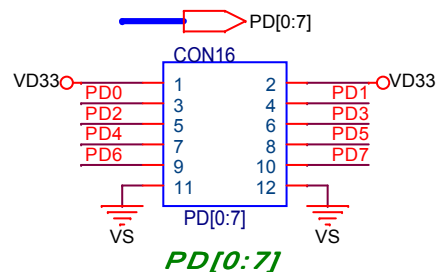
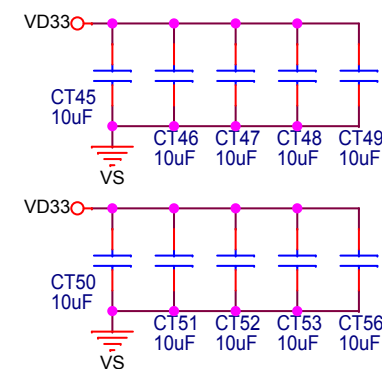
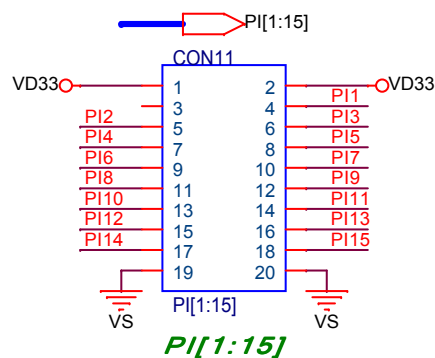
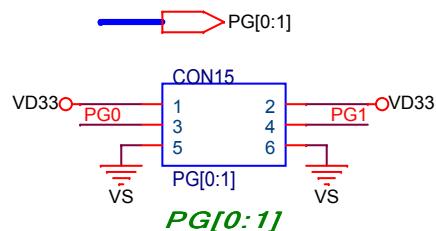
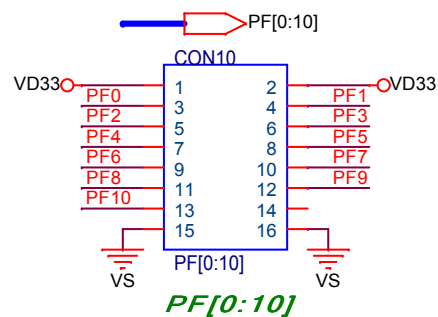
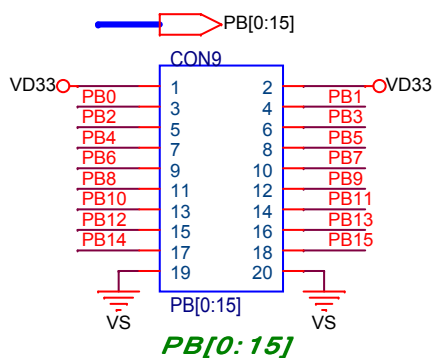
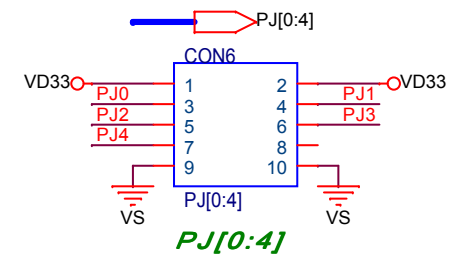
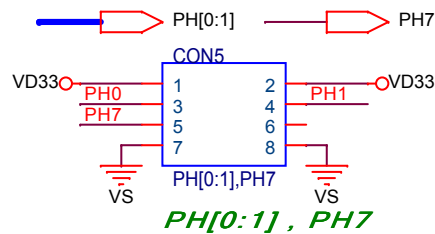
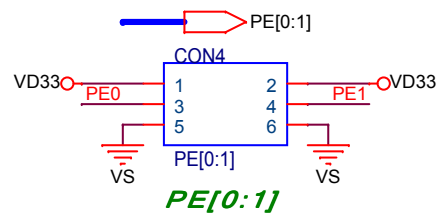
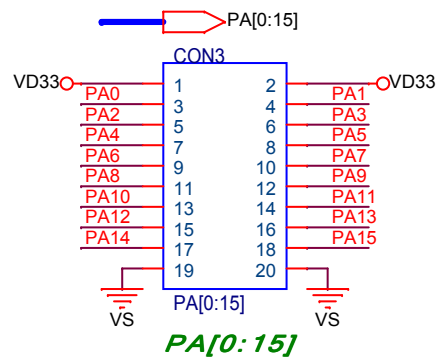
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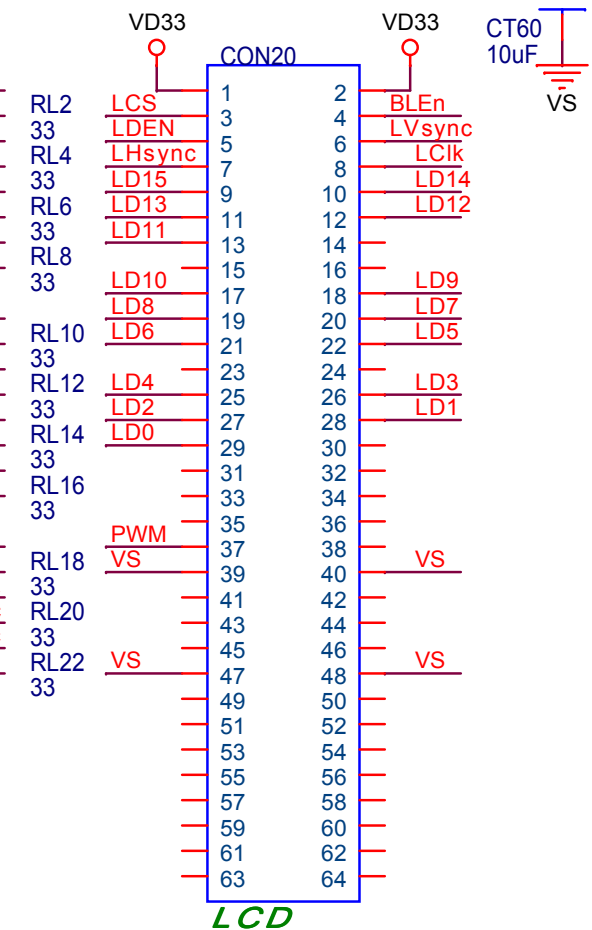
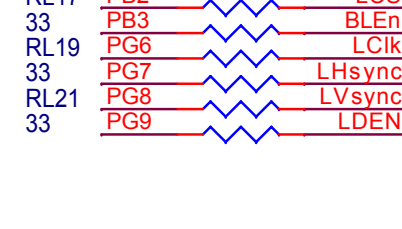
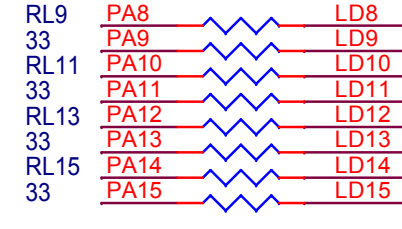
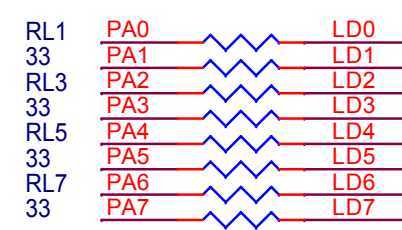
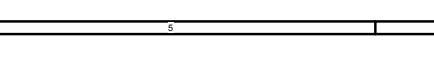
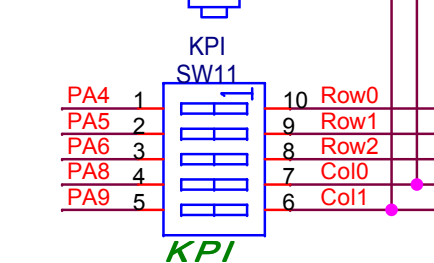
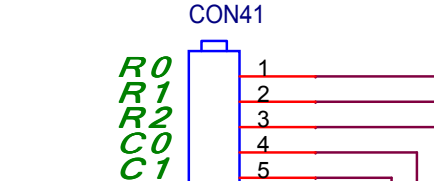
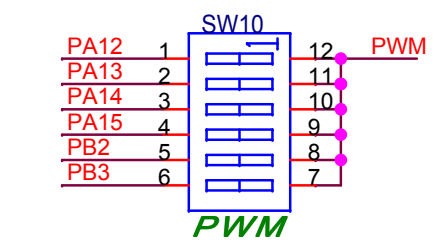
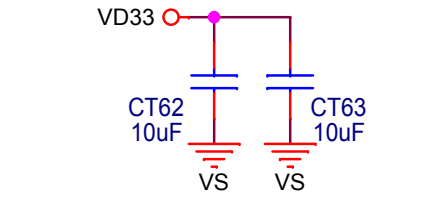
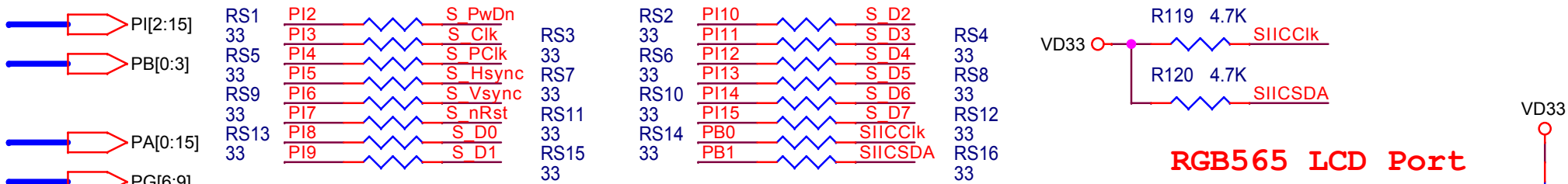


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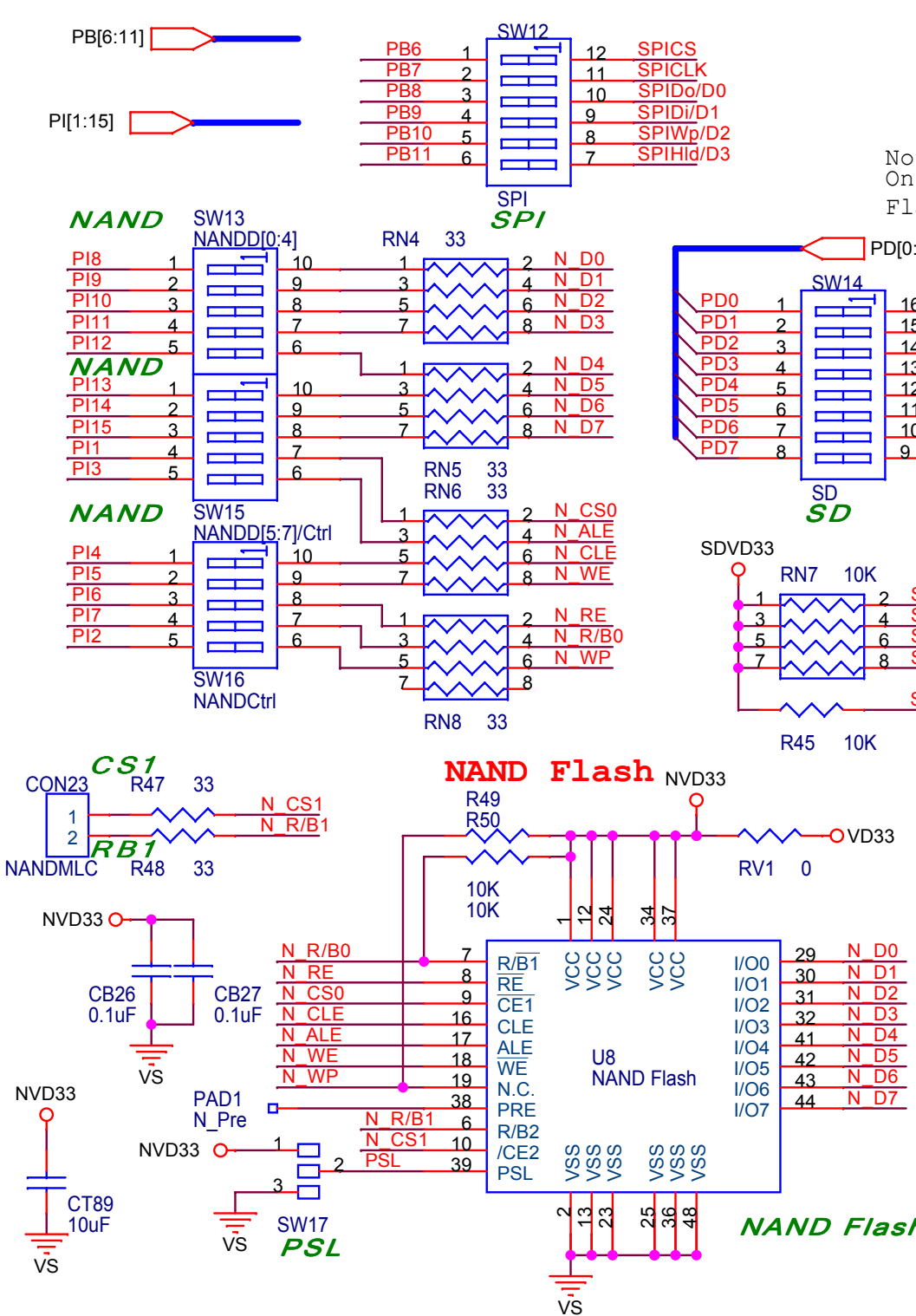
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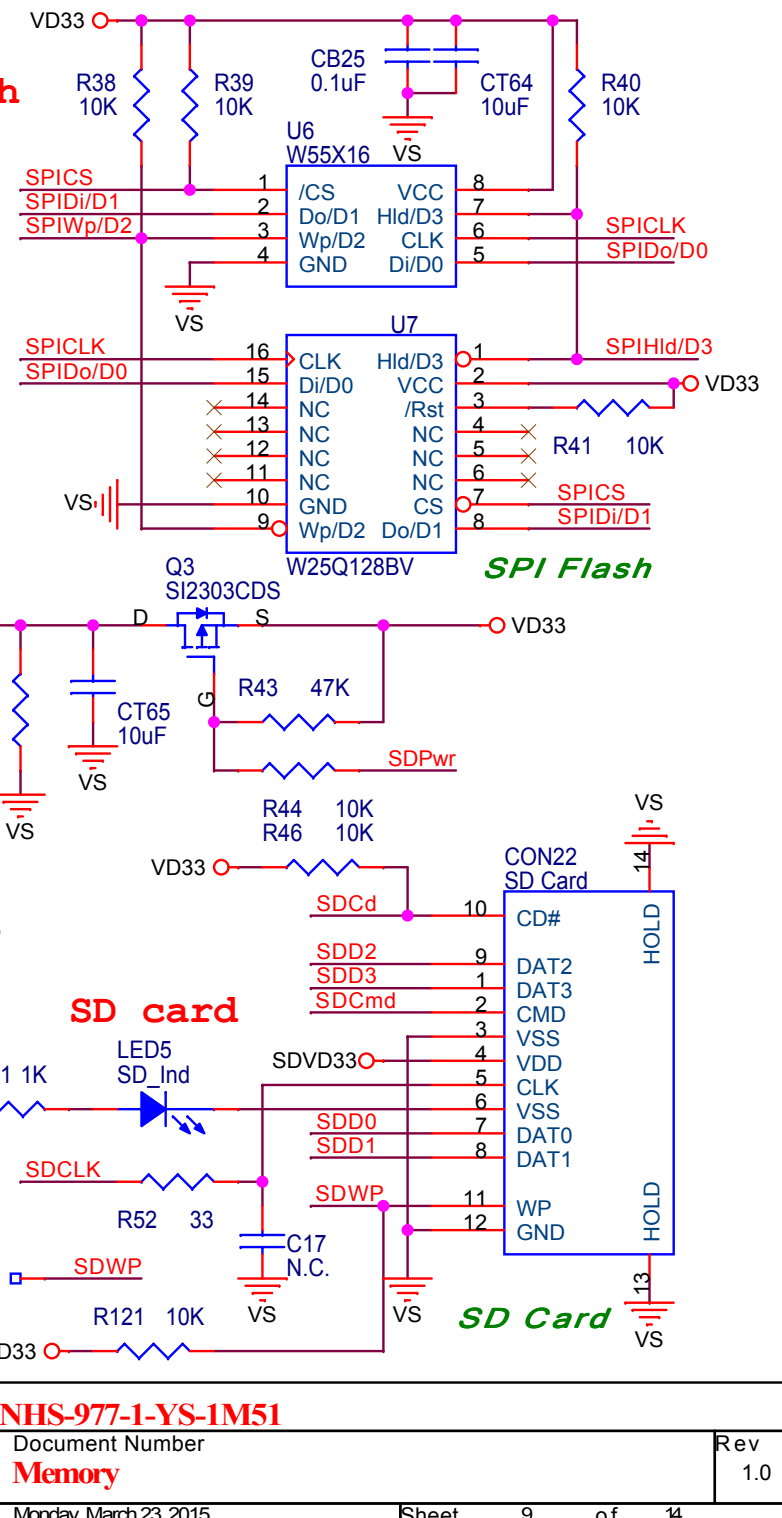
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SPI Flash

Note:
Only one SPI
Flash is used.

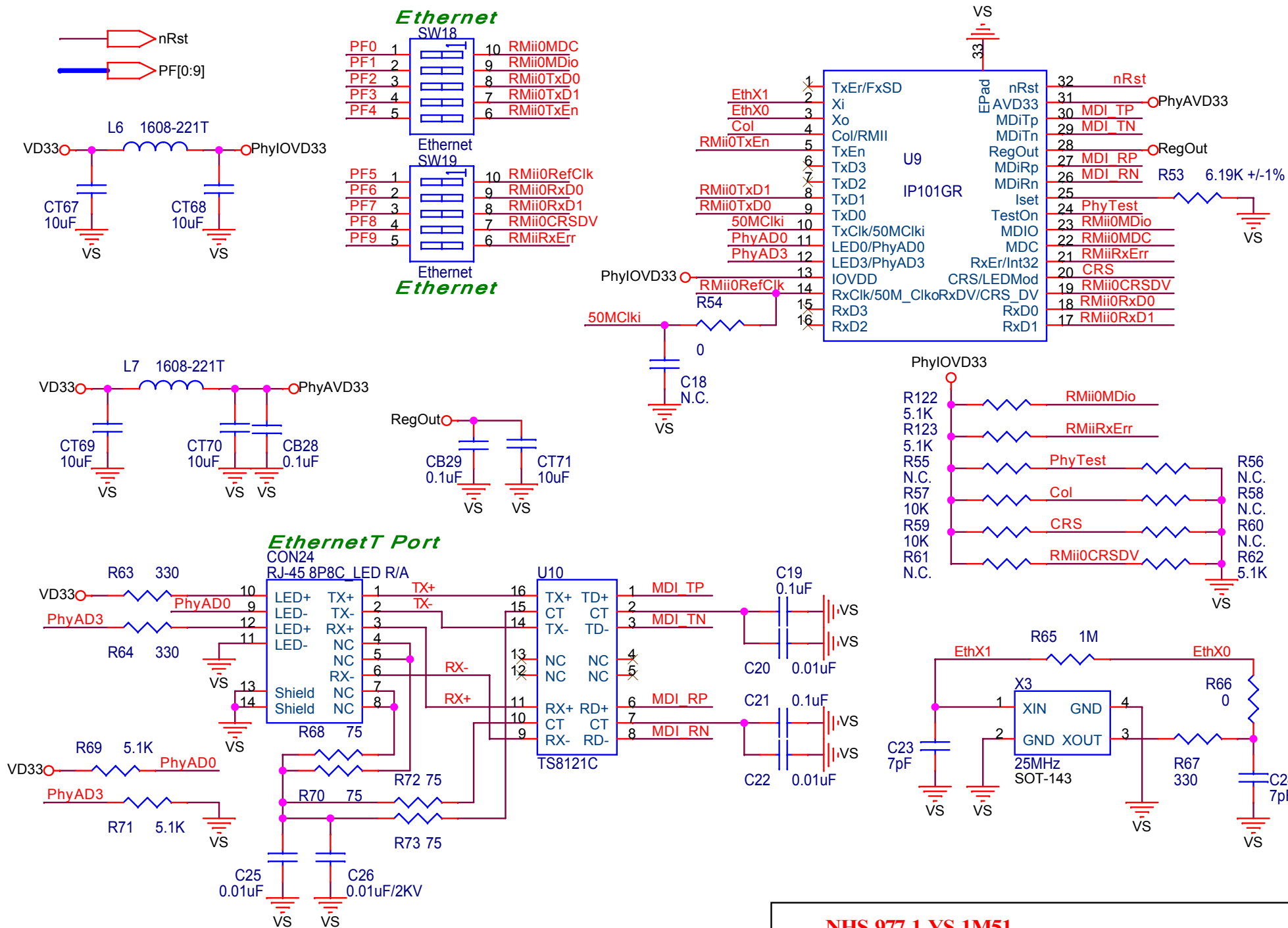
SD



Ethernet

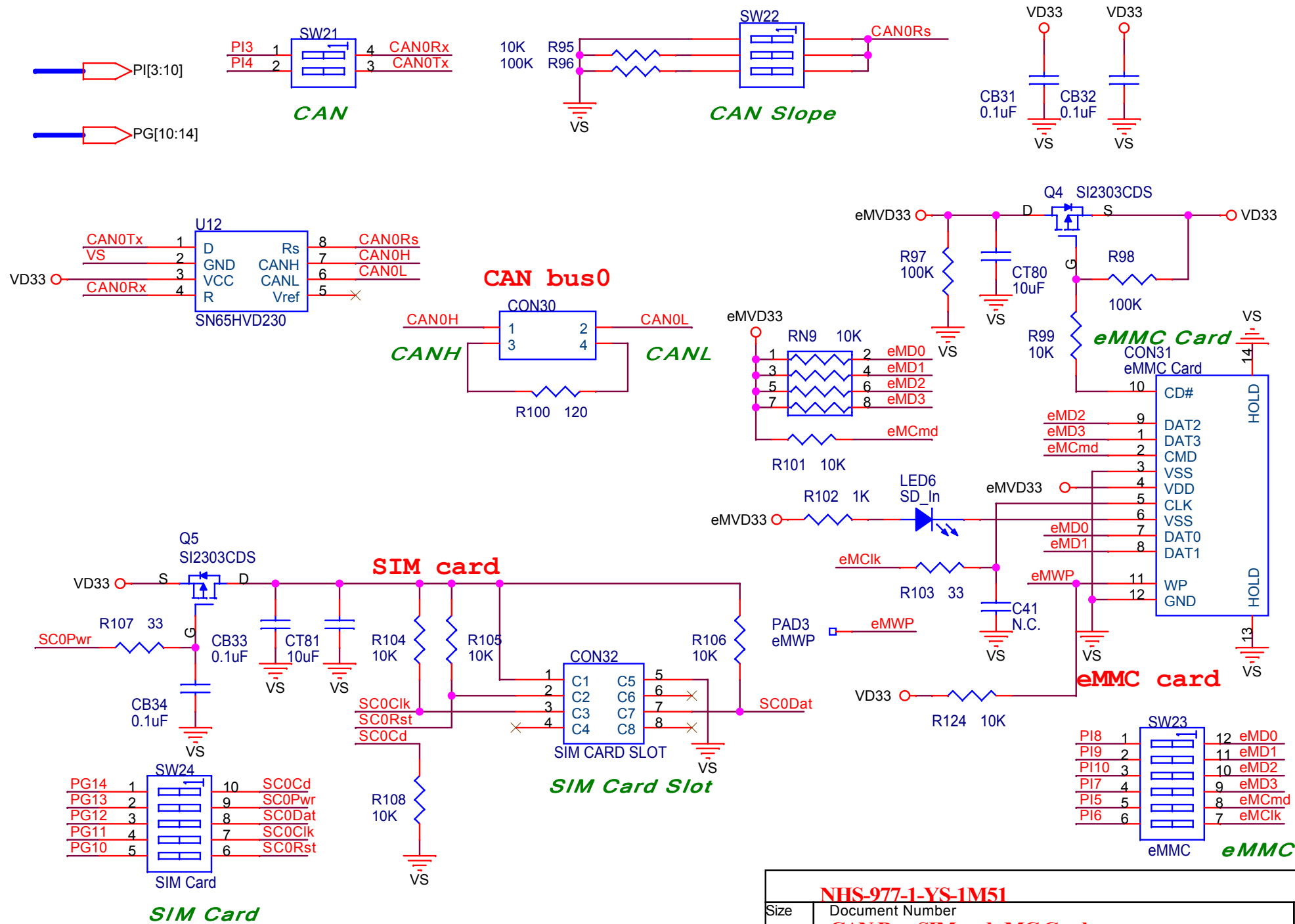
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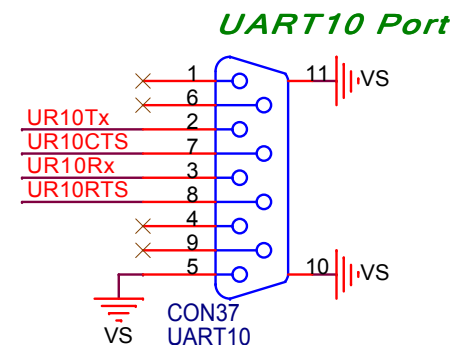
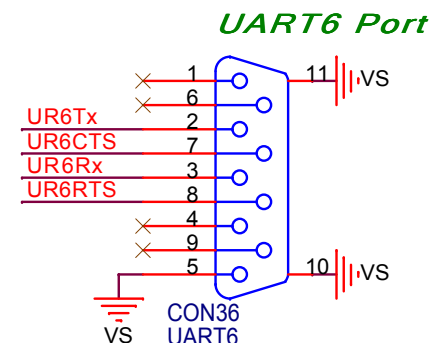
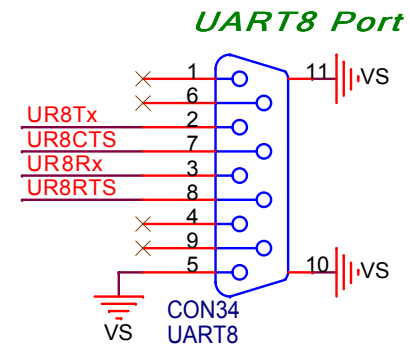
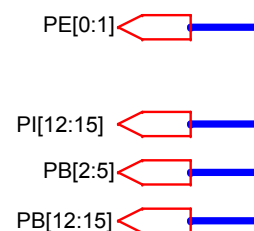
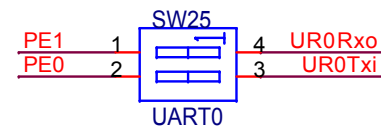
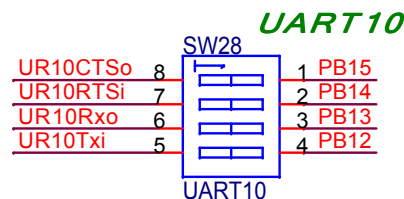
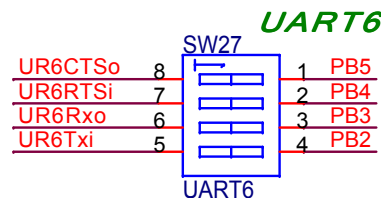
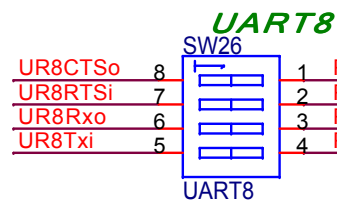
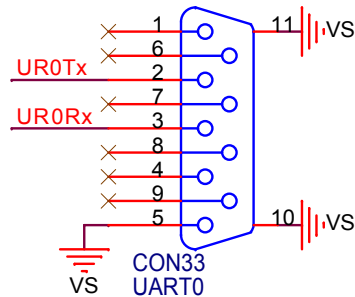
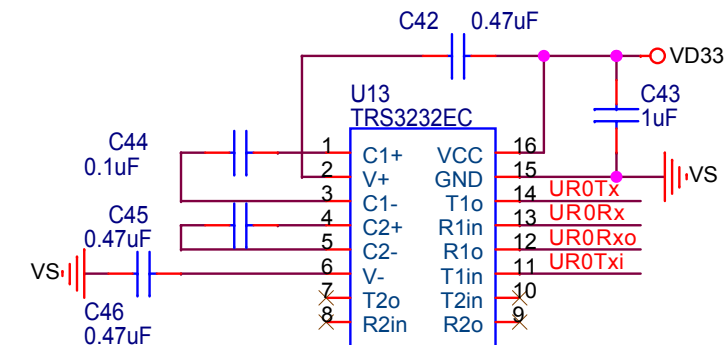
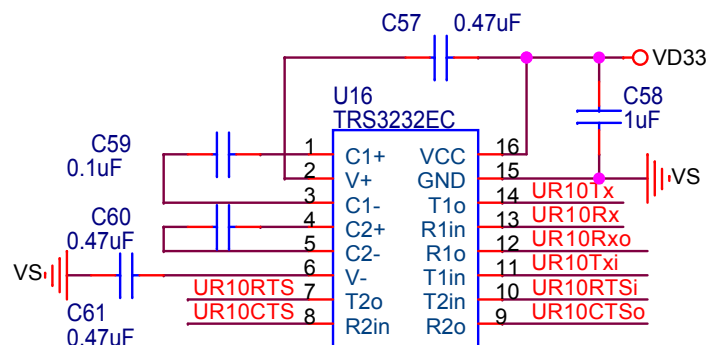
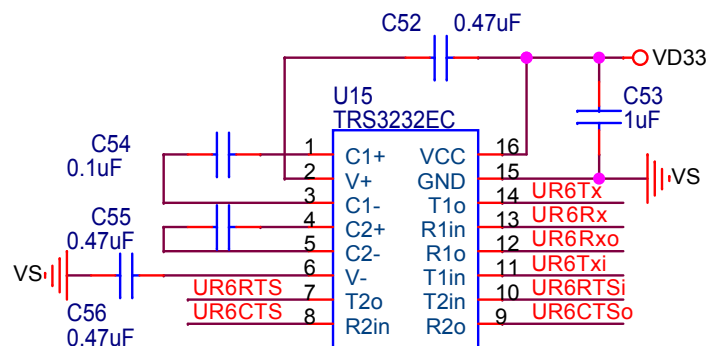
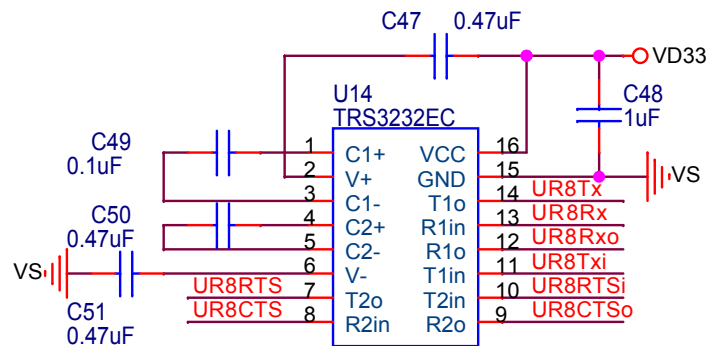
EthernetT Port



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