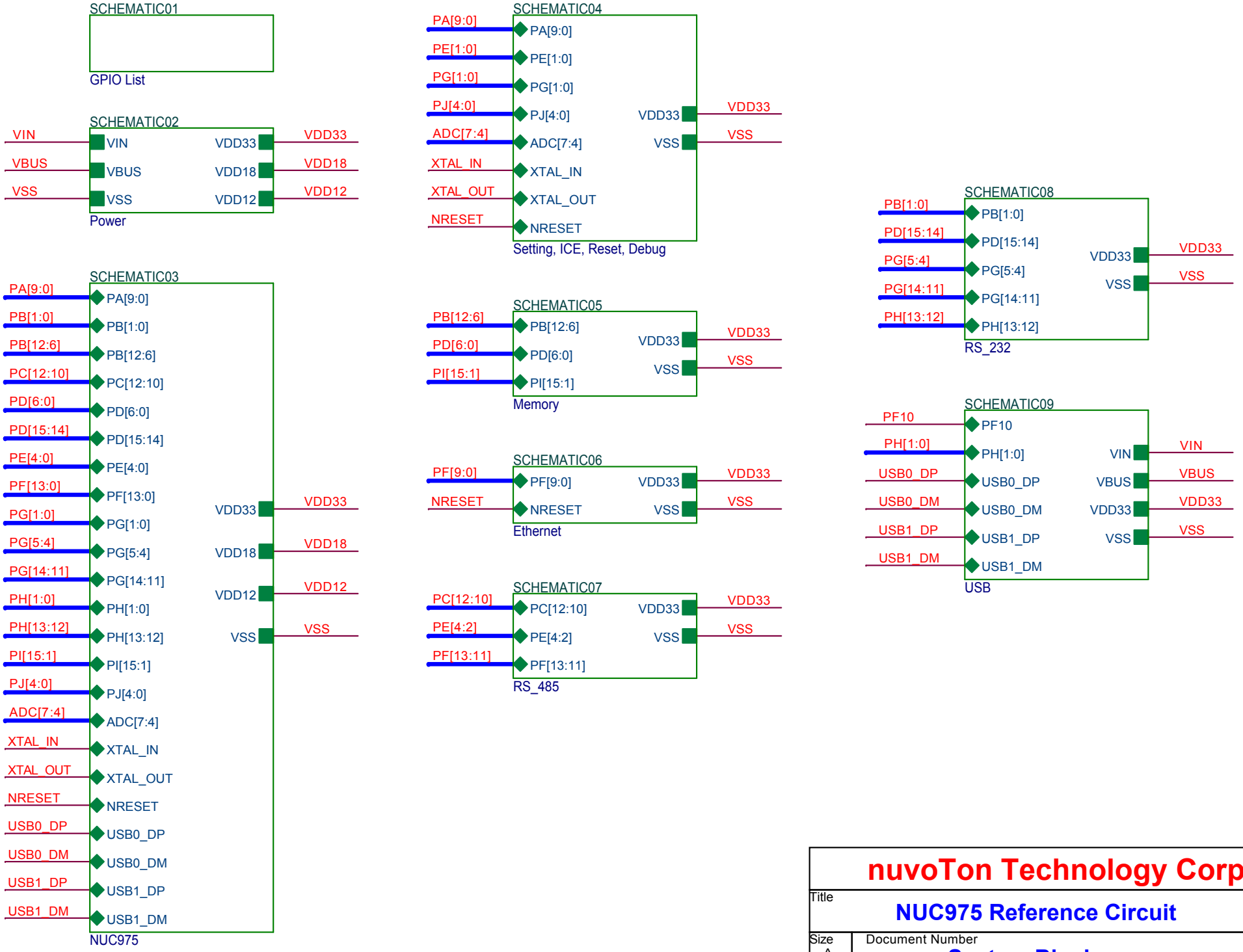
SCHEMATIC01
GPIO List
SCHEMATIC02
VIN
VBUS
VSS
VIN
VBUS
VSS
VDD33
VDD18
VDD12
VDD33
VDD18
VDD12
Power
SCHEMATIC03
PA[9:0]
PB[1:0]
PB[12:6]
PC[12:10]
PD[6:0]
PD[15:14]
PE[4:0]
PF[13:0]
PG[1:0]
PG[5:4]
PG[14:11]
PH[1:0]
PH[13:12]
PI[15:1]
PJ[4:0]
ADC[7:4]
XTAL_IN
XTAL_OUT
NRESET
USB0_DP
USB0_DM
USB1_DP
USB1_DM
VDD33
VDD18
VDD12
VSS
NUC975
SCHEMATIC04
PA[9:0]
PE[1:0]
PG[1:0]
PJ[4:0]
ADC[7:4]
XTAL_IN
XTAL_OUT
NRESET
VDD33
VSS
Setting, ICE, Reset, Debug
SCHEMATIC05
PB[12:6]
PD[6:0]
PI[15:1]
VDD33
VSS
Memory
SCHEMATIC06
PF[9:0]
NRESET
VDD33
VSS
Ethernet
SCHEMATIC07
PC[12:10]
PE[4:2]
PF[13:11]
VDD33
VSS
RS_485
SCHEMATIC08
PB[1:0]
PD[15:14]
PG[5:4]
PG[14:11]
PH[13:12]
VDD33
VSS
RS_232
SCHEMATIC09
PF10
PH[1:0]
USB0_DP
USB0_DM
USB1_DP
USB1_DM
VIN
VBUS
VDD33
VSS
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PIN	FUNCTION
PA0	PwrOnSet_0
PA1	PwrOnSet_1
PA2	PwrOnSet_2
PA3	PwrOnSet_3
PA4	PwrOnSet_4
PA5	PwrOnSet_5
PA6	PwrOnSet_6
PA7	PwrOnSet_7
PA8	PwrOnSet_8
PA9	PwrOnSet_9

PIN	FUNCTION
PB0	232_5_TXD
PB1	232_5_RXD
PB6	SPI0_SS0
PB7	SPI_CLK
PB8	SPI_DO(DATA0)
PB9	SPI_DI(DATA1)
PB10	SPI_DATA2
PB11	SPI_DATA3
PB12	SPI1_SS0
PB13	TBD

PIN	FUNCTION
PC10	485_4_TXD
PC11	485_4_RXD
PC12	485_4_RTS
PC13	TBD

PIN	FUNCTION
PD0	SD0_CMD
PD1	SD0_CLK
PD2	SD0_DATA0
PD3	SD0_DATA1
PD4	SD0_DATA2
PD5	SD0_DATA3
PD6	SD0_CD
PD14	232_9_TXD
PD15	232_9_RXD

PIN	FUNCTION
PE0	232_0_TXD
PE1	232_0_RXD
PE2	485_1_TXD
PE3	485_1_RXD
PE4	485_1_RTS
PE5	TBD

PIN	FUNCTION
PF0	RMII0_MDC
PF1	RMII0_MDIO
PF2	RMII0_TXD0
PF3	RMII0_TXD1
PF4	RMII0_TXEN
PF5	RMII0_REFCLK
PF6	RMII0_RXD0
PF7	RMII0_RXD1
PF8	RMII0_CRSDV
PF9	RMII0_RXERR
PF10	USB1_PWREN
PF11	485_2_TXD
PF12	485_2_RXD
PF13	485_2_RTS
PF14	TBD

PIN	FUNCTION
PI1	NAND_CS0
PI2	NAND_WP
PI3	NAND_ALE
PI4	NAND_CLE
PI5	NAND_WE
PI6	NAND_RE
PI7	NAND_RDY0
PI8	NAND_D0
PI9	NAND_D1
PI10	NAND_D2
PI11	NAND_D3
PI12	NAND_D4
PI13	NAND_D5
PI14	NAND_D6
PI15	NAND_D7

PIN	FUNCTION
PJ0	JTAG_TCK
PJ1	JTAG_TMS
PJ2	JTAG_TDI
PJ3	JTAG_TDO
PJ4	JTAG_NTRST

PIN	FUNCTION
PG0	I2C0_SCL
PG1	I2C0_SDA
PG4	232_7_TXD
PG5	232_7_RXD
PG10	TBD
PG11	232_6_TXD
PG12	232_6_RXD
PG13	232_6_RTS
PG14	232_6_CTS

PIN	FUNCTION
PH0	USB0_VBUSVLD
PH1	USB1_OVC
PH12	232_8_TXD
PH13	232_8_RXD

FUNCTION:

NAND Flash x1

SPI Flash x2

SD x1

ICE x1

ADC CH x4

I2C x1

LAN x1

GPIO x5

USB Device x1

USB HOST x1

RS485 half-duplex x3

RS232 data flow x1

RS232 normal x5

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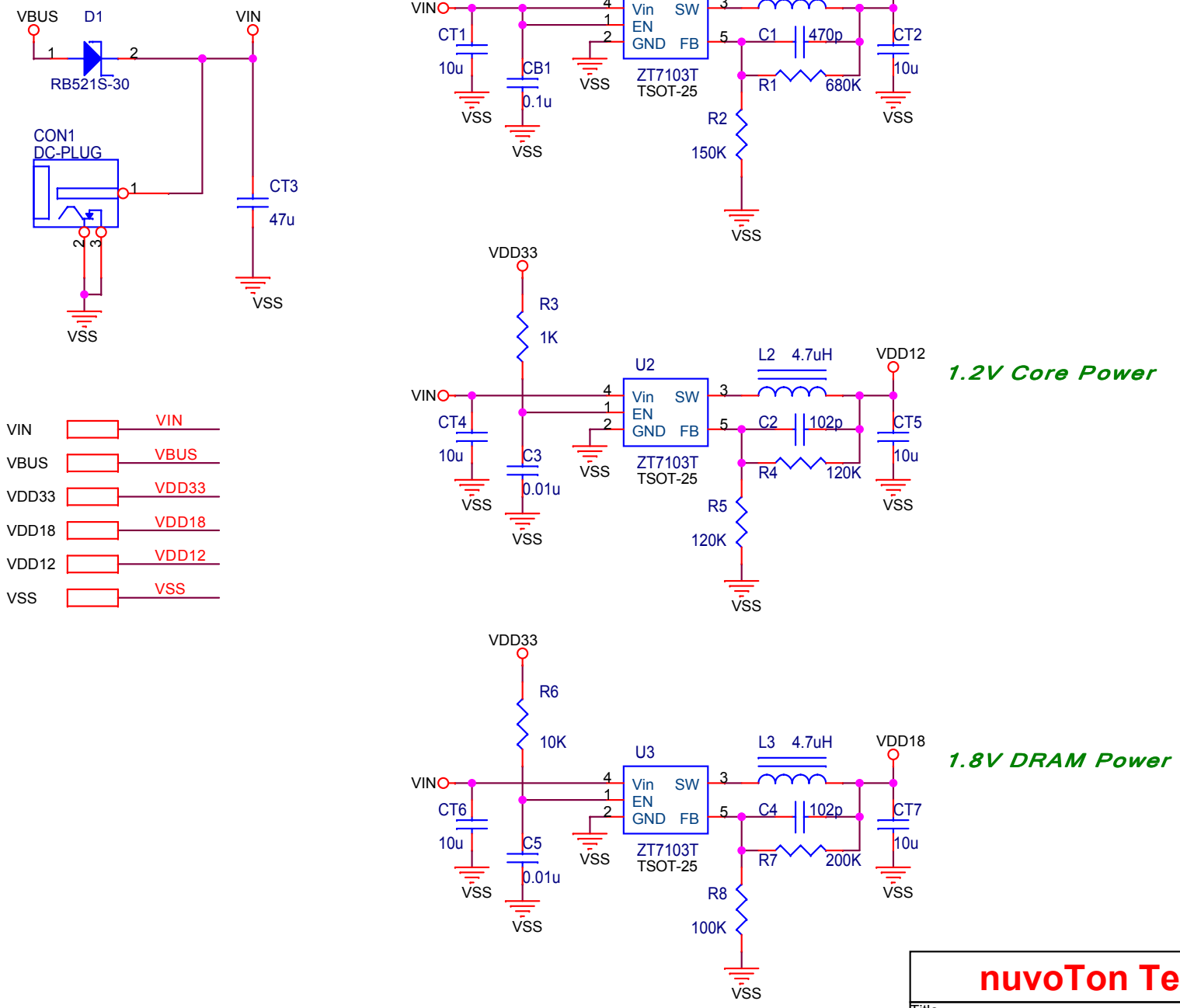

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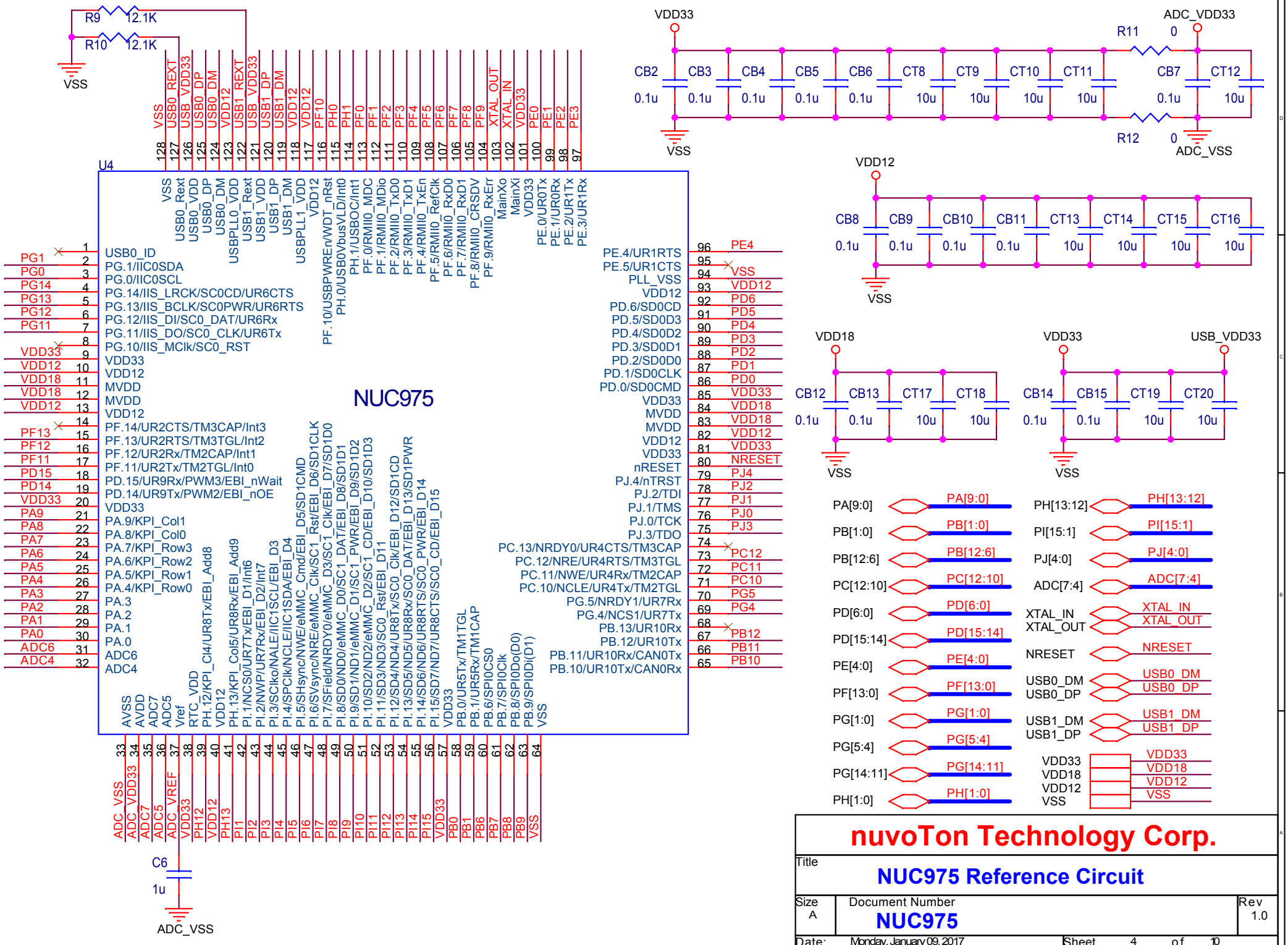

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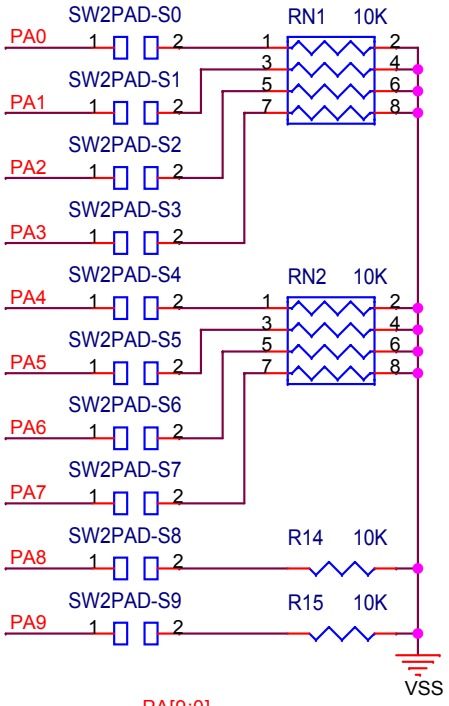
Power On Setting



S1(A1)	S0(A0)	Bootting from
ON	ON	USB.
ON	OFF	eMMC Flash.
OFF	ON	NAND Flash.
OFF	OFF	SPI Flash.

S2(A2)	System Clock from
ON	12MHz crystal.
OFF	UPLL output.

S3(A3)	Watch Dog Setting
ON	OFF after power on.
OFF	ON after power on.

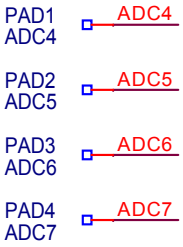
S4(A4)	Setting PJ[4:0]
ON	As GPIO pin.
OFF	As JTAG interface.

S5(A5)	UART Debug Message
ON	Output turn ON.
OFF	Output turn OFF.

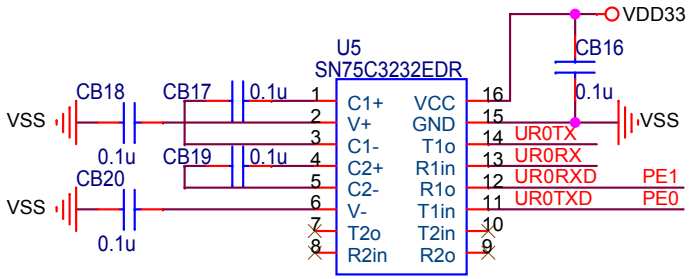
S7(A7)	S6(A6)	NAND Flash Page Size
ON	ON	2KB
ON	OFF	4KB
OFF	ON	8KB
OFF	OFF	None

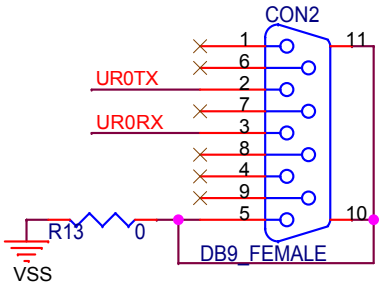
S9(A9)	S8(A8)	NAND Flash ECC type
ON	ON	BCH T12.
ON	OFF	BCH T15.
OFF	ON	BCH T24.
OFF	OFF	None

ADC

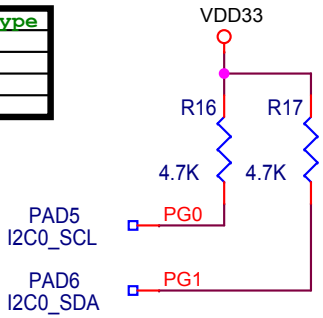


RS232_0 Debug message

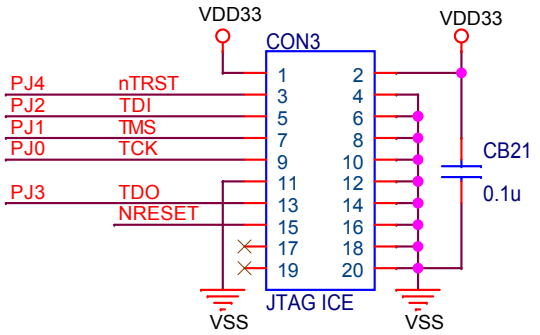


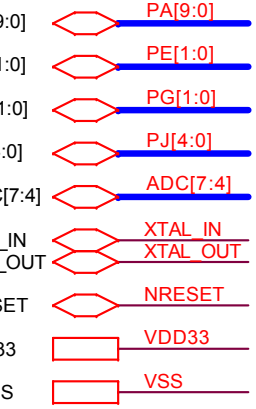


I2C

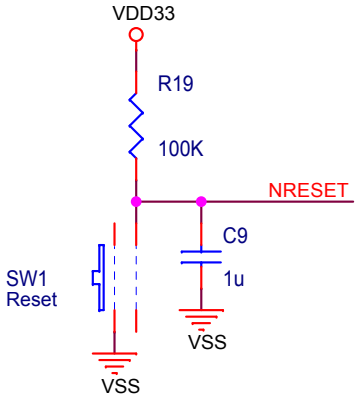


ICE I/F

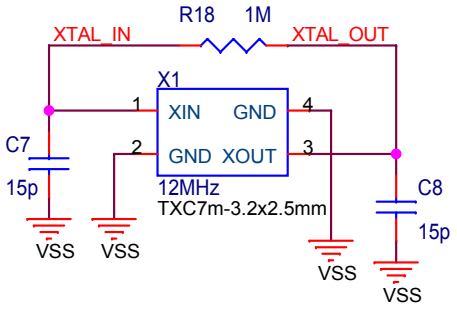




System Reset



12MHz Crystal



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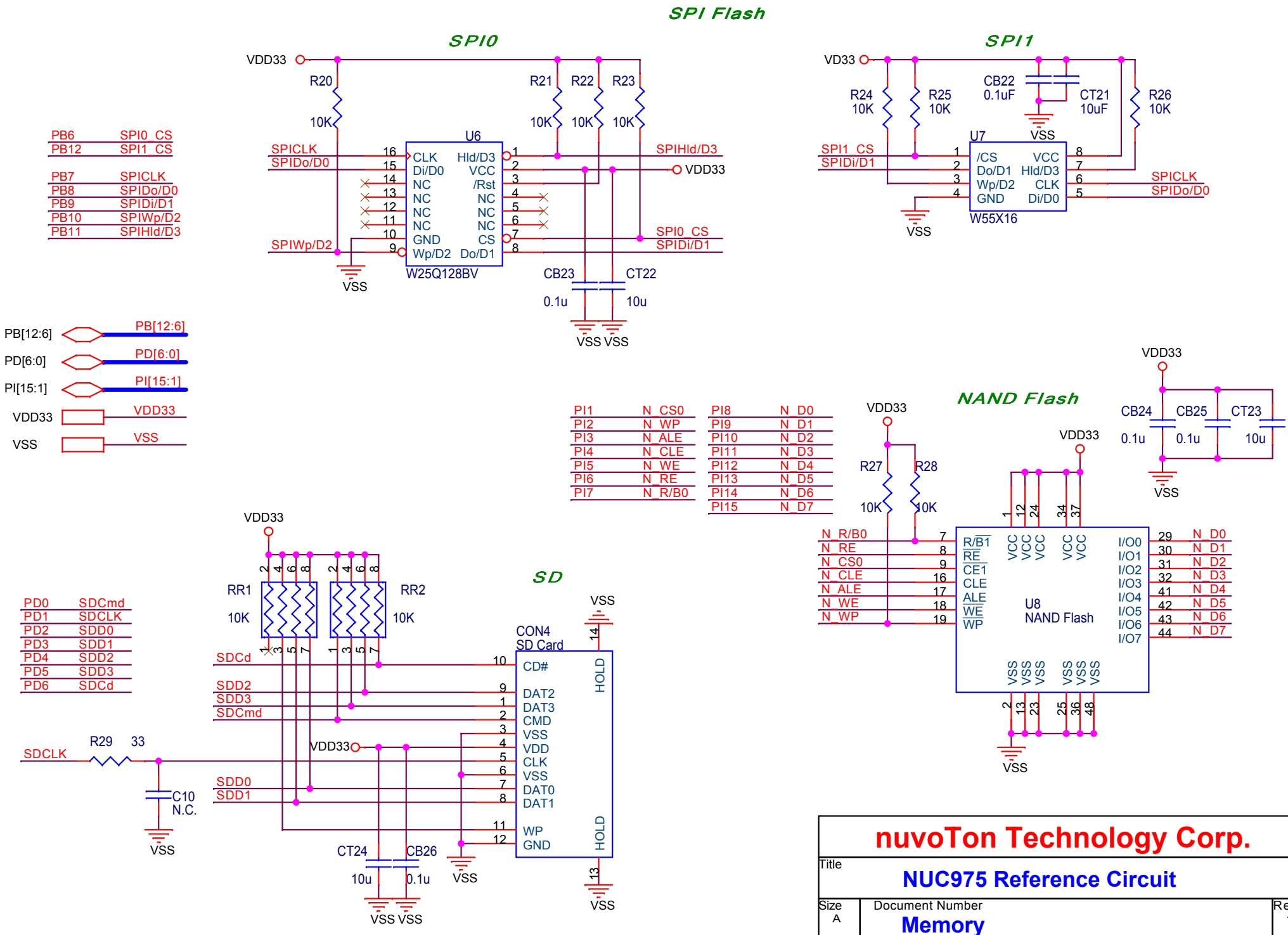
SPI Flash
SPI0
SPI1
PB6 SPI0_CS
PB12 SPI1_CS
PB7 SPICLK
PB8 SPIDo/D0
PB9 SPIDi/D1
PB10 SPIWp/D2
PB11 SPIHld/D3
VDD33
R20
10K
R21
R22
R23
U6
SPICLK
SPIDo/D0
SPIWp/D2
SPIHld/D3
SPI0_CS
SPIDi/D1
CLK
Di/D0
NC
/Rst
GND
CS
Wp/D2
Do/D1
W25Q128BV
VSS
CB23
0.1u
CT22
10u
VSS VSS
R24
10K
R25
10K
CB22
0.1uF
CT21
10uF
R26
10K
U7
SPI1_CS
SPIDi/D1
/CS
Do/D1
Wp/D2
GND
VCC
Hld/D3
CLK
Di/D0
W55X16
SPICLK
SPIDo/D0
PB[12:6]
PD[6:0]
PI[15:1]
VDD33
VSS
PI1 N_CS0
PI2 N_WP
PI3 N_ALE
PI4 N_CLE
PI5 N_WE
PI6 N_RE
PI7 N_R/B0
PI8 N_D0
PI9 N_D1
PI10 N_D2
PI11 N_D3
PI12 N_D4
PI13 N_D5
PI14 N_D6
PI15 N_D7
NAND Flash
R27
10K
R28
10K
CB24
0.1u
CB25
0.1u
CT23
10u
N_R/B0
N_RE
N_CS0
N_CLE
N_ALE
N_WE
N_WP
R/B1
RE
CE1
CLE
ALE
WE
WP
VCC
I/O0
I/O1
I/O2
I/O3
I/O4
I/O5
I/O6
I/O7
U8
NAND Flash
SD
PD0 SDCmd
PD1 SDCLK
PD2 SDD0
PD3 SDD1
PD4 SDD2
PD5 SDD3
PD6 SDCd
RR1
10K
RR2
10K
SDCd
SDD2
SDD3
SDCmd
SDD0
SDD1
SDCLK
R29 33
C10
N.C.
CON4
SD Card
CD#
DAT2
DAT3
CMD
VSS
VDD
CLK
VSS
DAT0
DAT1
WP
GND
HOLD
CT24
10u
CB26
0.1u
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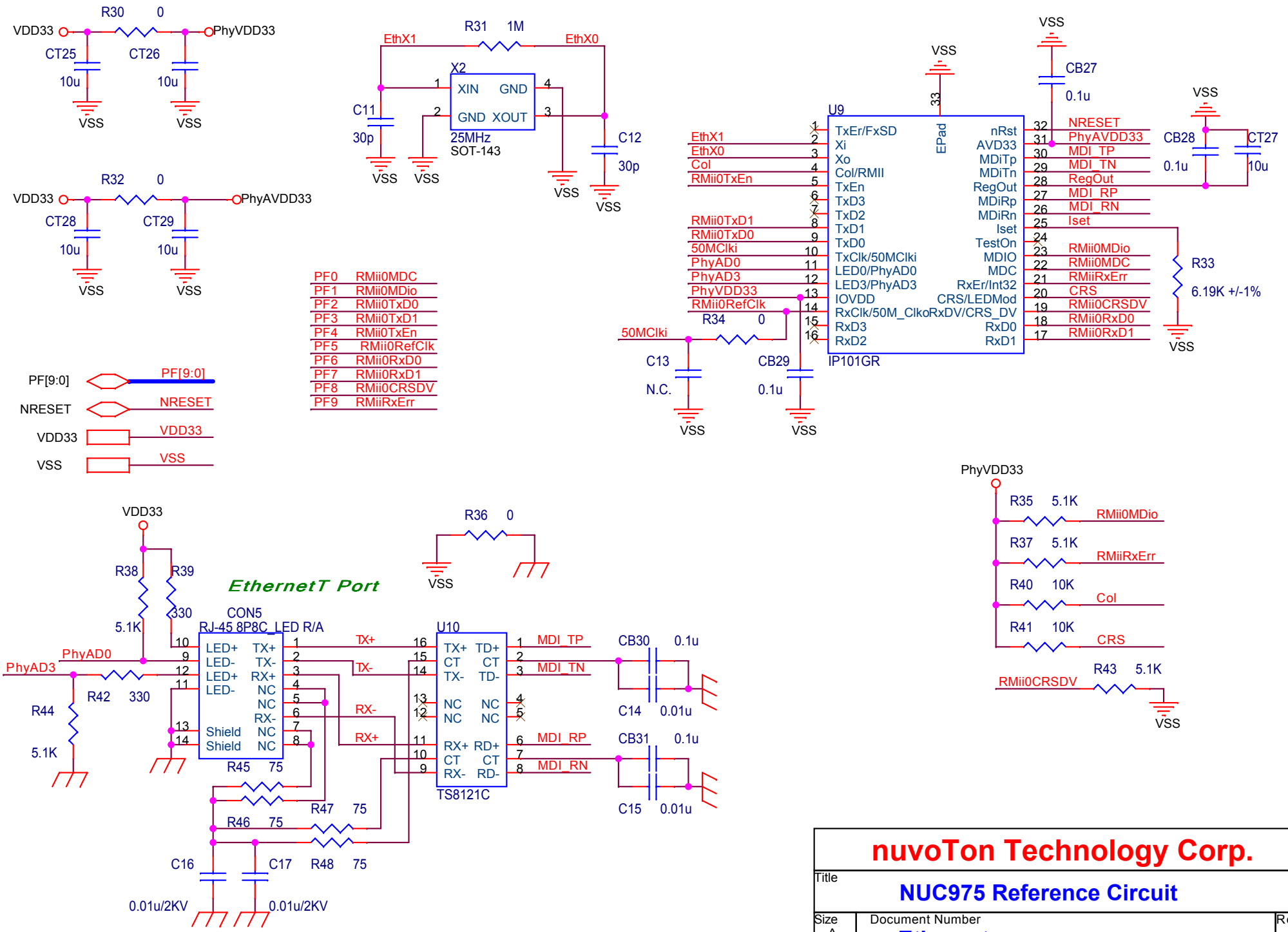

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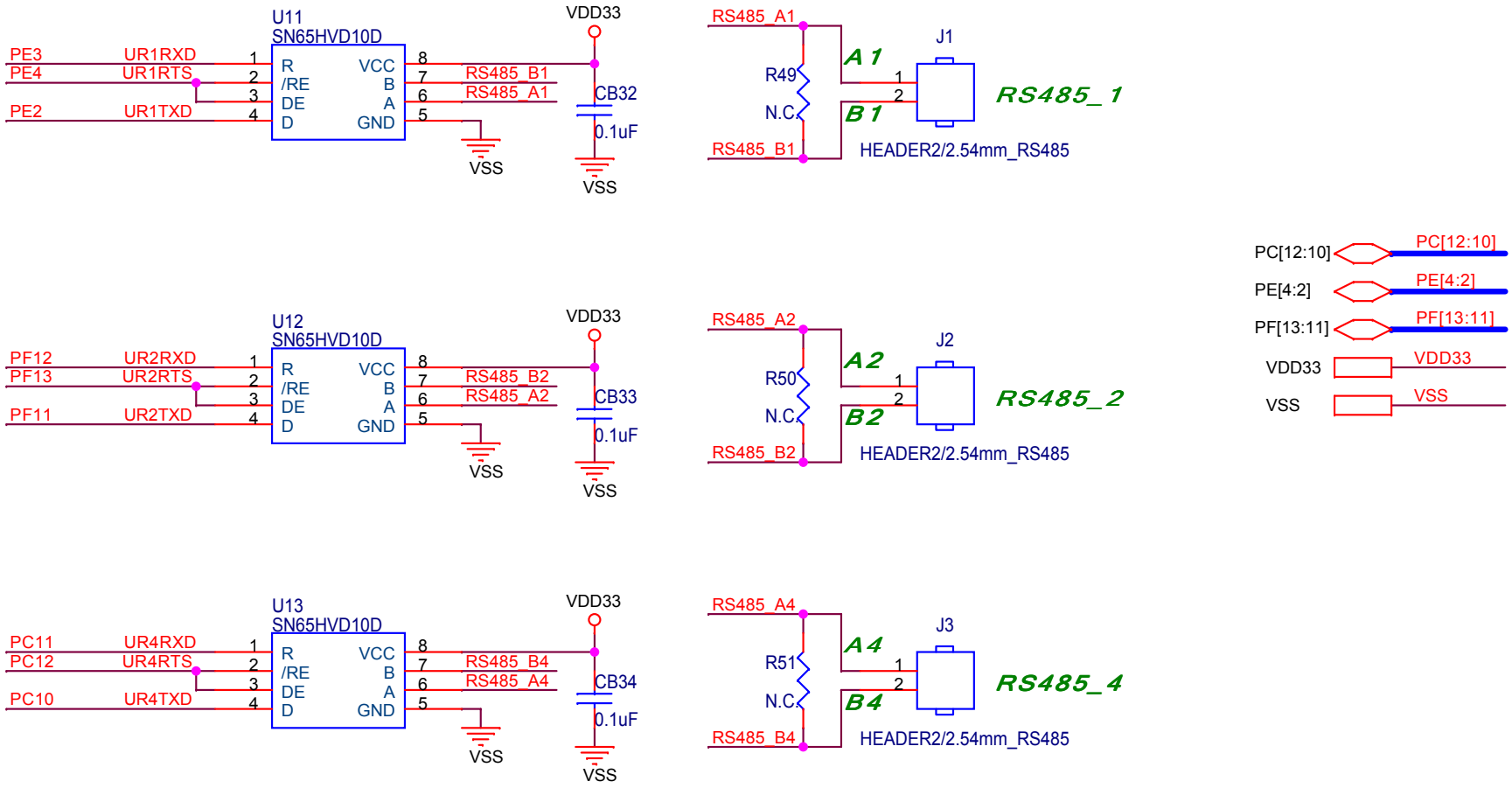
RS_485



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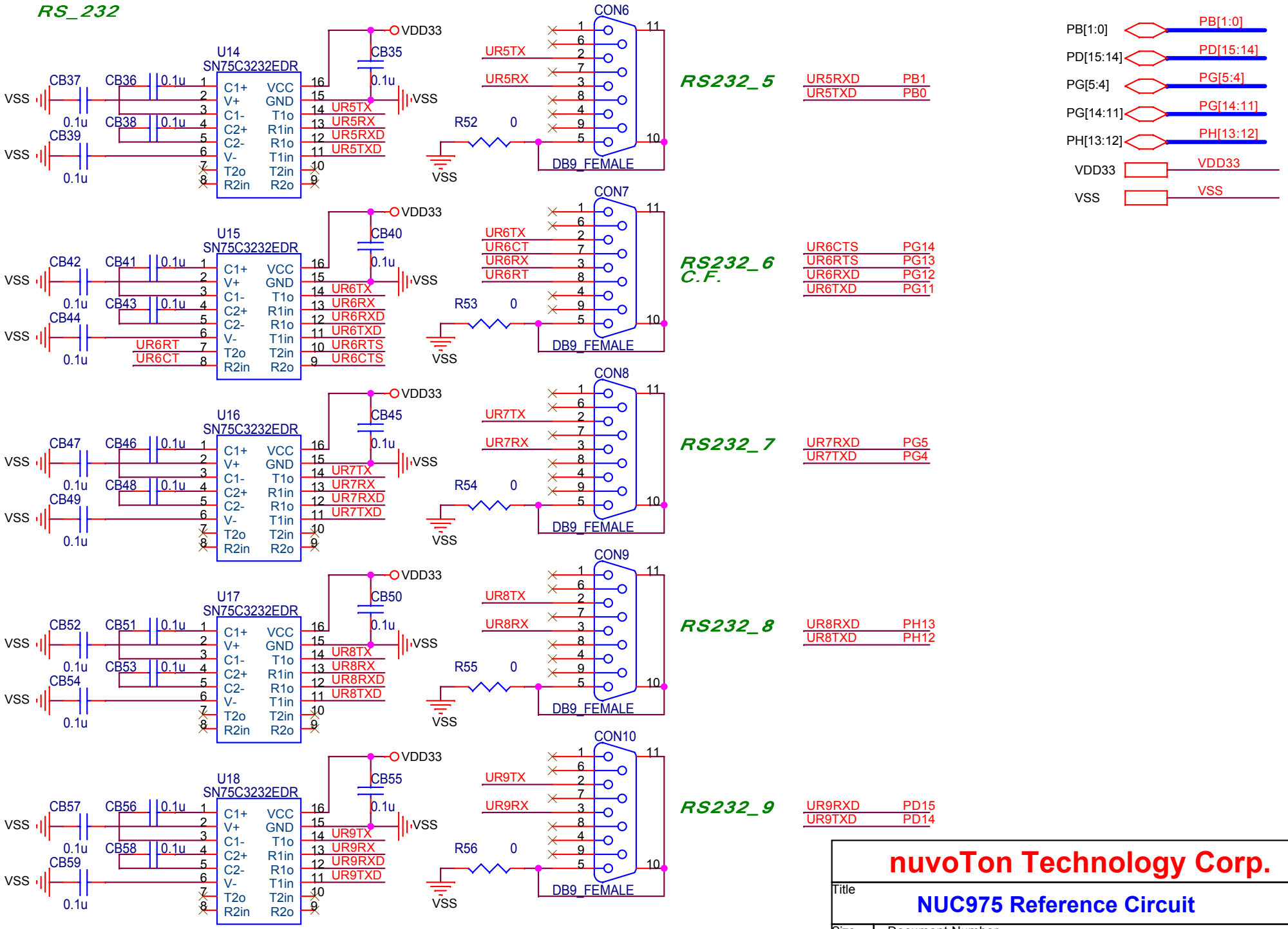

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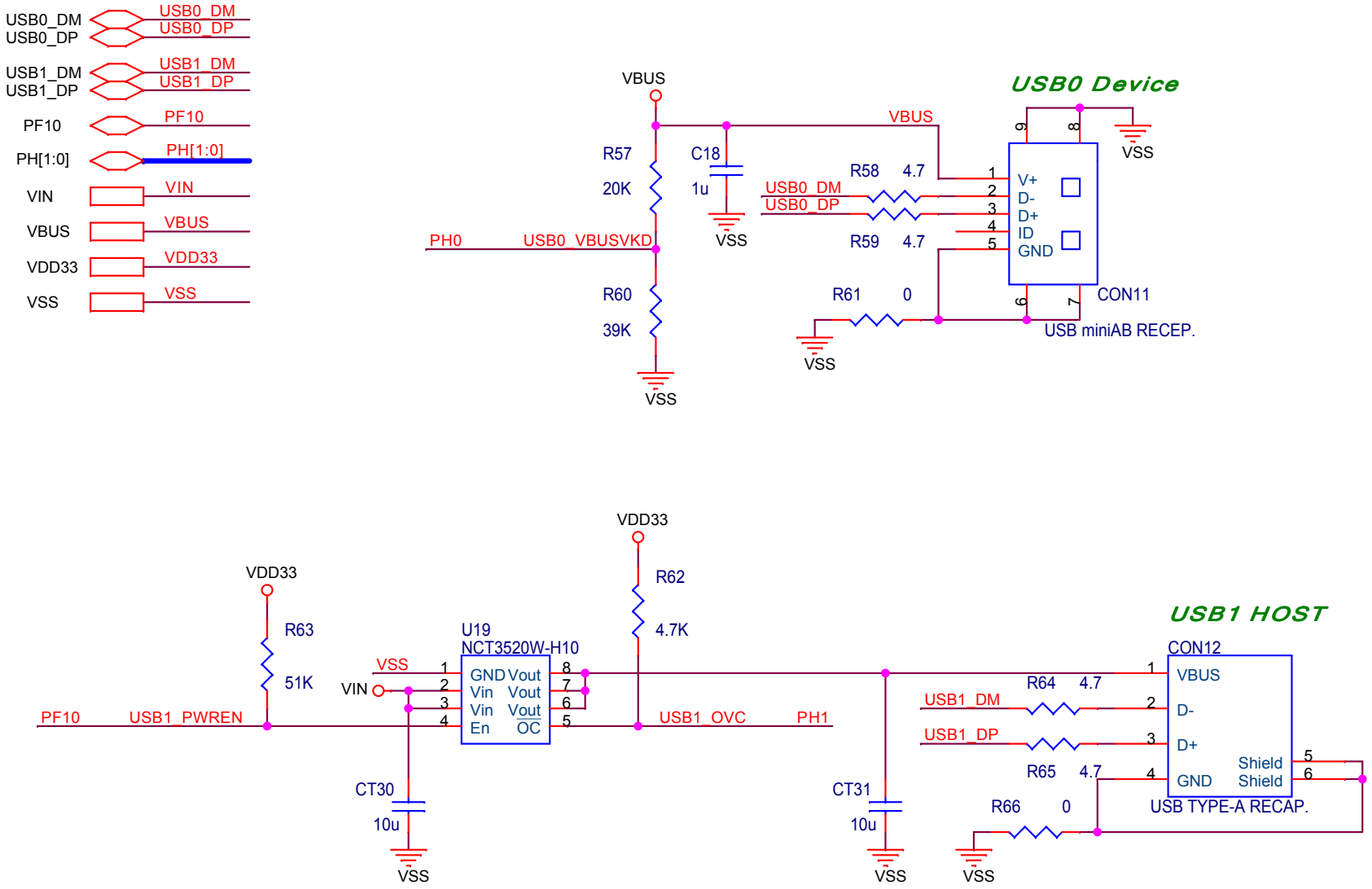

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