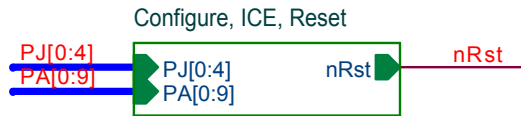
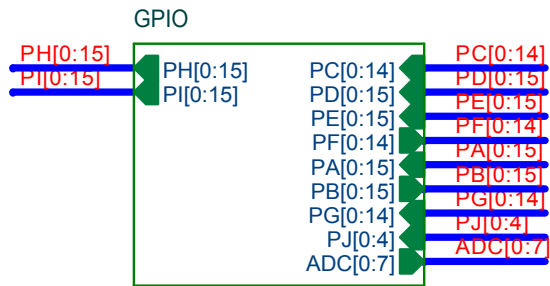


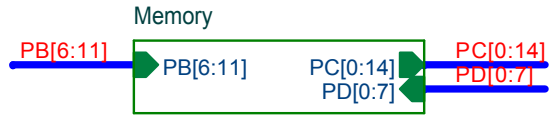
Schematic01



Schematic04



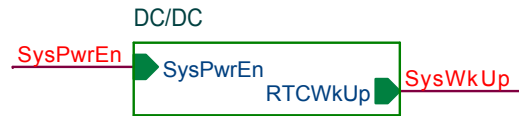
Schematic06



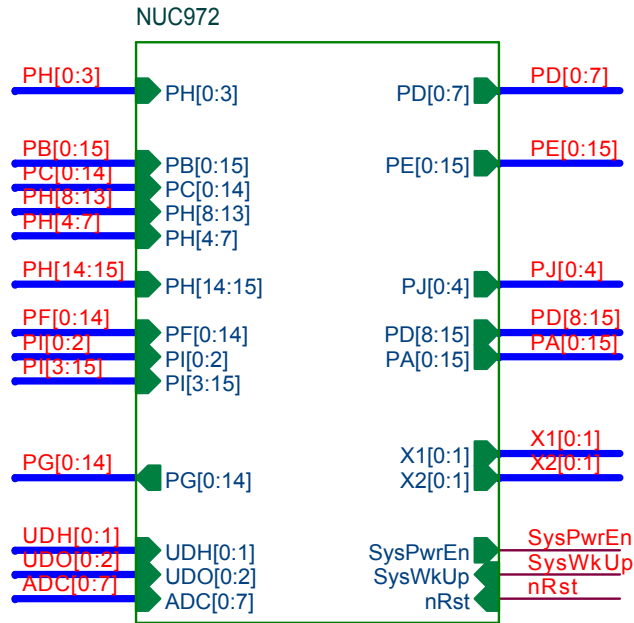
Schematic08



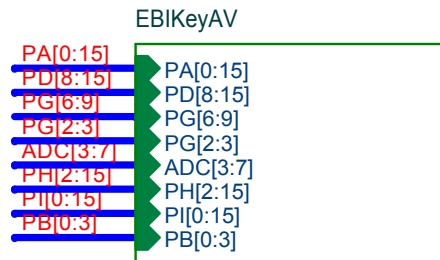
Schematic09



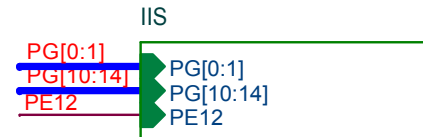
Schematic02



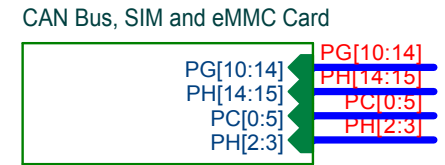
SCHEMATIC03



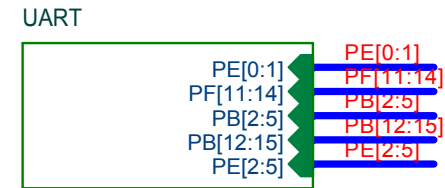
Schematic07



Schematic10



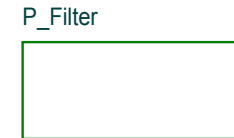
Schematic11



Schematic12

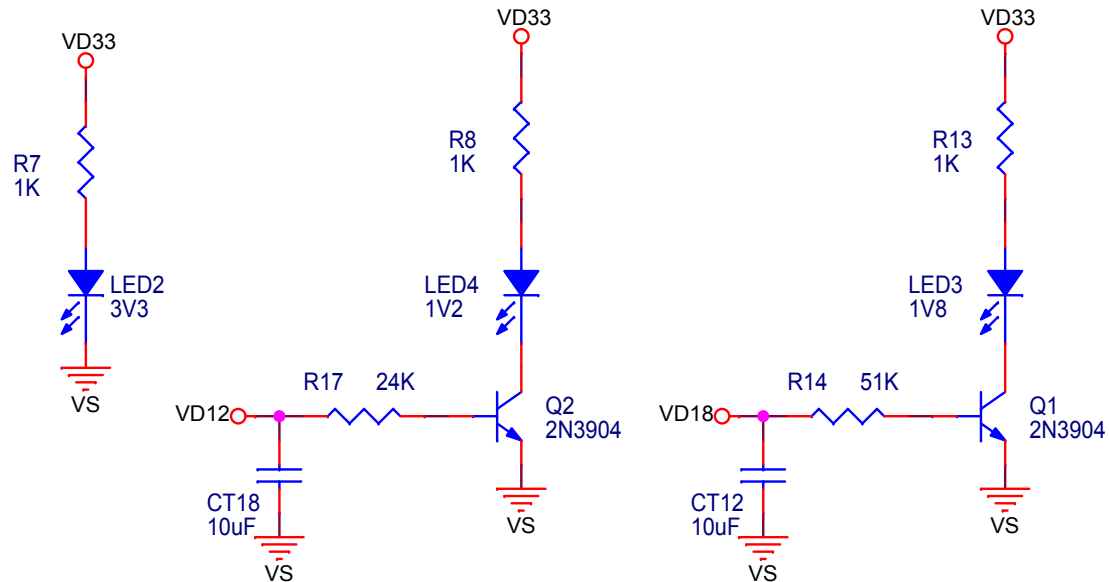
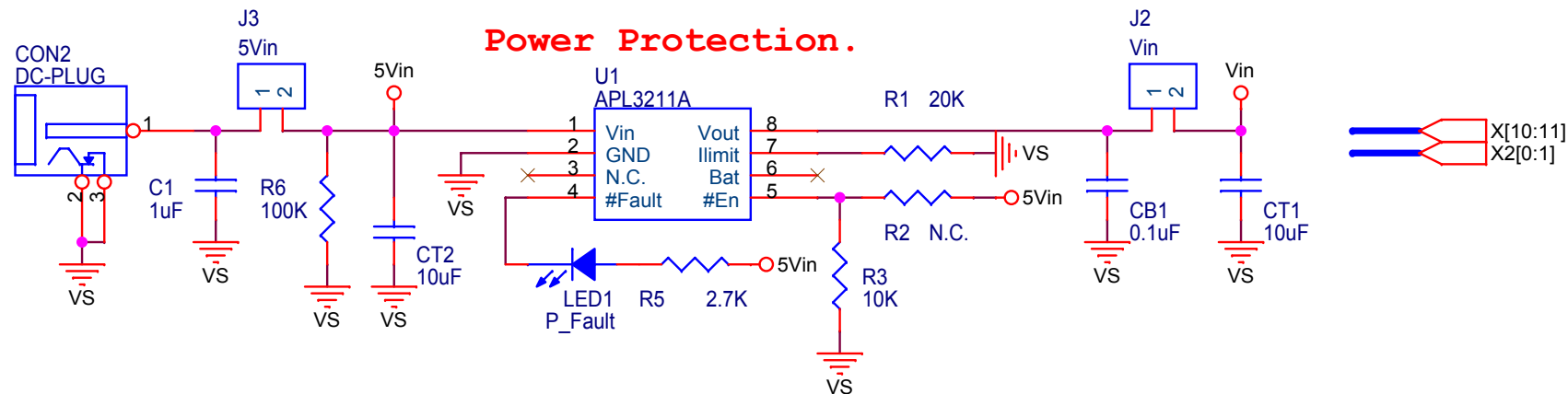


Schematic13

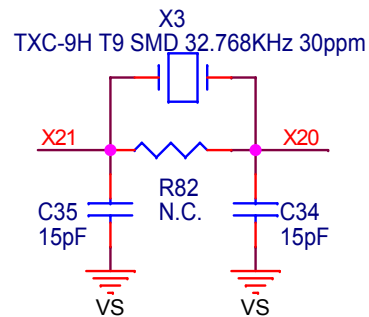


Schematic05

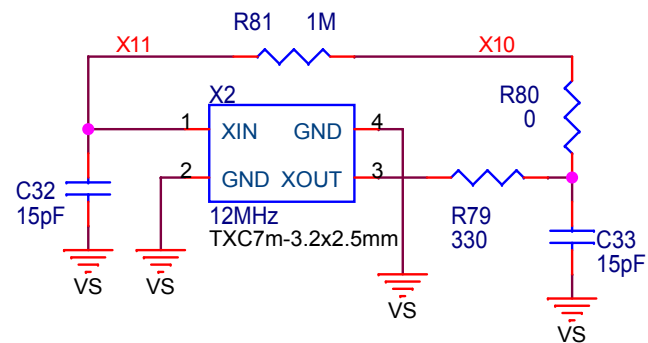
NHS-972-1-CY-1M54		
Size	Document Number	Rev
Custom	System Block	1.0
Date:	Wednesday, October 14, 2015	Sheet 1 of 14



RTC Crystal

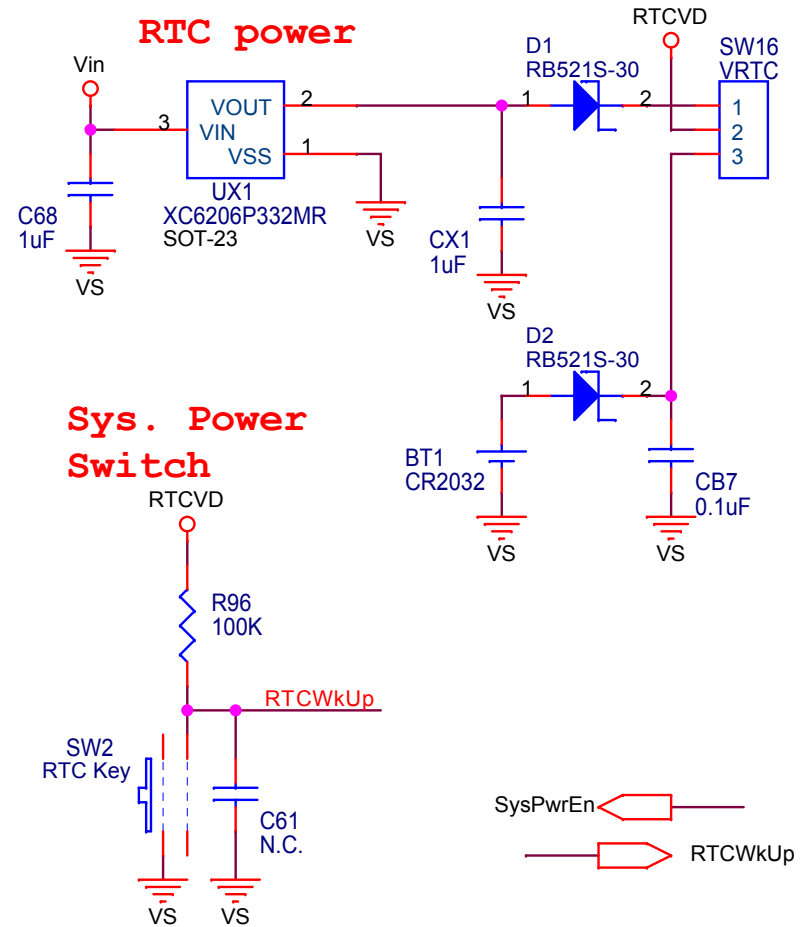
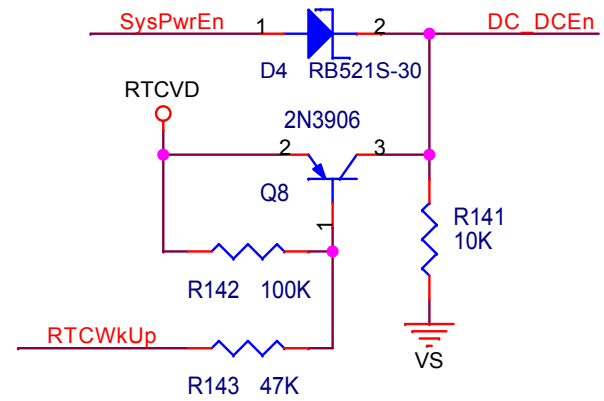
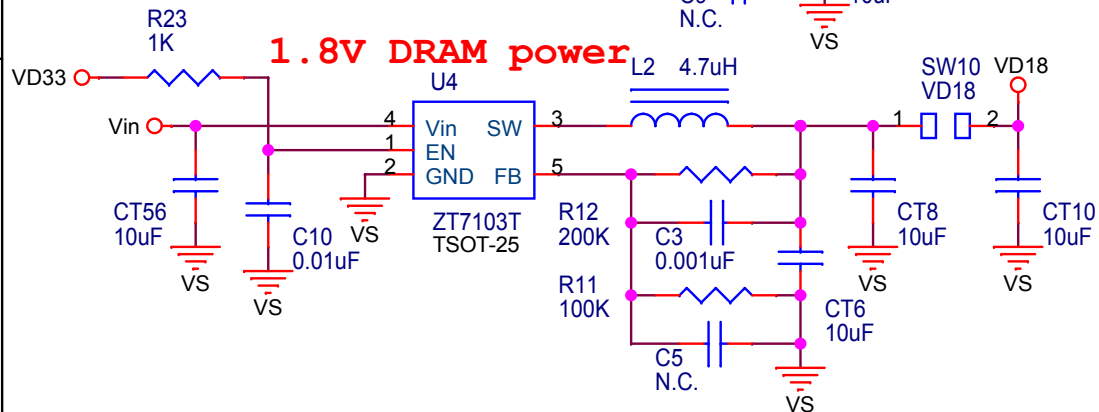
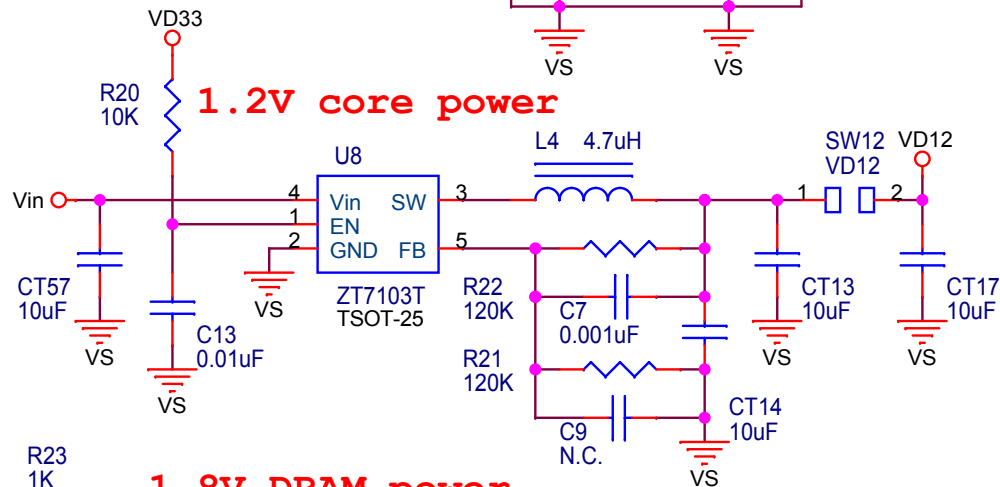
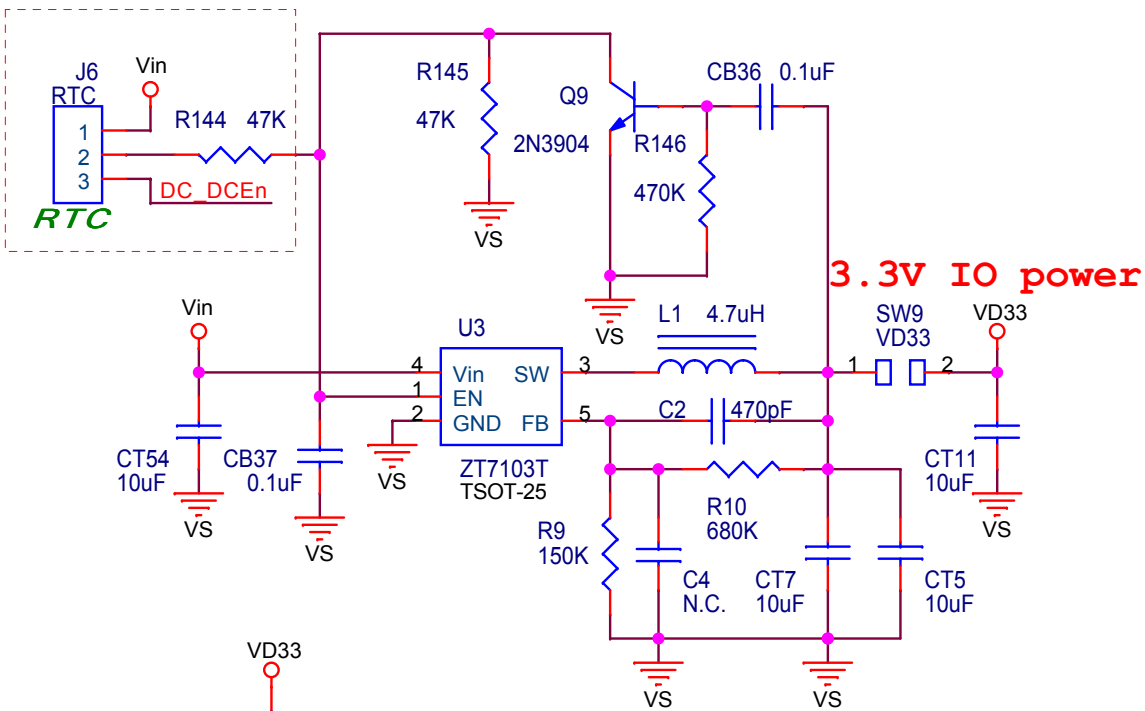


System Main Crystal

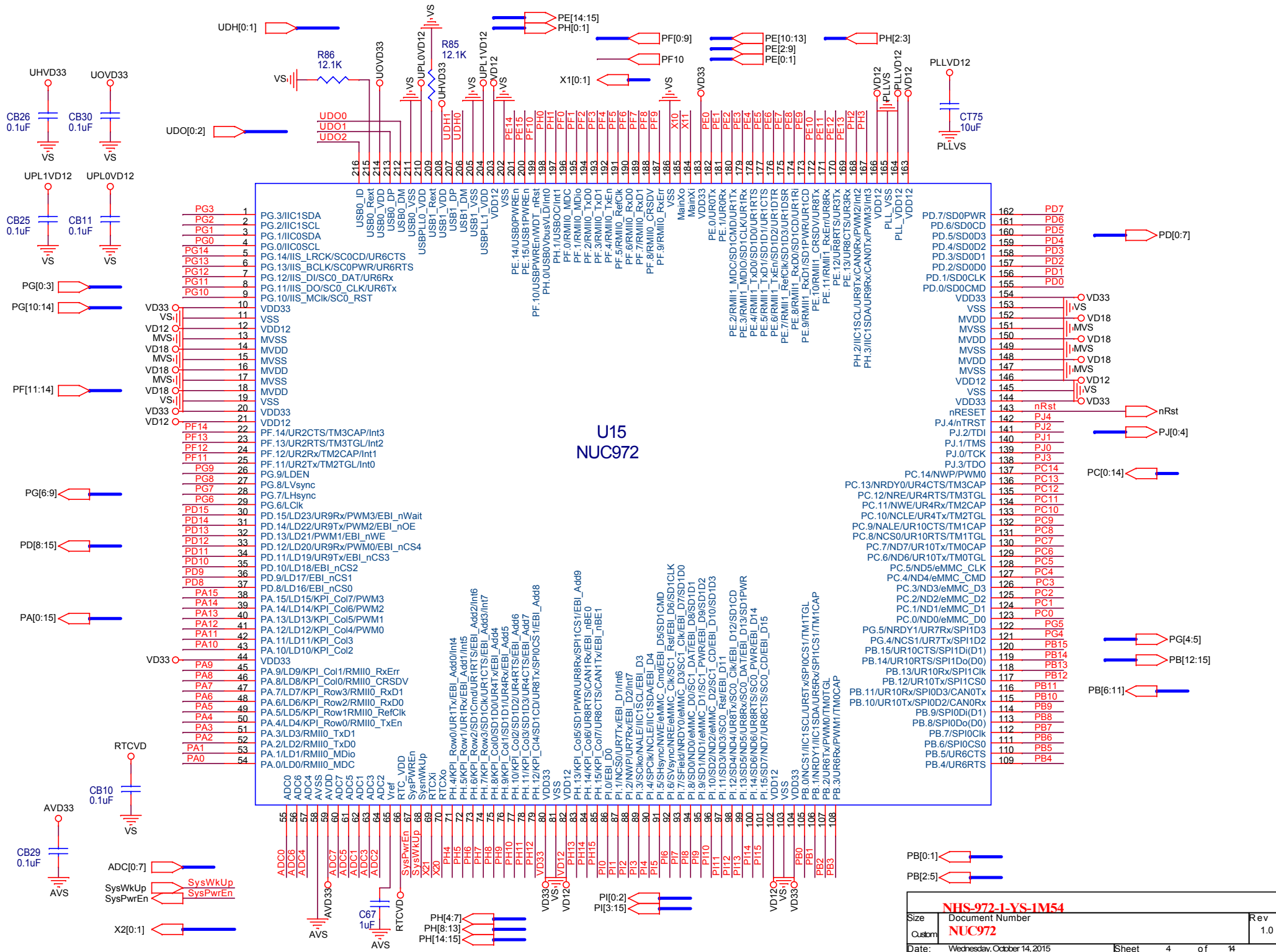


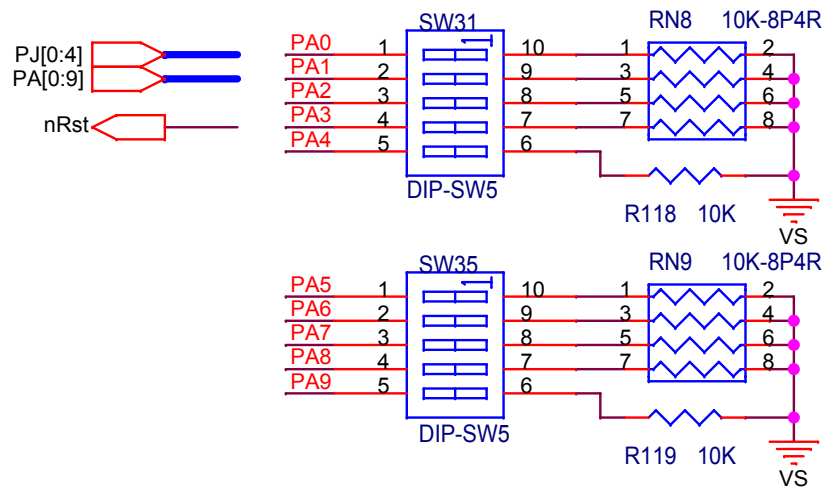
NHS-972-1-CY-1M54

Size	Document Number	Rev
Custom	Power Input, Crystal	1.0
Date:	Monday, October 19, 2015	Sheet 2 of 14



NHS-972-1-CY-1M54		
Size	Document Number	Rev
Custom	System Power	1.0
Date:	Wednesday, April 06, 2016	Sheet 3 of 14





Cfg1	Cfg0	Bootting from
ON	ON	USB.
ON	OFF	eMMC Flash.
OFF	ON	NAND Flash.
OFF	OFF	SPI Flash.

Cfg2	System Clock from
ON	12MHz crystal.
OFF	UPLL output.

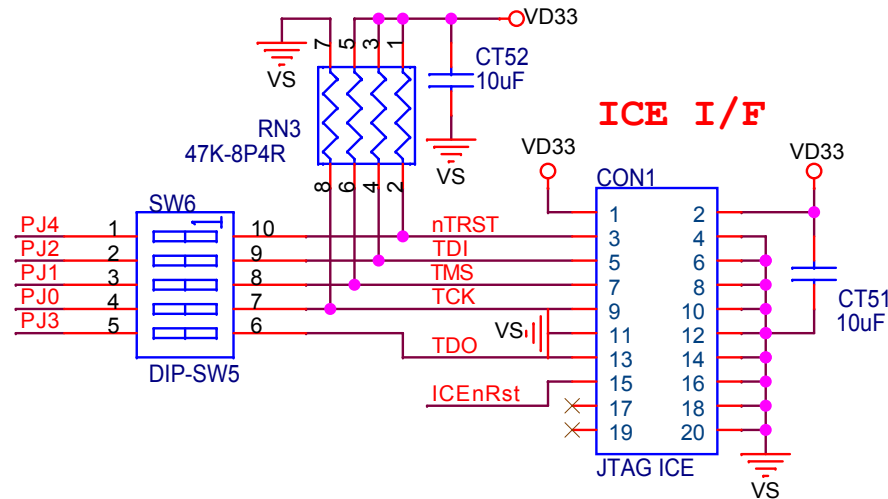
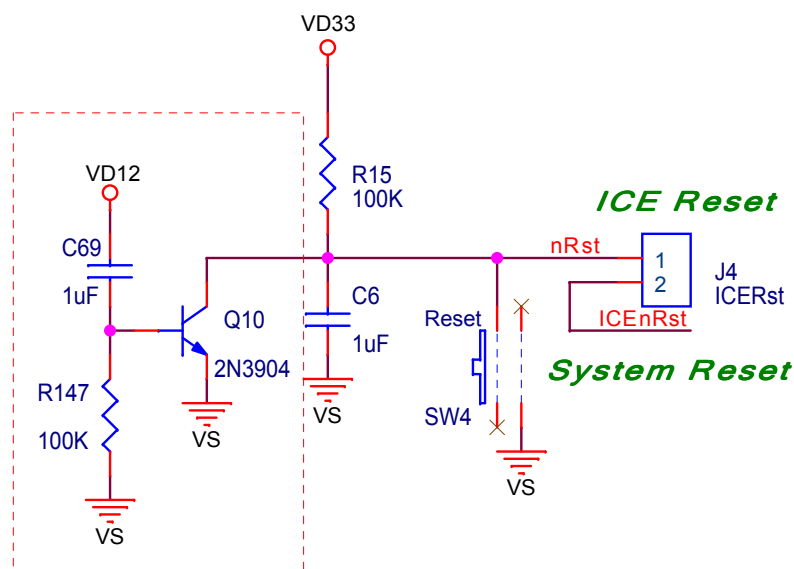
Cfg3	Watch Dog Setting
ON	OFF after power on.
OFF	ON after power on.

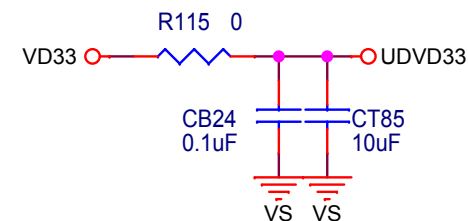
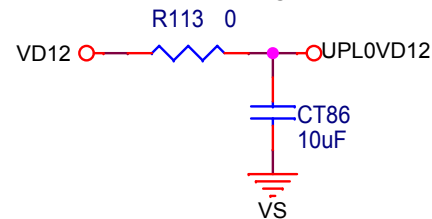
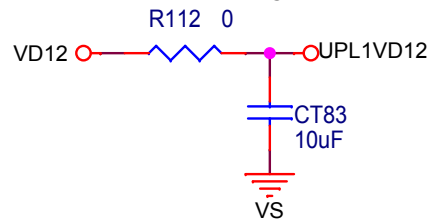
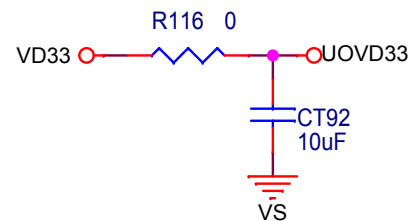
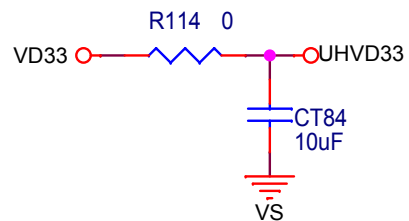
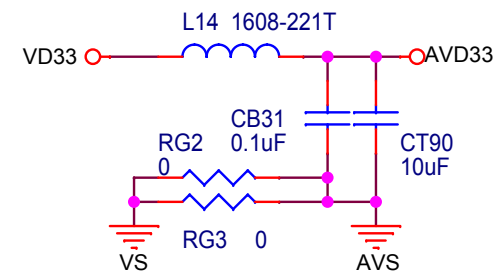
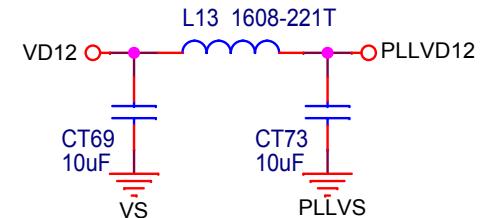
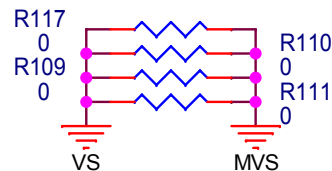
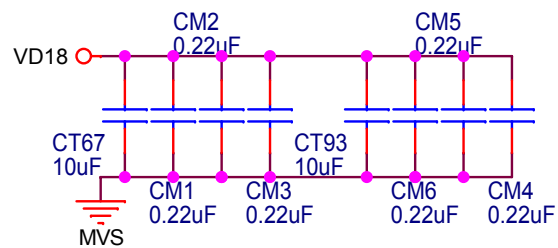
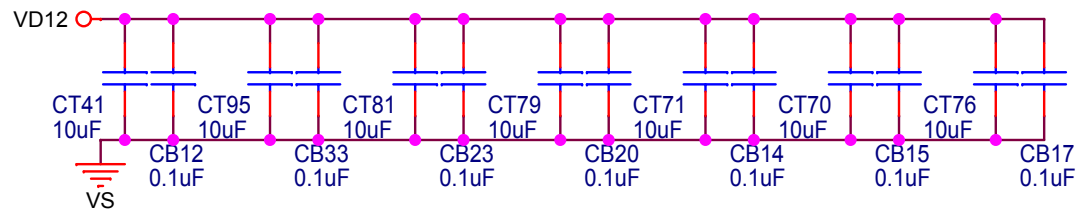
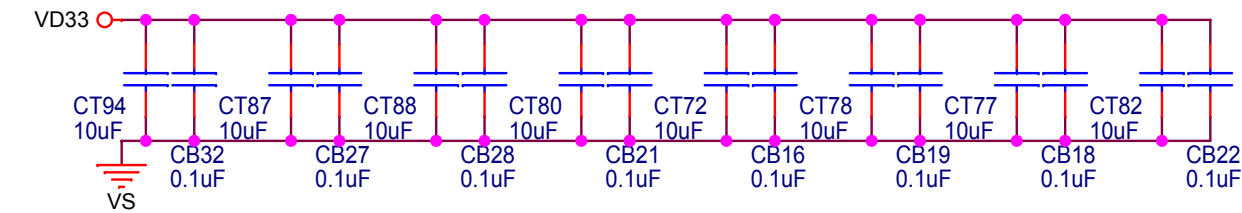
Cfg4	Setting PJ[4:0]
ON	As GPIO pin.
OFF	As JTAG interface.

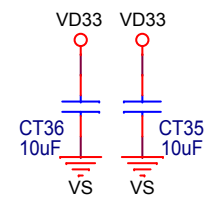
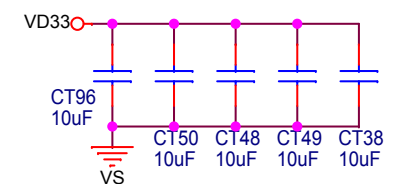
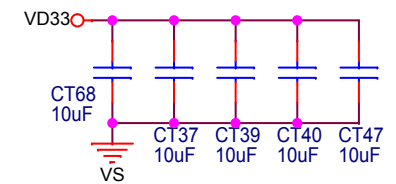
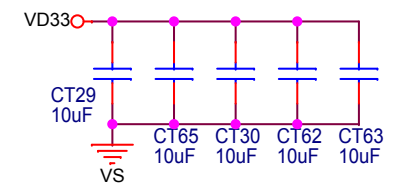
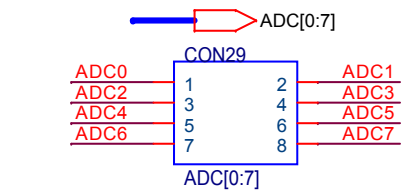
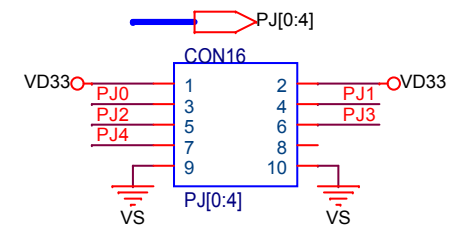
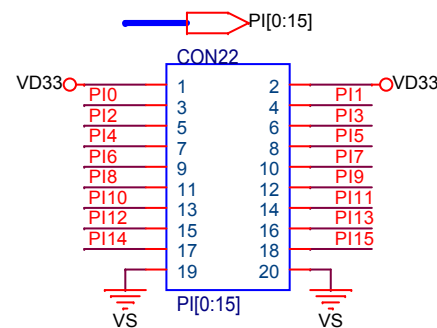
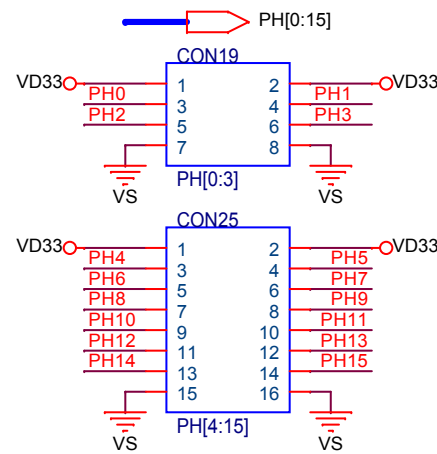
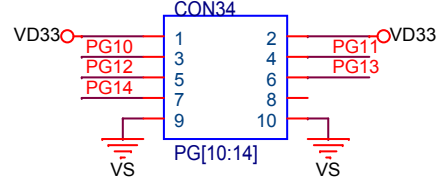
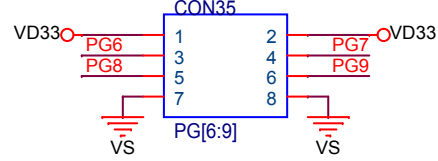
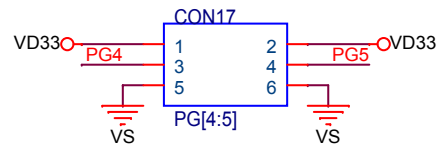
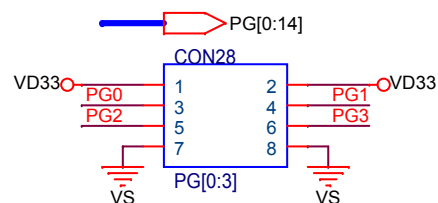
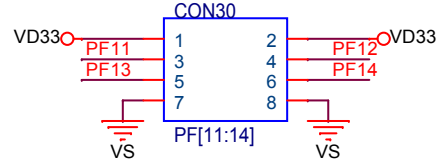
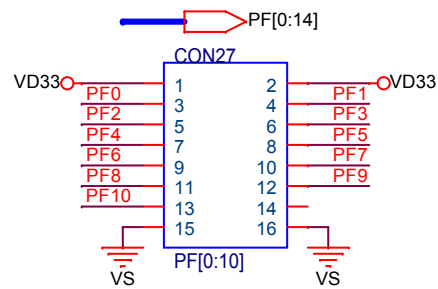
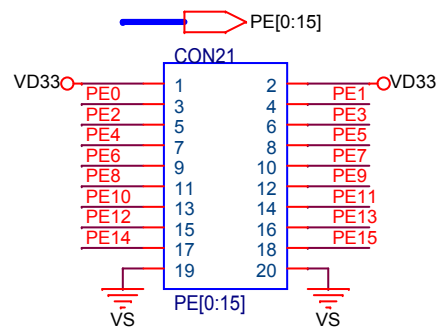
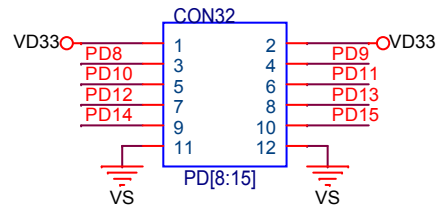
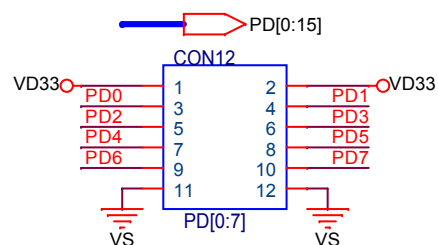
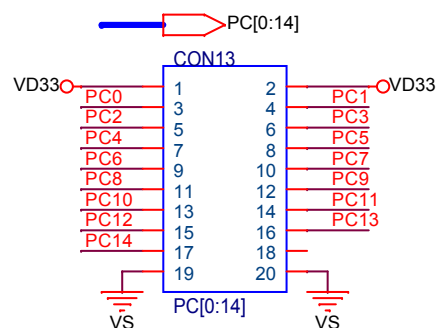
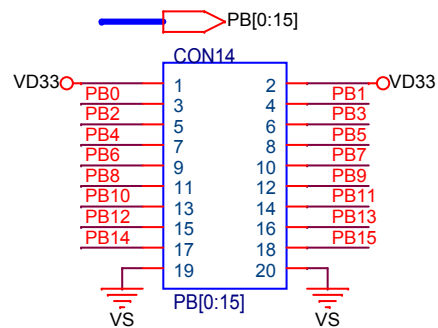
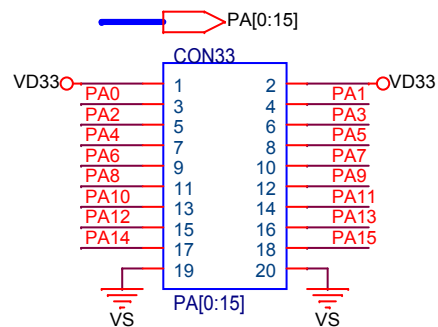
Cfg5	UART Debug Message
ON	Output turn ON.
OFF	Output turn OFF.

Cfg7	Cfg6	NAND Flash Page Size
ON	ON	2KB
ON	OFF	4KB
OFF	ON	8KB
OFF	OFF	None

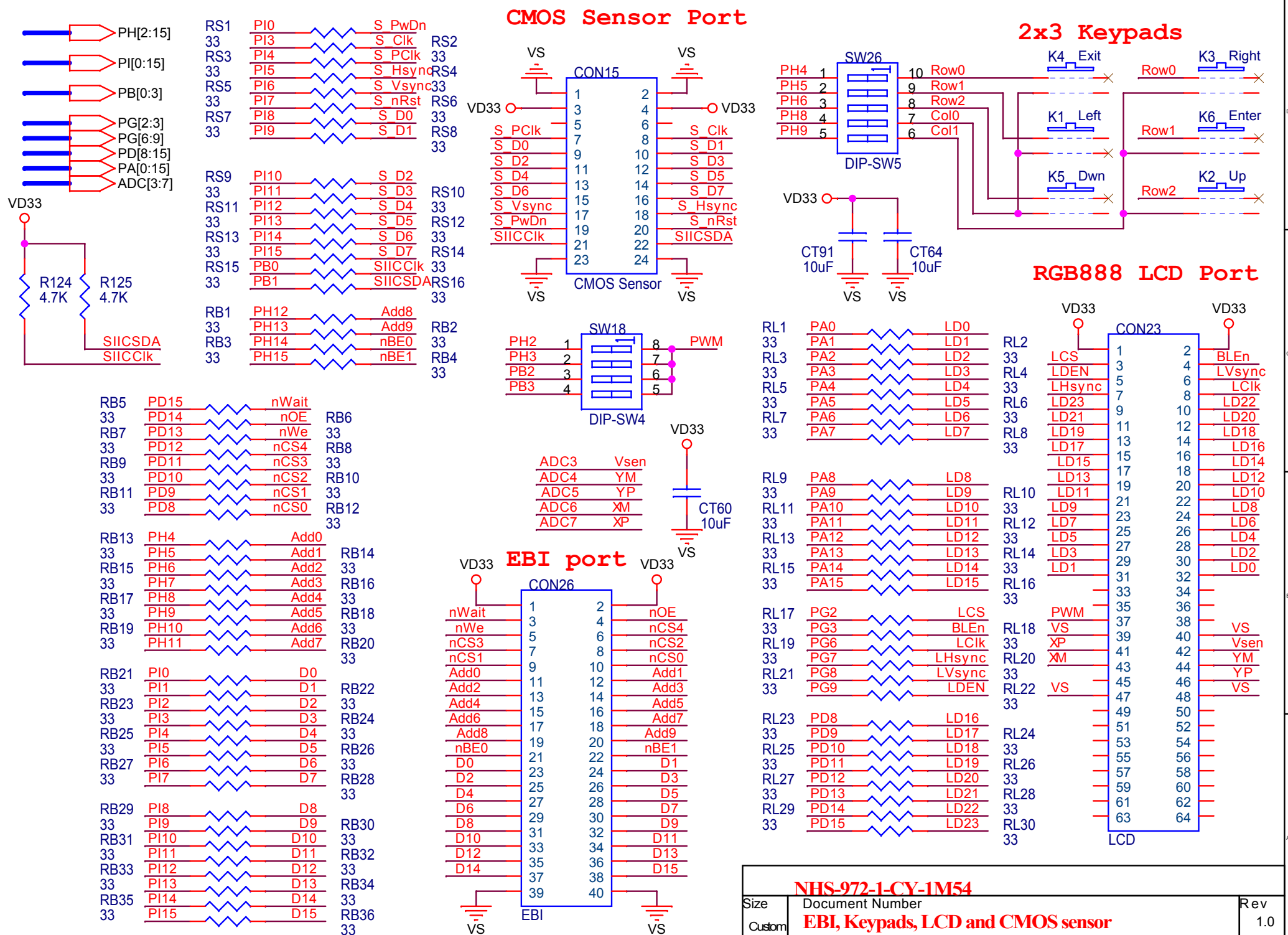
Cfg9	Cfg8	NAND Flash ECC type
ON	ON	BCH T12.
ON	OFF	BCH T15.
OFF	ON	BCH T24.
OFF	OFF	None





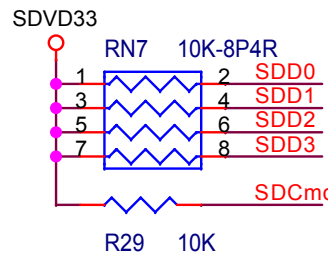
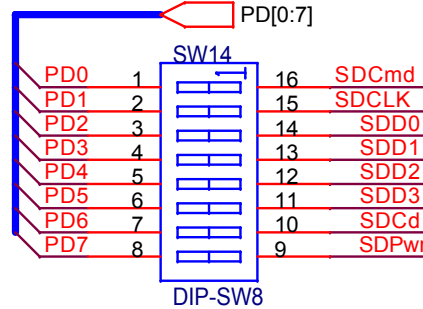
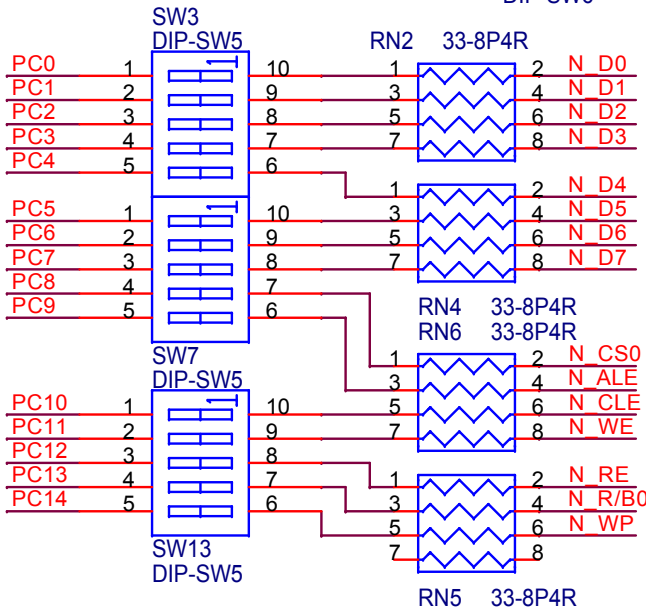
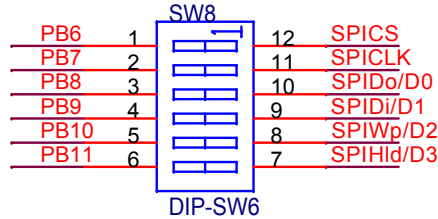




NHS-972-1-CY-1M54

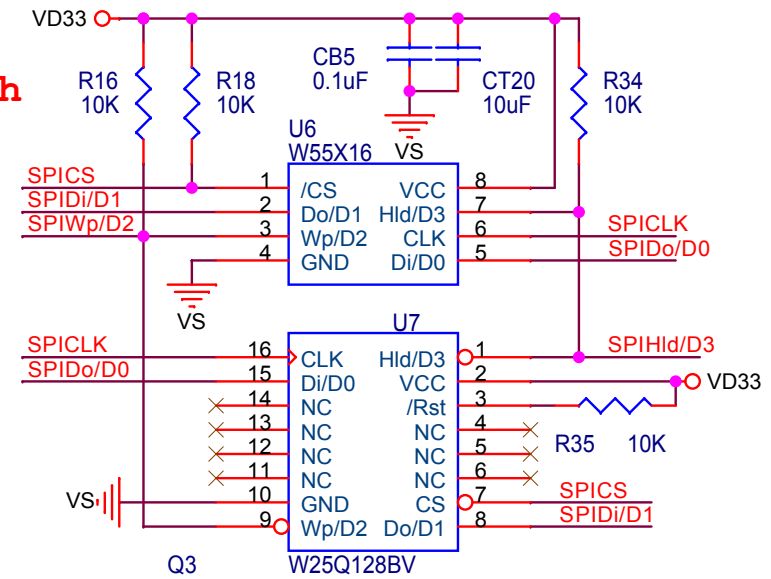


PB[6:11] 
PC[0:14] 

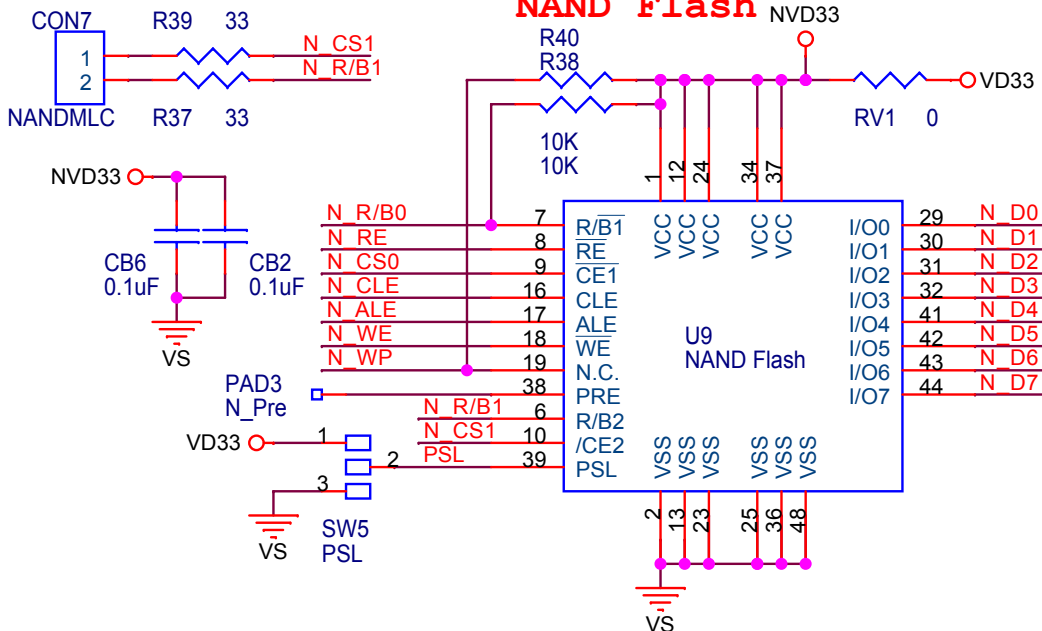


SPI Falsh

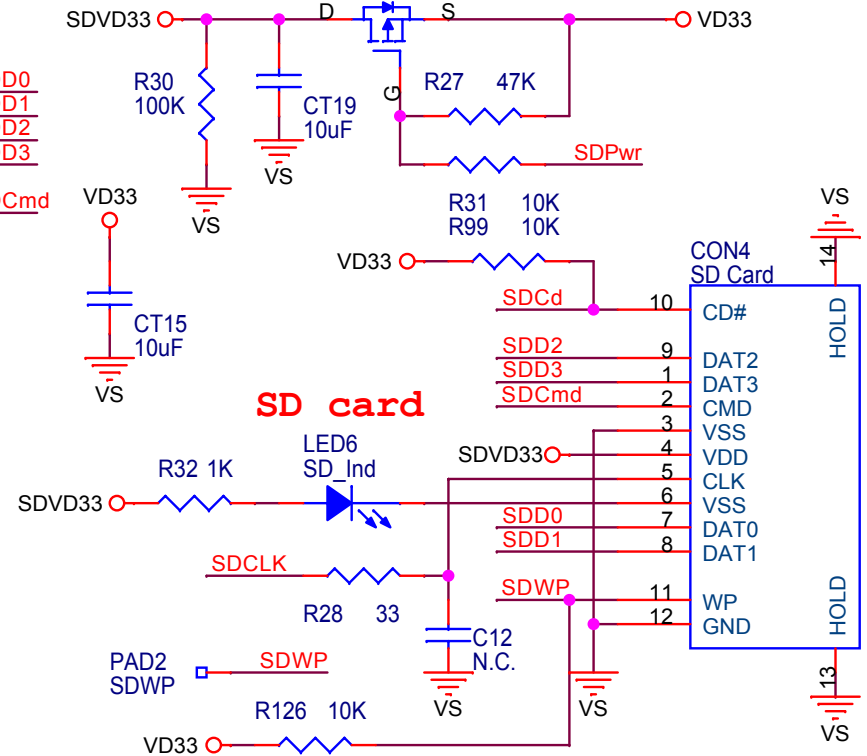
Note:
Only one SPI
Flash is used.



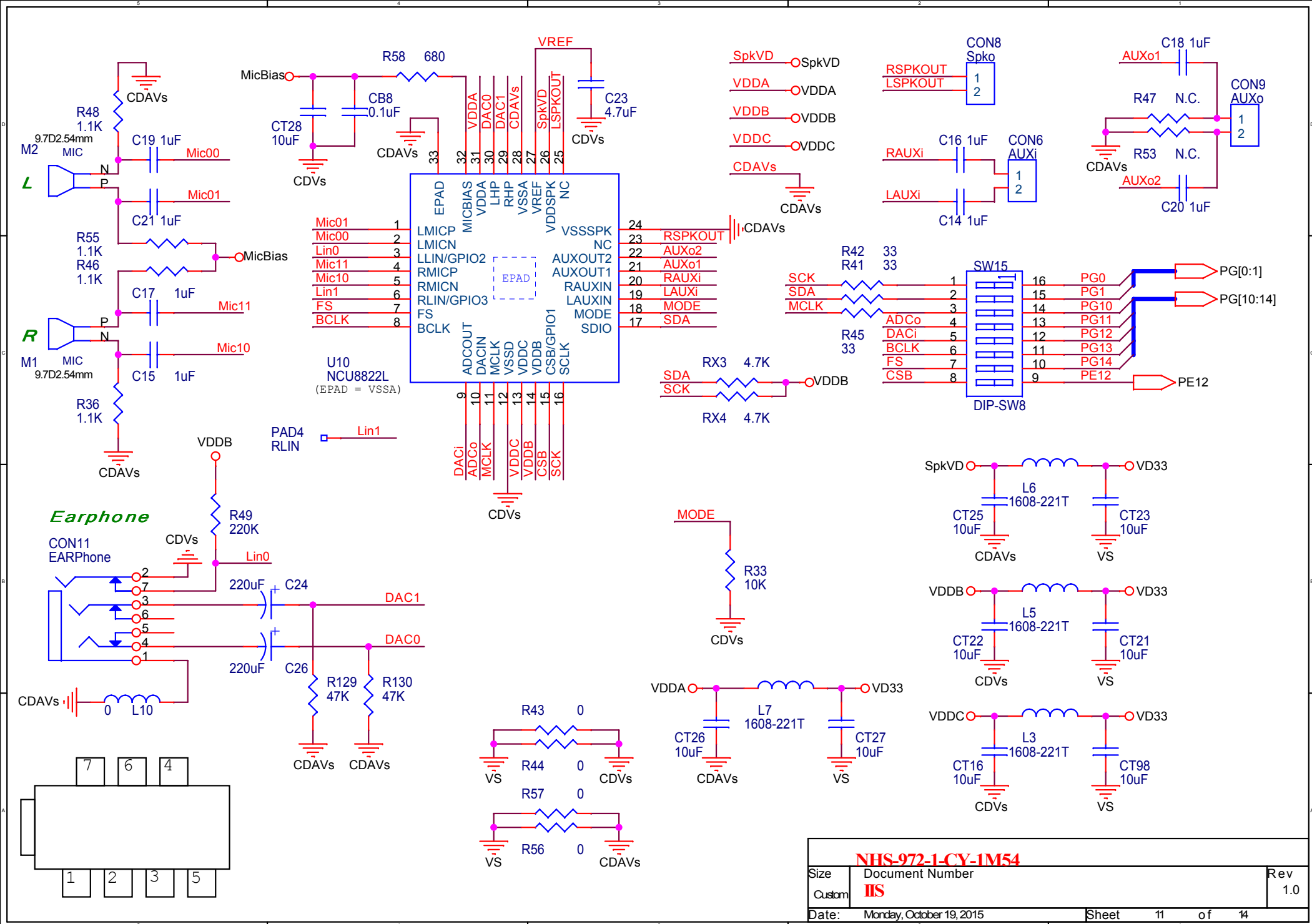
NAND Flash



SD card

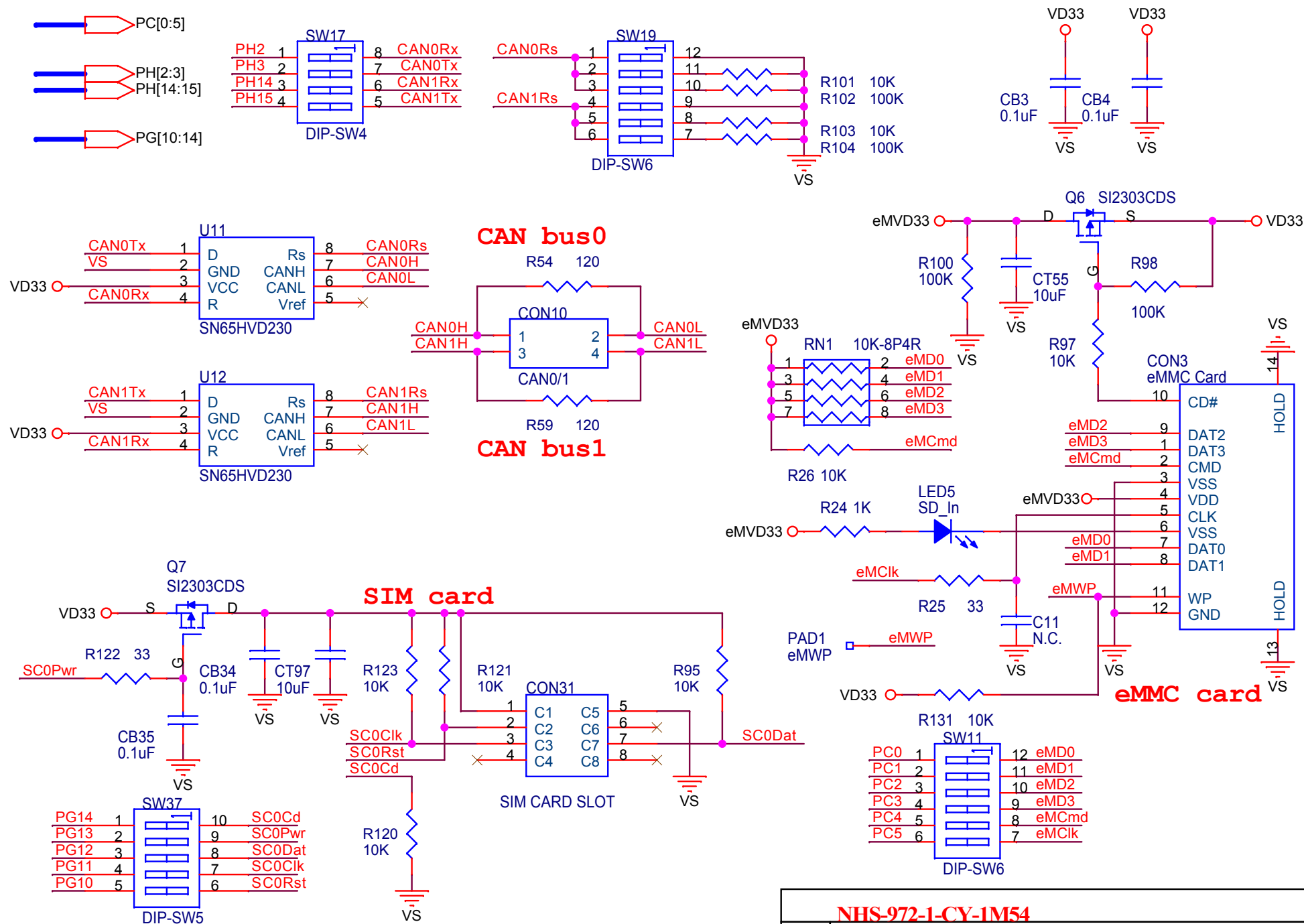


NHS-972-1-CY-1M54



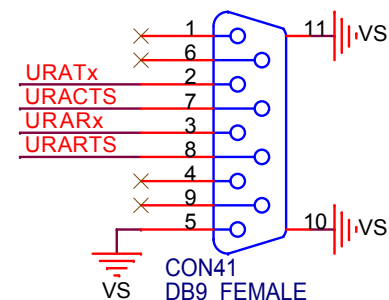
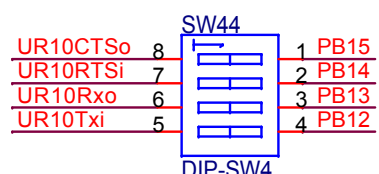
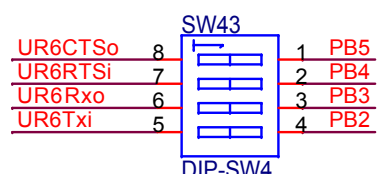
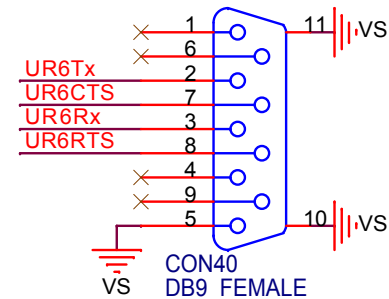
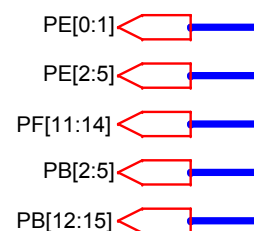
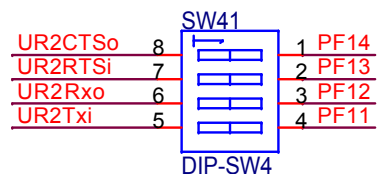
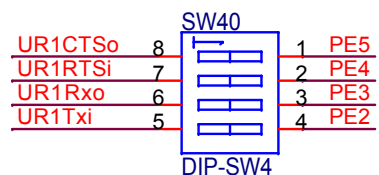
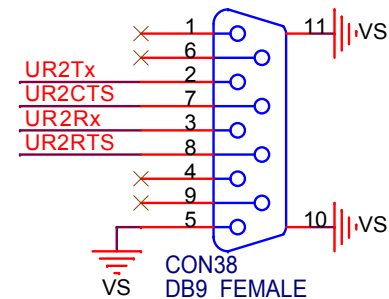
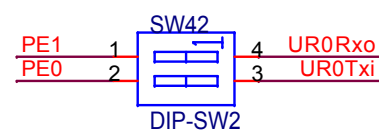
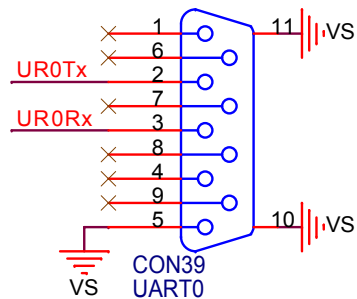
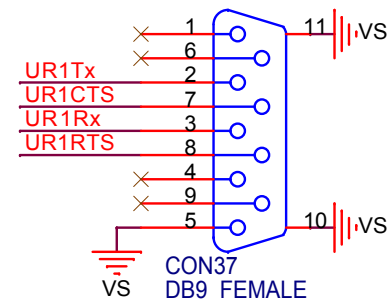
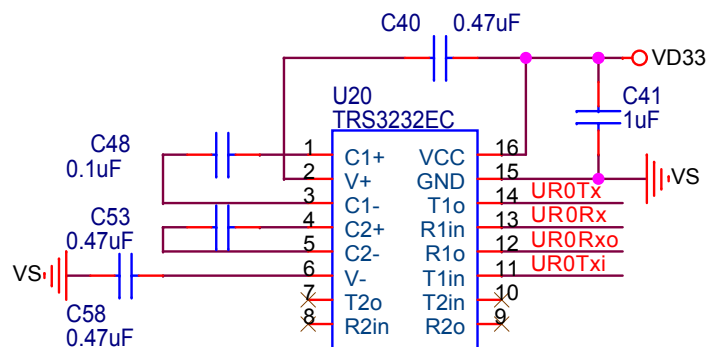
NHS-972-1-CY-1M54

Size	Document Number	Rev
Custom	IIS	1.0
Date:	Monday, October 19, 2015	Sheet 11 of 14



NHS-972-1-CY-1M54

Size	Document Number	Rev
Custom	CAN Bus, SIM and eMC Card	1.0
Date:	Monday, October 19, 2015	Sheet 12 of 14



Size	Document Number	Rev
Custom	UART	1.0
Date:	Monday, October 19, 2015	Sheet 13 of 14

