


# NUC977,NUC976 & NUC975 series Power Sequence



Nuvoton Technology Corp.

# Power Up/Down Sequence

- Power Up sequence

- Sequence:  $T_{33} = T_{18} = T_{12}$
- The time delay gap  $T_{33}$  with  $T_{12}$ , 0mS is recommend
- The time delay gap  $T_{18}$  with  $T_{12}$ , 0mS is recommend

- Power Down Sequence

- Sequence:  $T_{12} = T_{18} = T_{33}$

Note.

- $T_{12}$  means 1.2V powered time for Core & PLL
- $T_{18}$  means 1.8V powered time for MVDD
- $T_{33}$  means 3.3V powered time for digital IO and analog IO

# Recommended Connection for Power IC enable CTL of IO, MVDD & Core

