HISTO CODE PROBLEM: REDO

Support Slides

Hector 'Hectron' Williams

Histo Amazon

Send Mechanism

Setup pulses within *SEND* state

h

h_z

h_zz

Valid Setup Gen

0	1	2	3	4	5	6

Data Stream Setup Gen

0	1	2	3	4	5	6

Data Bit Stream RCVD by DUT

0	1	2	3	4	5	6

Data Bit Stream RCVD by DUT

Data received by HISTOGRAM CIRCUIT on next clk edge!

Histogram Circuit

DUT processes/handles data at each tick

Histo Amazon
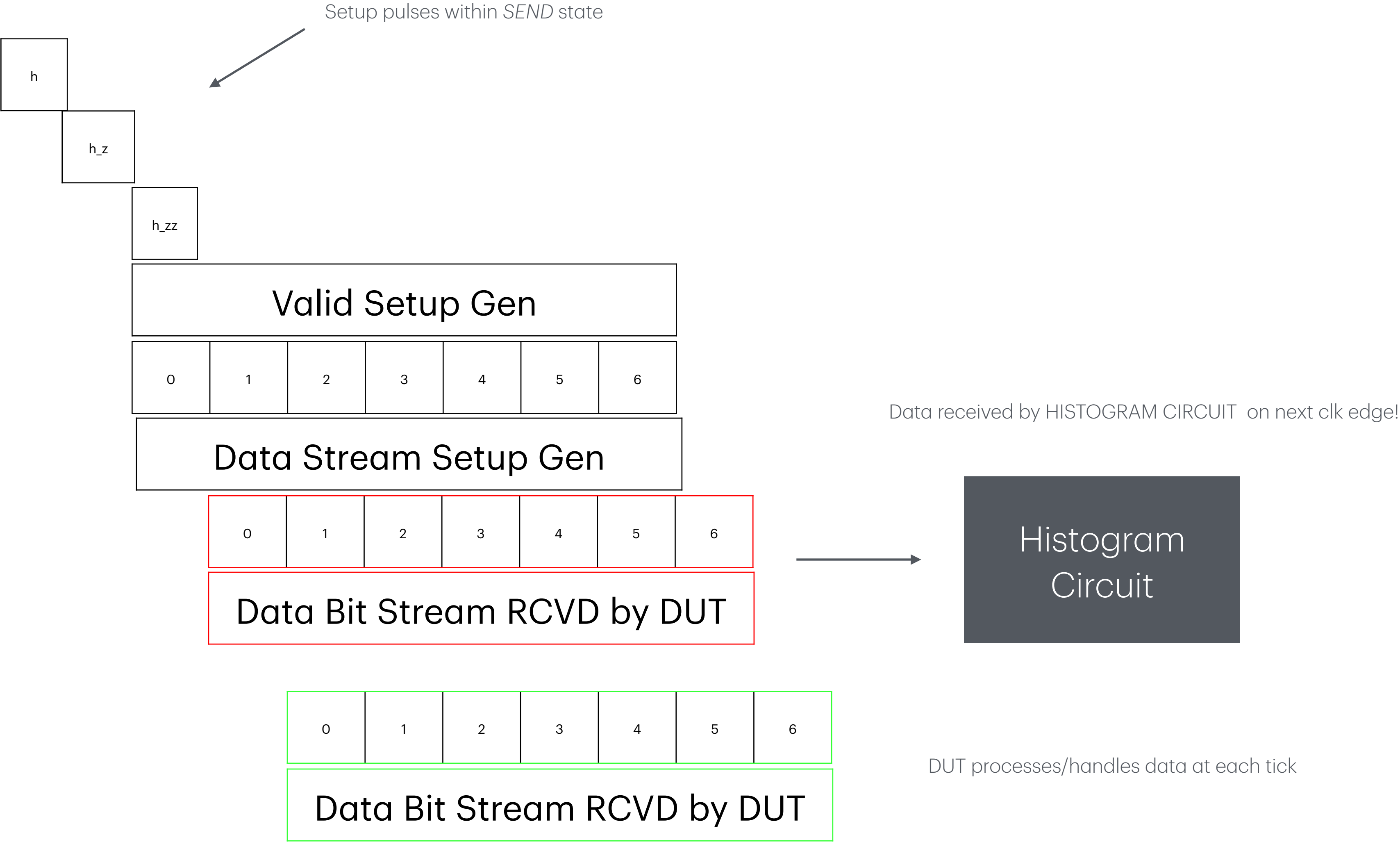
Receive Mechanism (Test Address)

Setup pulses

h

h_z

Address 0	Address 1	Address 2	Address 3	Address N

Testbench Step T-0

Address 0	Address 1	Address 2	Address 3	Address N

Testbench Step T-1 (Received by DUT)

H(0)	H(1)	H(2)	H(3)	H(4)

Testbench Step T-2 (Response from DUT)