

1. Description

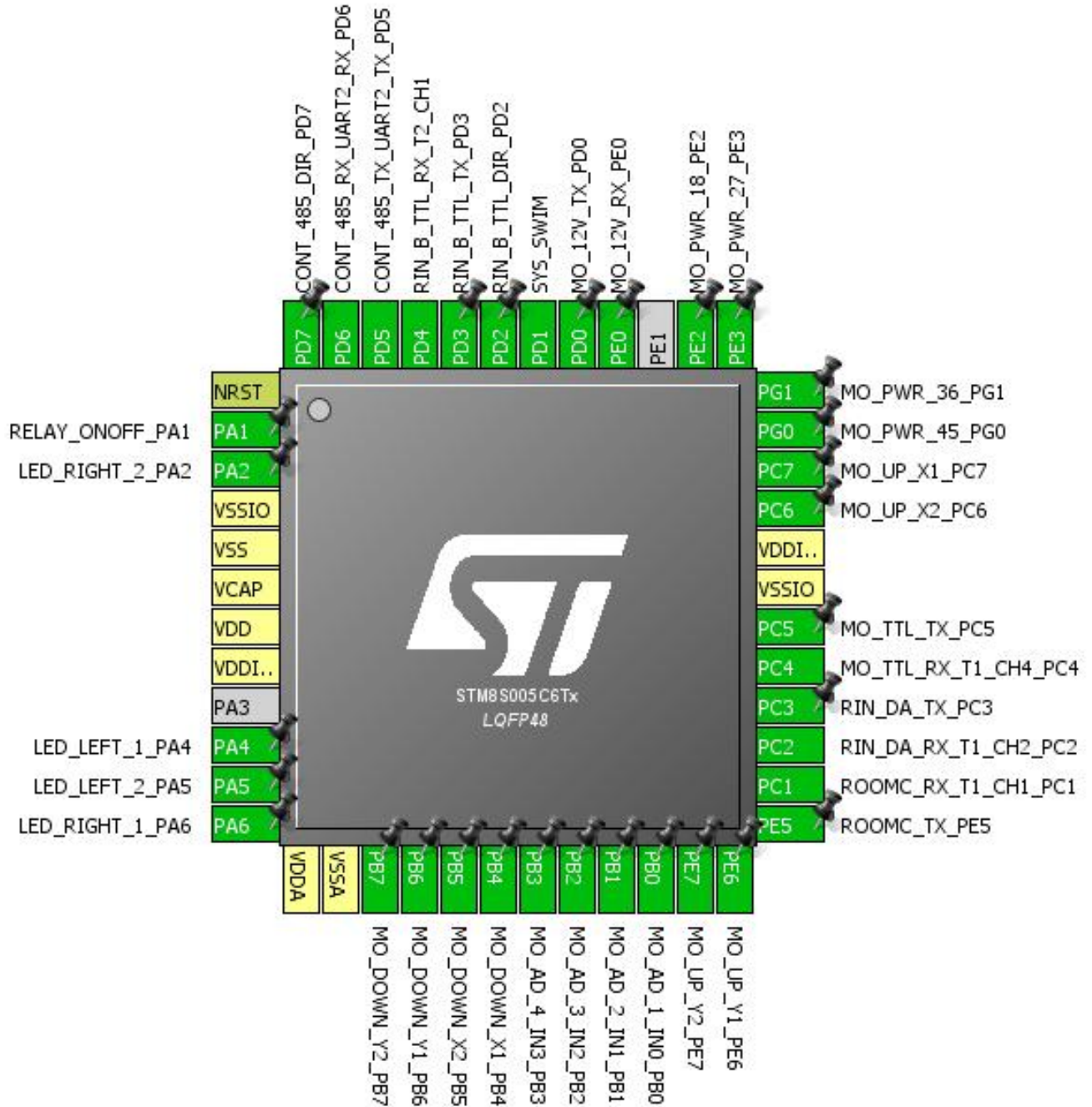
1.1. Project

Project Name	stm8s005c6_conv
Board Name	No information
Generated with:	STM8CubeMX 1.3.0
Date	08/03/2018

1.2. MCU

MCU Series	STM8S
MCU Line	STM8S003/005/007 Value Line
MCU name	STM8S005C6Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



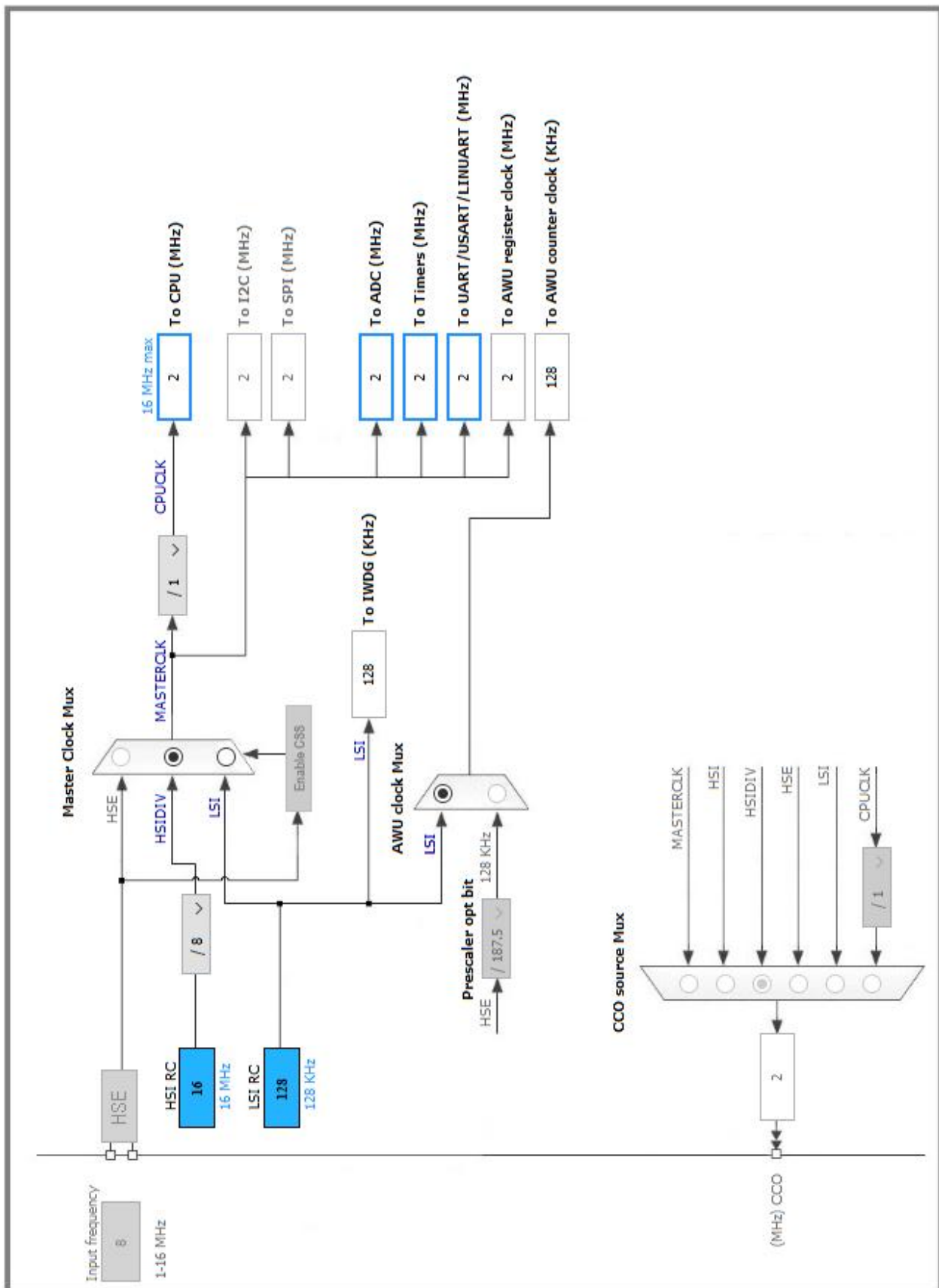
3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	NRST	Reset		
2	PA1 *	I/O	GPIO_Output	RELAY_ONOFF_PA1
3	PA2 *	I/O	GPIO_Output	LED_RIGHT_2_PA2
4	VSSIO	Power		
5	VSS	Power		
6	VCAP	Power		
7	VDD	Power		
8	VDDIO	Power		
10	PA4 *	I/O	GPIO_Output	LED_LEFT_1_PA4
11	PA5 *	I/O	GPIO_Output	LED_LEFT_2_PA5
12	PA6 *	I/O	GPIO_Output	LED_RIGHT_1_PA6
13	VDDA	Power		
14	VSSA	Power		
15	PB7 *	I/O	GPIO_Output	MO_DOWN_Y2_PB7
16	PB6 *	I/O	GPIO_Output	MO_DOWN_Y1_PB6
17	PB5 *	I/O	GPIO_Output	MO_DOWN_X2_PB5
18	PB4 *	I/O	GPIO_Output	MO_DOWN_X1_PB4
19	PB3	I/O	ADC_IN3	MO_AD_4_IN3_PB3
20	PB2	I/O	ADC_IN2	MO_AD_3_IN2_PB2
21	PB1	I/O	ADC_IN1	MO_AD_2_IN1_PB1
22	PB0	I/O	ADC_IN0	MO_AD_1_IN0_PB0
23	PE7 *	I/O	GPIO_Output	MO_UP_Y2_PE7
24	PE6 *	I/O	GPIO_Output	MO_UP_Y1_PE6
25	PE5 *	I/O	GPIO_Output	ROOMC_TX_PE5
26	PC1	I/O	TIM1_CH1	ROOMC_RX_T1_CH1_PC1
27	PC2	I/O	TIM1_CH2	RIN_DA_RX_T1_CH2_PC2
28	PC3 *	I/O	GPIO_Output	RIN_DA_TX_PC3
29	PC4	I/O	TIM1_CH4	MO_TTL_RX_T1_CH4_PC4
30	PC5 *	I/O	GPIO_Output	MO_TTL_TX_PC5
31	VSSIO	Power		
32	VDDIO	Power		
33	PC6 *	I/O	GPIO_Output	MO_UP_X2_PC6
34	PC7 *	I/O	GPIO_Output	MO_UP_X1_PC7
35	PG0 *	I/O	GPIO_Output	MO_PWR_45_PG0
36	PG1 *	I/O	GPIO_Output	MO_PWR_36_PG1
37	PE3 *	I/O	GPIO_Output	MO_PWR_27_PE3

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
38	PE2 *	I/O	GPIO_Output	MO_PWR_18_PE2
40	PE0 *	I/O	GPIO_Input	MO_12V_RX_PE0
41	PD0 *	I/O	GPIO_Output	MO_12V_TX_PD0
42	PD1	I/O	SYS_SWIM	
43	PD2 *	I/O	GPIO_Output	RIN_B_TTL_DIR_PD2
44	PD3 *	I/O	GPIO_Output	RIN_B_TTL_TX_PD3
45	PD4	I/O	TIM2_CH1	RIN_B_TTL_RX_T2_CH1
46	PD5	I/O	UART2_TX	CONT_485_TX_UART2_TX _PD5
47	PD6	I/O	UART2_RX	CONT_485_RX_UART2_RX _PD6
48	PD7 *	I/O	GPIO_Output	CONT_485_DIR_PD7

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

Series	STM8S
Line	STM8S003/005/007 Value Line
MCU	STM8S005C6Tx
Datasheet	22186_Rev4

5.2. Parameter Selection

Temperature	25
Vdd	null