



Hiral Shah


Software Engineer

About Me

Hi, I am a strong engineering professional with a Master of Technology - MTech focused in Software Engineering.

- Software Engineer with 6+ years of experience in software design and development .
- Having a good grasp of Data Structures and Algorithms with hands-on experience in C and C++ programming.
- Good understanding of Computer Networking protocols, Computer architecture.
- Working Experience of Python Scripting for Automation.
- Working Experience of Image processing in python.
- Hands on tools like perforce, GDB, source insight, visual studio, atom, DOORS, Coverty, Enterprise Architect, Git etc

Bio

 Age	26
 Email	heer141195@gmail.com
 Phone	+91-7046299304
 Location	Hydrabad, Telangana

Professional Skills

C / C++



Python



Linux Platform



Data Structure & Algorithm Design

ADVANCE

Design patters

ADVANCE

Perforce

MASTER

Software Engineering Methodology : SAFE & Agile

INTERMEDIATE

HTML/CSS/JS

INTERMEDIATE

AWS

BEGINNER

Data Science / Machine Learning / AI

BEGINNER

Work Experience

Software Engineer II at F5 Inc.

Sep 2021 – Present

- Owning and managing multiple F5 components.
- Designing, implementing and managing creative solutions specific to owned modules.
- Debugging and fixing complex issues / bugs based on customer service requests.
- **Automation Scripting**
 - Python automation scripting for testing and verification.

Software Engineer I at F5 Inc.

Feb 2020 - Sep 2021

- Debugging complex design flows based on customer service requests
- Designing and implementing customer specific RFEs.
- Designing and implementing network security and load balancing related extensions for F5 network appliances in TCL and nodejs.

Software Engineer at LG Soft India

May 2017 - Feb 2020

- Implementation of Diagnostic service [using UDS protocol] on Application processor[Xilinx board] which is connected to MCU.
- SW building, flashing and verification on Xilinx PETALinux boards.
- SW integration and verification
- Work Closely with HQ team members in Seoul for Architecture Design and implementation related Activities.
- Debugging board crash issues and MISRA C / Cpp Issue with the help of different log analysis techniques and tools such as Coverty.

- **Automation Scripting**
 - Implemented Python Script to automate SQT process
 - worked on an image processing module for automation of verification processes.
- **Onsite Experience** (2 business trips to Seoul, Korea)
 - Work with HQ Detailed Design team to develop low-level design and implementation of software units.

Education

MTech in Software Systems from Birla Institute of Technology and Science, Pilani (WILP)

2019 - 2021

- Build up strong foundation in software development methods and best practices.
- Lerner to understand and analys requirements of large software systems to design, develop & manage them in an effective manner.
- Completed the course with 7.7 CGPA

Bachelor of Information Technology from Gujarat Technical University, Ahmedabad

2013 - 2017

- Build up strong foundation for programming lenguges like C, CPP , JAVA, .NET and diffrent web technologies like html, css, js etc.
- worked on diffrent projects to gain practice hands on diffrent technologies
- Completed the course with 8.77 CGPA

HSC (Maths, Physics & Chemistry) from GSHEB

1995 - 2007

- Gained deep undertanding of diffrent concepts of Maths , Physics and Chemistry
- Completed the course with 89%

Testimonials



Atul Singh

Senior RTL Design Engineer at SeviTech Systems Pvt. Ltd.

“ I have had pleasure of knowing Hiral for more than 2 years during which we have worked on same project. Her commitment towards work is something I have always appreciated.



Hishak kv

Senior Test Engineer at TestingXperts Pvt Ltd

“ A great colleague and active professional. Proactive.

[View more on LinkedIn](#)

Get in Touch!

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