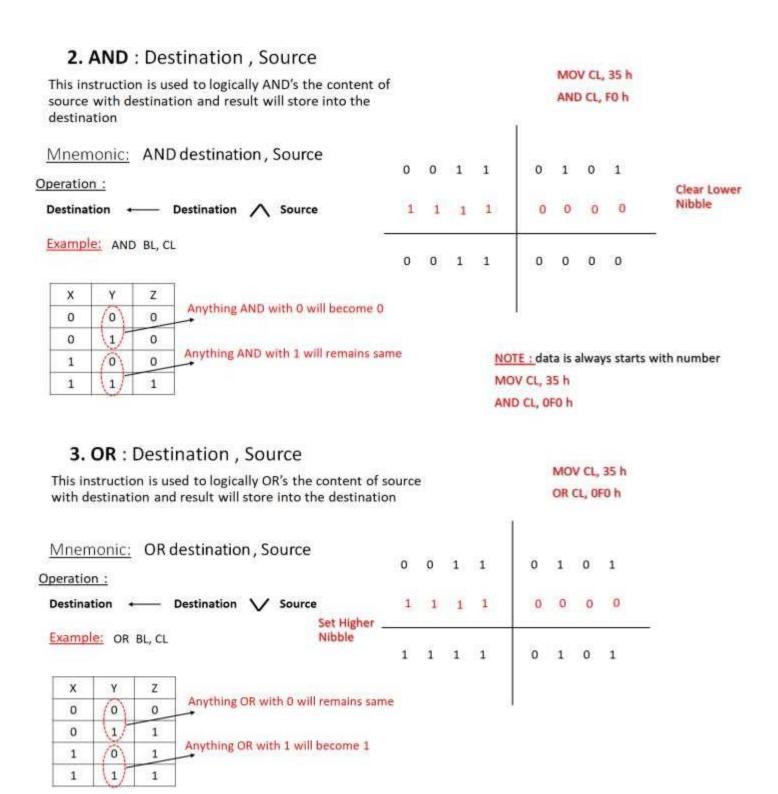
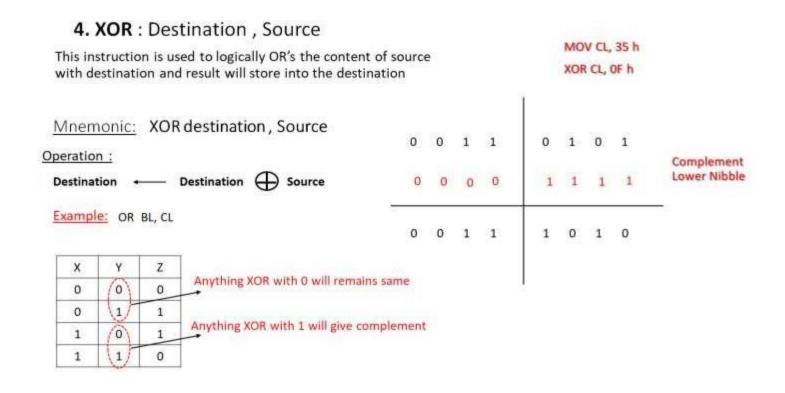
| Experiment No: 03 | Date : Roll No: | | | | | | | | | |
|---|---------------------------|-----------|-------|--------|-------|--------|-------|---|--|--|
| Aim: To find 2's complement using logical instructions Theory: | of 8086 | i. | | | | | | | | |
| Logical Instructions: | | | | | | | | | | |
| Logical instructions are used to perform the bit op instructions of 8086 processor: | eratio | n. Fo | ollow | ving a | re th | ne log | gical | | | |
| 1. NOT | | | | | | | | | | |
| 2. TEST | | | | | | | | | | |
| 3. AND | | | | | | | | | | |
| 4. OR | | | | | | | | | | |
| 5. X-OR | | | | | | | | | | |
| | | | | | | | | | | |
| 1. NOT : Destination | | | | | | | | | | |
| This instruction forms the 1's complement of destination and result stores into destination | Before Execution CL= 01 h | | | | | | | | | |
| Mnemonic: NOT destination | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | | |
| Operation : | | | | | | | | | | |
| Destination | After Execution CL= FE h | | | | | | | | | |
| 5905-1310 To-06-20-20 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | | |
| Example: MOV CL, 01h NOT CL | | | | | | | | | | |





Program 1: WAP to find 1's complement of given number using logical instruction

| Algorithm: | | |
|---------------------------|--|--|
| Flowchart : | | |
| | | |
| Program : | | |
| Steps to display Output : | | |

Output:

Program 2: WAP to find 2's complement of given number using logical instruction

| (Note: for 2's complement don't use NEG instruction) |
|--|
| Algorithm: |
| Flowchart: |
| |
| |
| Program : |
| |
| Steps to display Output : |
| Output: |
| |
| |
| |
| |
| |
| Conclusion: |