

*TITLE:- MODELING ELECTROMAGNETIC CHARGE SATURATION, AND AI
DRIVEN FAULT TOLERANCE IN ORBITING DYSON NODES*

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Abstract:- Solar Wind Consists of high-energy protons and Electrons from the Sun. During Solar FLux or CME(Coronal Mass Ejections), Particle Density Increases Exponentially , Causing;

- ★ *EMI(Electromagnetic Interference)]*
- ★ *Static Charge Accumulation*
- ★ *Component Burnout*
- ★ *Data Corruption in memory Buses.*

Nature of Solar Winds:-

<i>Features</i>	<i>Typical Value</i>
<i>Particle Energy</i>	<i>1 - 10 KeV</i>
<i>Speed</i>	<i>300 - 800 km/s</i>
<i>Burst Frequency</i>	<i>Random/ Flare Triggered</i>
<i>CME Intensity</i>	<i>1000x Background Levels</i>

The interaction with node Hardware occurs primarily, through Induction, Surface Charging and Electrons Disruption in Signal Buses.

Electromagnetic Modeling:- Let;

★ $\Phi_{sw} = \text{Particle Flux}(\text{particle}/\text{m}^2 * \text{s})$

★ $I_{induced} = \text{Induced Current due to particle}$

Interaction

★ $C_{node} = \text{Node's Charged Tolerance}$

We simulate;

$$I_{induced} = k * \Phi_{sw} * A * \cos(\theta)$$

Where., $A = \text{Area of Exposure}$

$\theta = \text{Impact Angle}$

$K = \text{Material Constant}$

Fallback occurs when.,

$$Q_{node} > Q_{max}$$

Node Charge Balanced Architecture:-

<i>SubSystem</i>	<i>Charge Tolerance</i>
<i>CPU Logic</i>	<i>50 μC</i>
<i>RAM Controller</i>	<i>30 μC</i>
<i>FLux Panels</i>	<i>80 μC</i>
<i>Capacitor Unit</i>	<i>100 μC</i>

Shielding Status (ON/ OFF) modifies Tolerance using;

$$Q_{eff} = Q_{raw} * (1 - S)$$

Where., S is Shielding Efficiency

Error & Noise Propagation:- High Flux Value Causes;

- ★ *Signal Decay Spikes(ΔT)*
- ★ *Memory Read / Error Write*
- ★ *CRC Failure*
- ★ *Thermal Spikes in Inductive Loops*

References:- ★ NASA Goddard: Space Weather Impact (2023)

★ ESA Solar particle Interaction models (2022)

★ EMI & Fault Tolerance in Space Nodes, “MIT AI LAB, 2024”