

	1	2	3	4	5	6
A	<div>Sheet: Power</div> <div>File: Power.sch</div>				<div>Sheet: ECBus</div> <div>File: EDBus.sch</div>	
B	<div>Sheet: Indications</div> <div>File: Indications.sch</div>		<div>Sheet: uController</div> <div>File: uController.sch</div>		<div>Sheet: PCMBus</div> <div>File: PCMBus.sch</div>	
C	<div>Sheet: Baseband</div> <div>File: Baseband.sch</div>	<div>Sheet: IO</div> <div>File: IO.sch</div>			<div>Sheet: Serial2WiFi</div> <div>File: Ser2WiFi.sch</div>	
D	<div>Sheet: I2C</div> <div>File: I2C.sch</div>	<div>Sheet: RS232</div> <div>File: RS232.sch</div>	<div>SOFTWARE: <a href="https://github.com/cstroie/Arabell300">https://github.com/cstroie/Arabell300</a> <a href="https://github.com/hegars/300BaudModem">https://github.com/hegars/300BaudModem</a> IN DEVELOPMENT <a href="https://pcb.hegars.com/">https://pcb.hegars.com/</a></div> <div>Sheet: / File: 300BaudModem.sch</div> <div><b>Title: 300 Baud Audio FSK Modem</b></div> <div><div>Size: A4</div><div>Date: 2021-07-08</div><div>Rev: 0.1</div></div> <div><div>KiCad E.D.A. kicad (5.1.9)-1</div><div>Id: 1/11</div></div>			
	1	2	3	4	5	6

RST\_Request

Signaling-RINGDOWN

Signaling-OFFHOOK

I2C-SDL

I2C-SDA

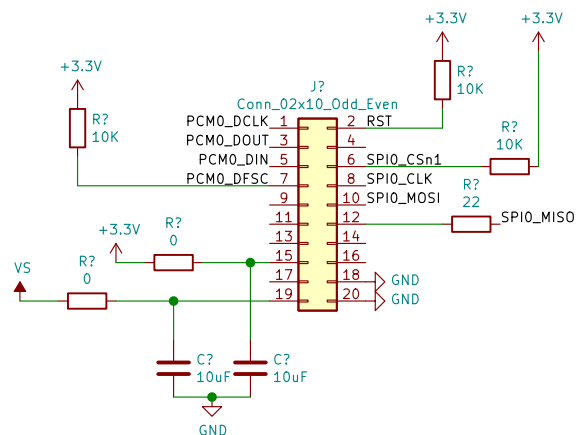
Baseband-OUT

Baseband-IN

Baseband to PCM transcoder

+5V  
GND

PCM0\_DFSC  
PCM0\_DIN  
PCM0\_DOUT  
PCM0\_DCLK



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /PCMBus/  
File: PCMBus.sch

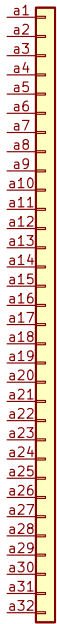
**Title: 300 Baud Audio FSK Modem**

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.9)-1

Rev: 0.1

Id: 2/11



J?  
DIN41612\_01x32\_A

SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /ECBus/  
File: ECBus.sch

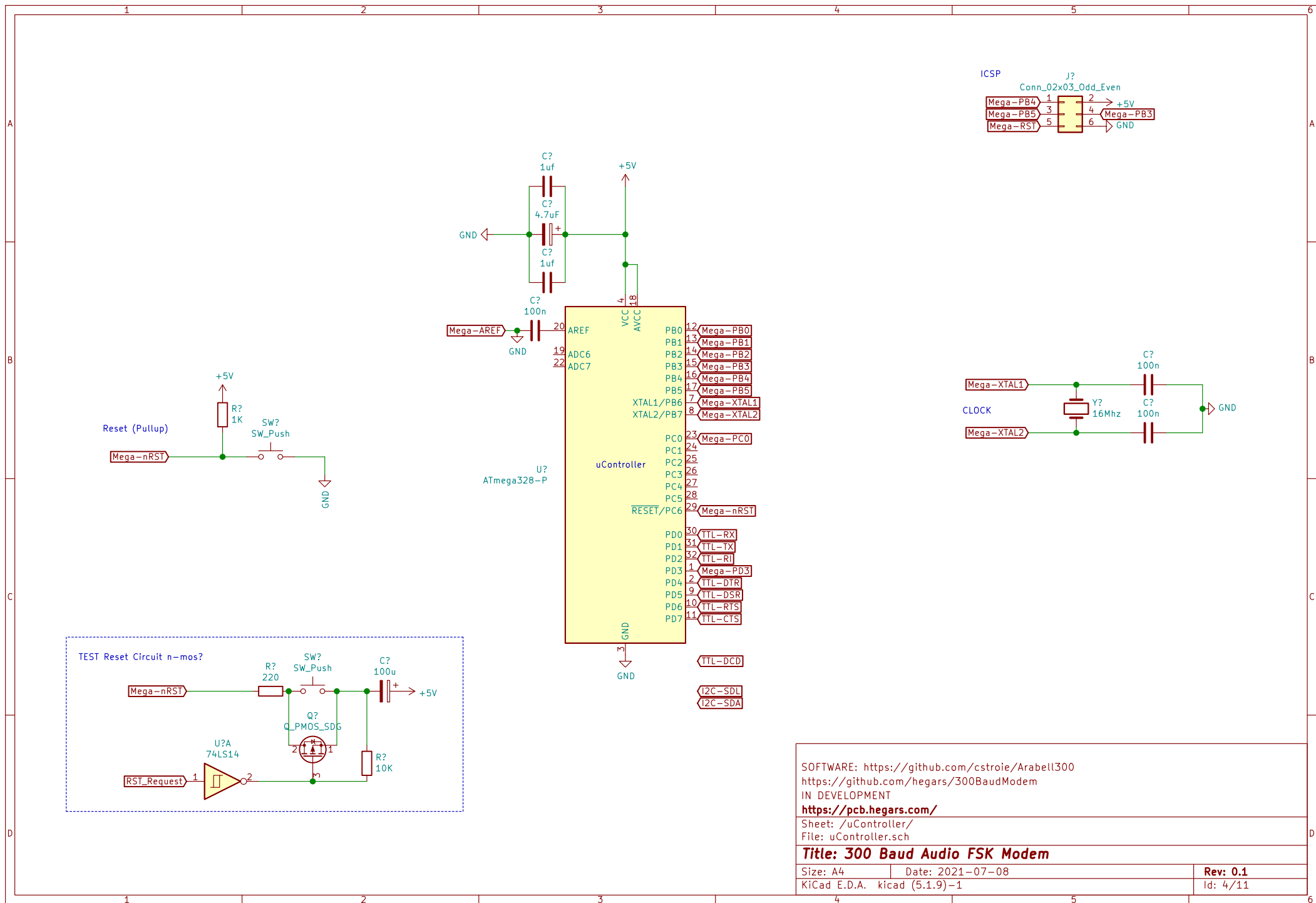
**Title: 300 Baud Audio FSK Modem**

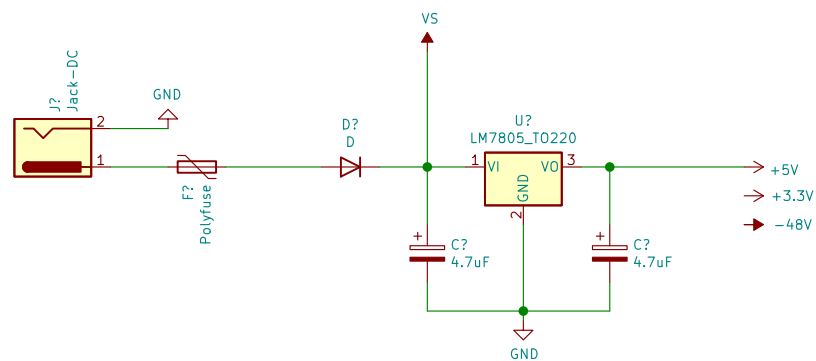
Size: A4      Date: 2021-07-08

KiCad E.D.A.    kicad (5.1.9)-1

**Rev: 0.1**

Id: 3/11





SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /Power/  
 File: Power.sch

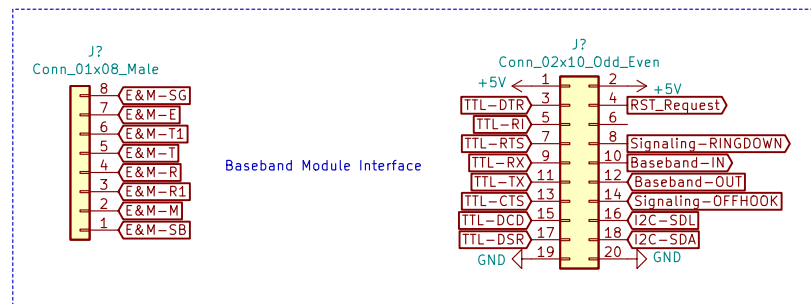
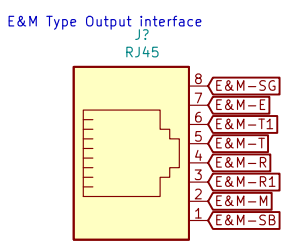
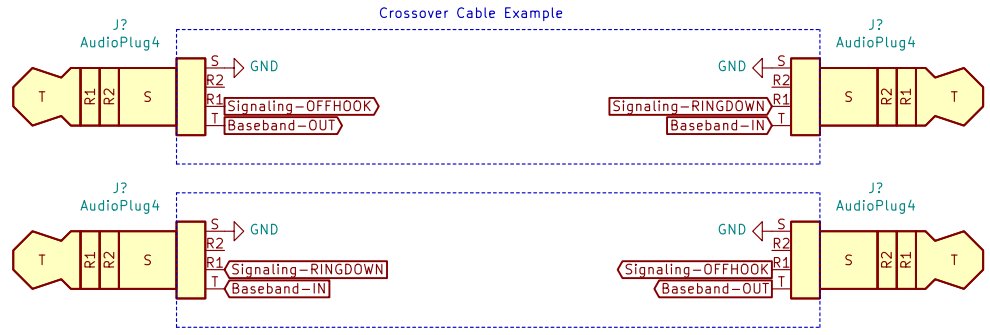
**Title: 300 Baud Audio FSK Modem**

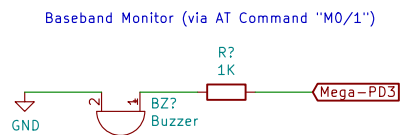
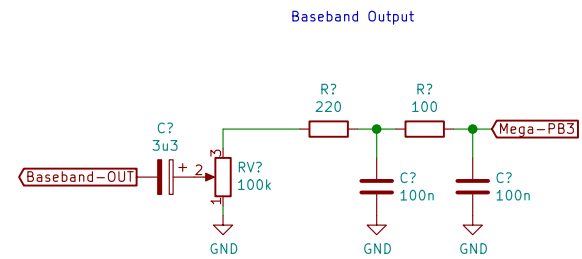
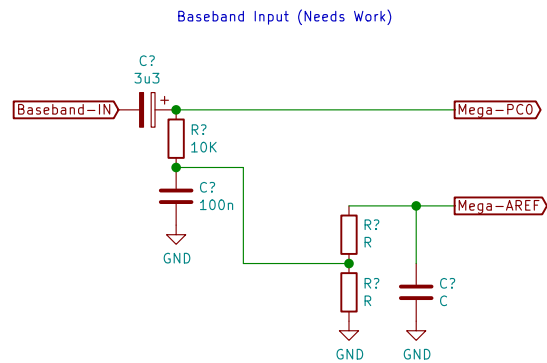
Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.9)-1

Rev: 0.1

Id: 5/11





SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /Baseband/  
 File: Baseband.sch

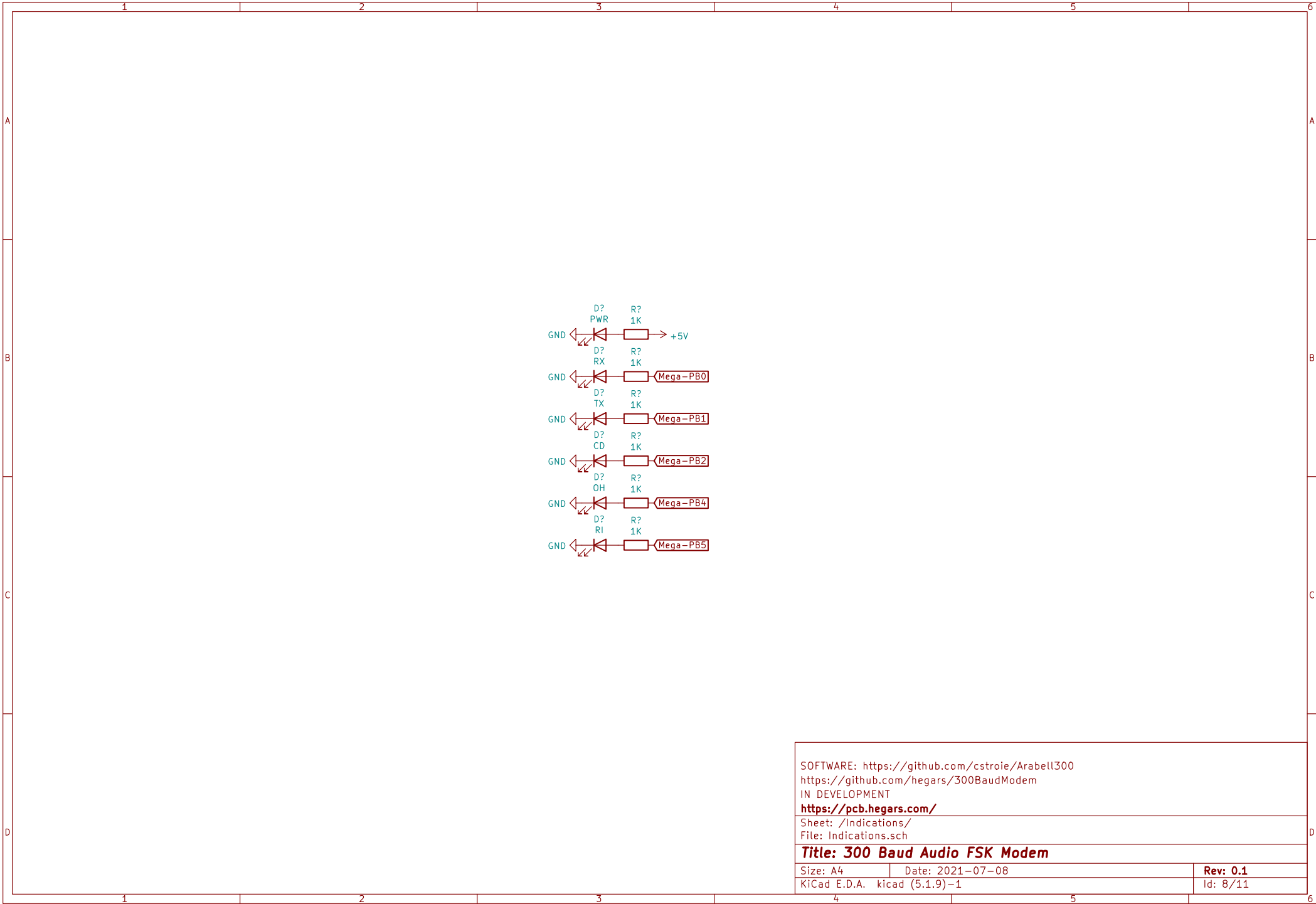
**Title: 300 Baud Audio FSK Modem**

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.9)-1

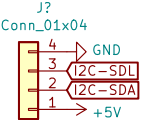
**Rev: 0.1**

Id: 7/11





MAC Address Module

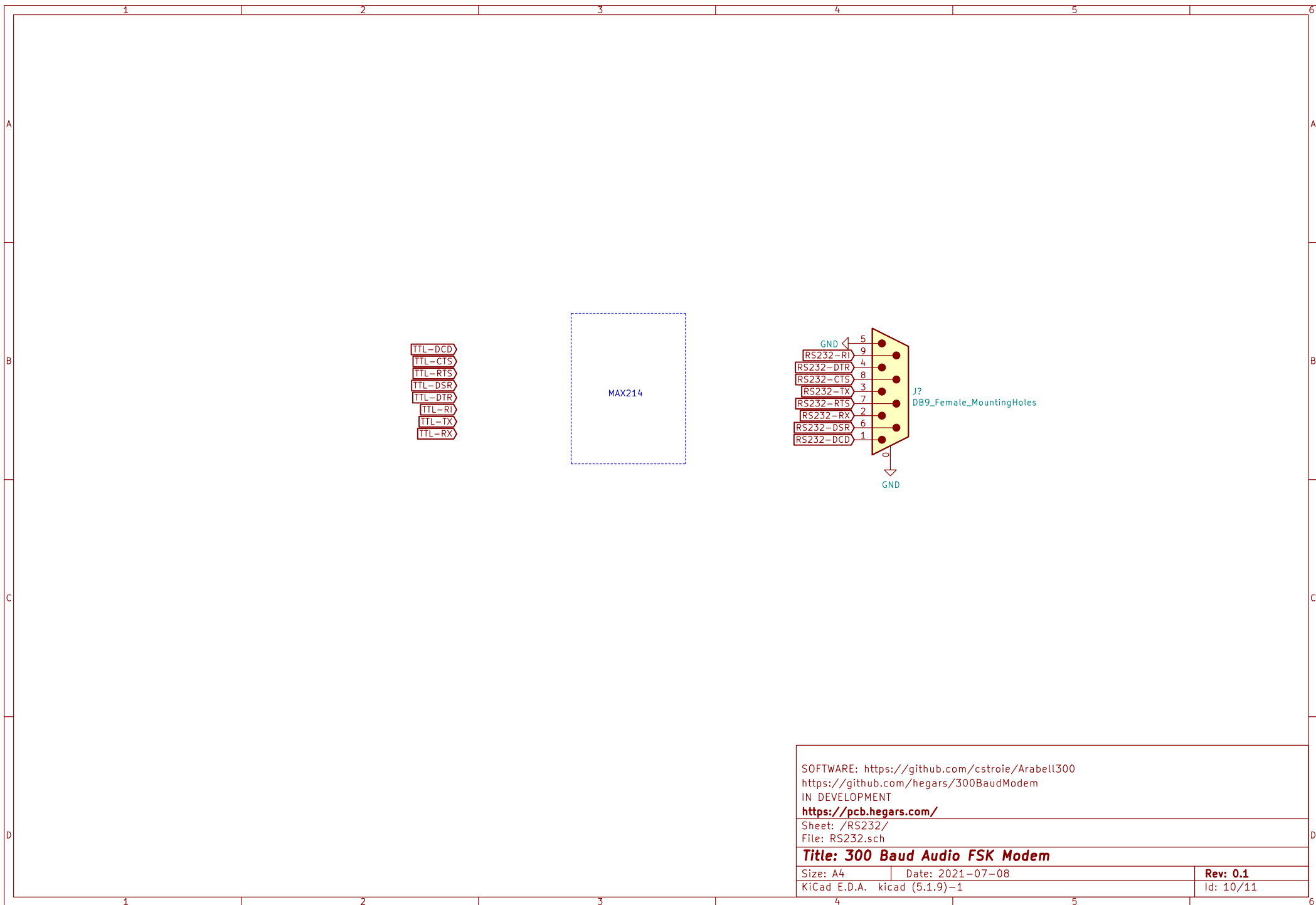


SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

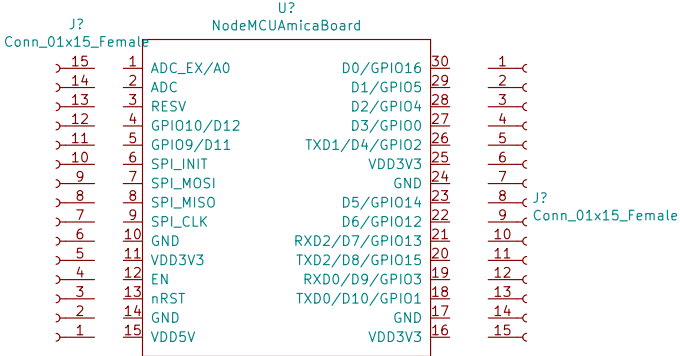
Sheet: /I2C/  
File: I2C.sch

**Title: 300 Baud Audio FSK Modem**

Size: A4	Date: 2021-07-08	Rev: 0.1
KiCad E.D.A. kicad (5.1.9)-1		Id: 9/11



SOFTWARE: <a href="https://github.com/cstroie/Arabell300">https://github.com/cstroie/Arabell300</a> <a href="https://github.com/hegars/300BaudModem">https://github.com/hegars/300BaudModem</a> IN DEVELOPMENT <a href="https://pcb.hegars.com/">https://pcb.hegars.com/</a>		
Sheet: /RS232/ File: RS232.sch		
Title: 300 Baud Audio FSK Modem		
Size: A4	Date: 2021-07-08	Rev: 0.1
KiCad E.D.A. kicad (5.1.9)-1		Id: 10/11



Sheet: /Serial2WiFi/  
File: Ser2WiFi.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad (5.1.9)-1

Date:

Rev:

Id: 11/11