

	1	2	3	4	5	6
A	<div>Sheet: Power</div> <div>File: Power.sch</div>				<div>Sheet: ECBus</div> <div>File: EDBus.sch</div>	
B	<div>Sheet: Indications</div> <div>File: Indications.sch</div>		<div>Sheet: uController</div> <div>File: uController.sch</div>		<div>Sheet: PCMBus</div> <div>File: PCMBus.sch</div>	
C	<div>Sheet: Baseband</div> <div>File: Baseband.sch</div>	<div>Sheet: IO</div> <div>File: IO.sch</div>			<div>Sheet: Serial2WiFi</div> <div>File: Ser2WiFi.sch</div>	
D	<div>Sheet: I2C</div> <div>File: I2C.sch</div>	<div>Sheet: RS232</div> <div>File: RS232.sch</div>	<div>Sheet: hybrid</div> <div>File: hybrid.sch</div>	<div>SOFTWARE: https://github.com/cstroie/Arabell300 https://github.com/hegars/300BaudModem IN DEVELOPMENT https://pcb.hegars.com/</div> <div>Sheet: / File: 300BaudModem.sch</div> <div>Title: 300 Baud Audio FSK Modem</div> <div><div>Size: A4</div><div>Date: 2021-07-08</div><div>Rev: 0.1</div></div> <div><div>KiCad E.D.A. kicad (5.1.10)-1</div><div>Id: 1/12</div></div>		
	1	2	3	4	5	6

RST_Request

Signaling-RINGDOWN

Signaling-OFFHOOK

I2C-SDL

I2C-SDA

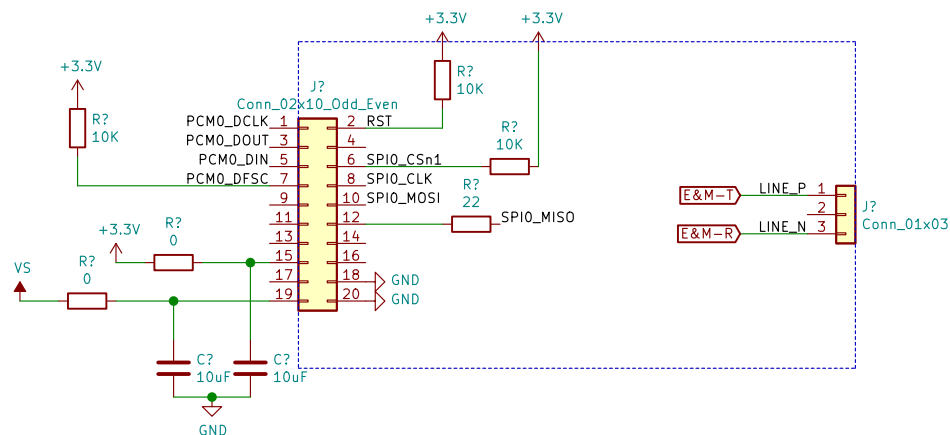
Baseband-OUT

Baseband-IN

Baseband to PCM transcoder

+5V
GND

PCM0_DFSC
PCM0_DIN
PCM0_DOUT
PCM0_DCLK



SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /PCMBus/
File: PCMBus.sch

Title: 300 Baud Audio FSK Modem

Size: A4 Date: 2021-07-08

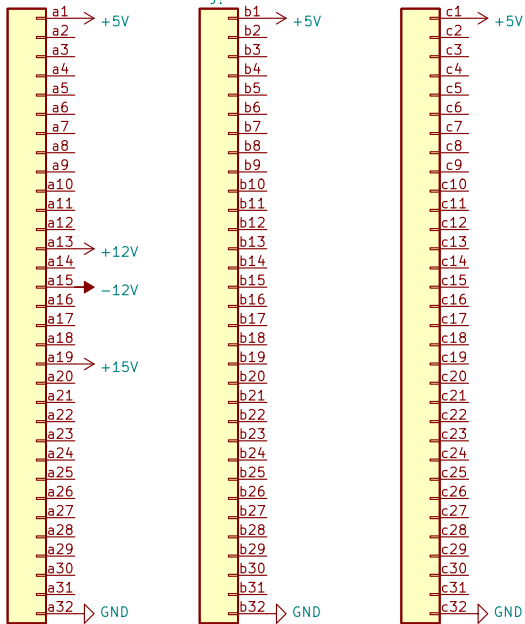
KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1

Id: 2/12

DIN41612_01x32_A

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SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /ECBus/
File: ECBus.sch

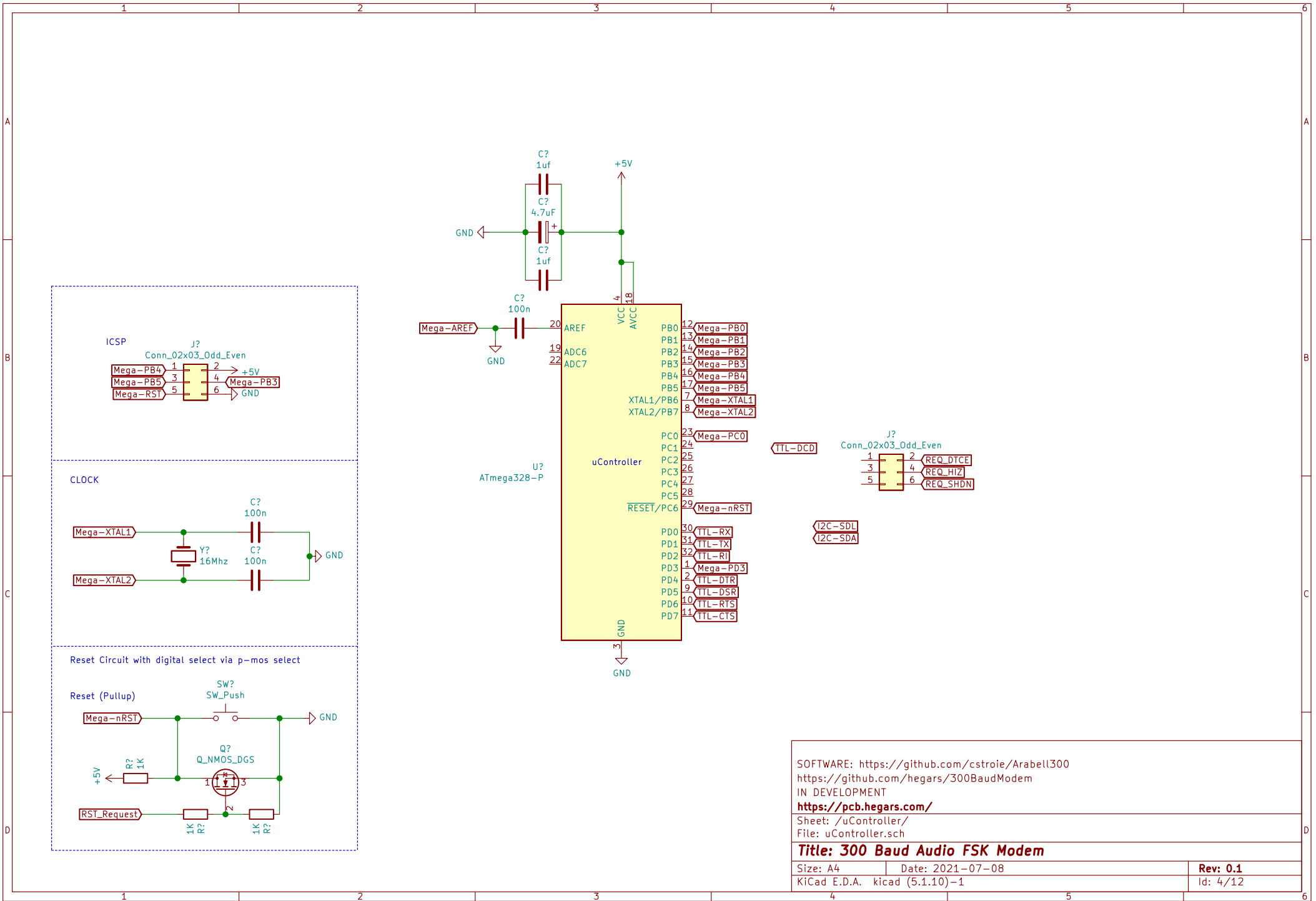
Title: 300 Baud Audio FSK Modem

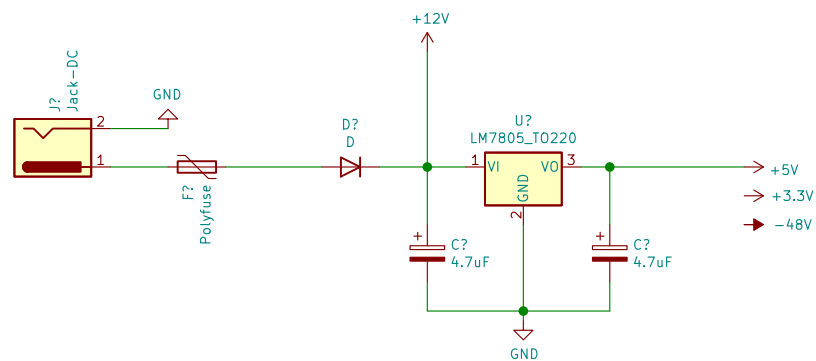
Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1

Id: 3/12





SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /Power/
File: Power.sch

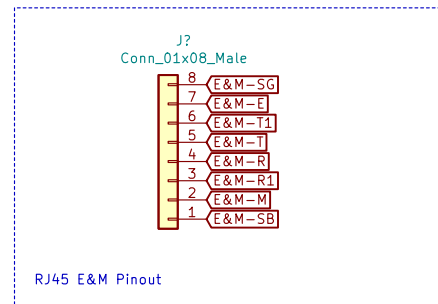
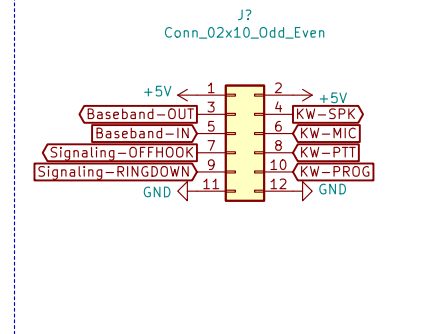
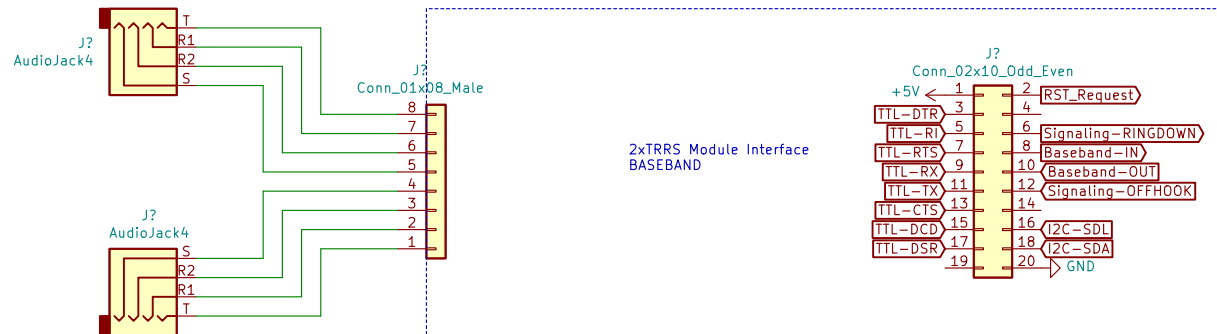
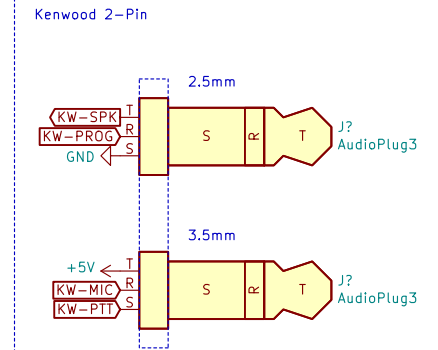
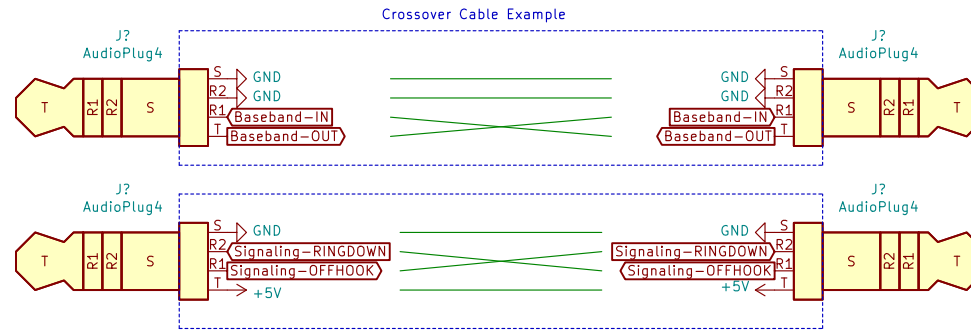
Title: 300 Baud Audio FSK Modem

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1

Id: 5/12



SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
 IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /IO/
 File: IO.sch

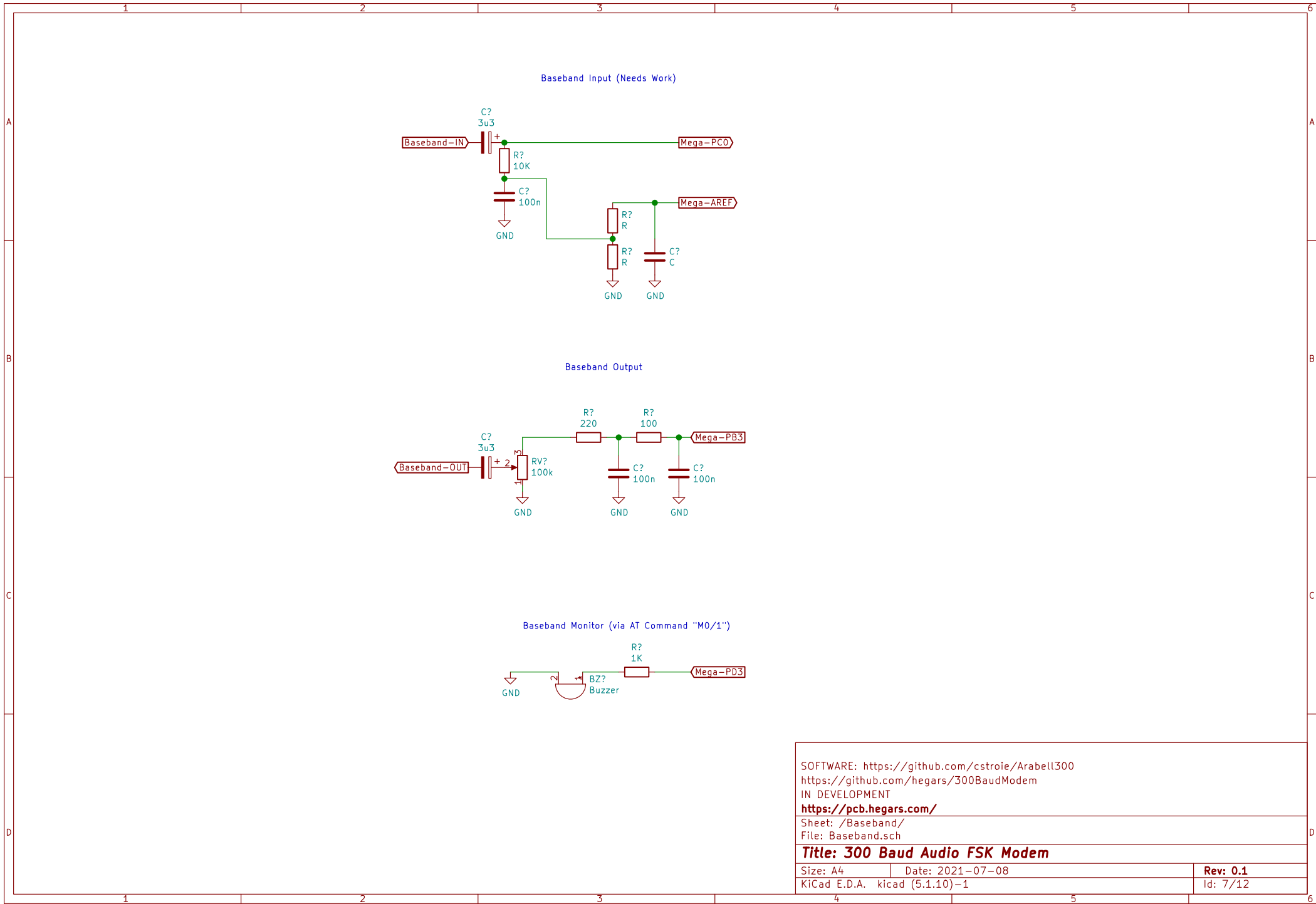
Title: 300 Baud Audio FSK Modem

Size: A4 Date: 2021-07-08

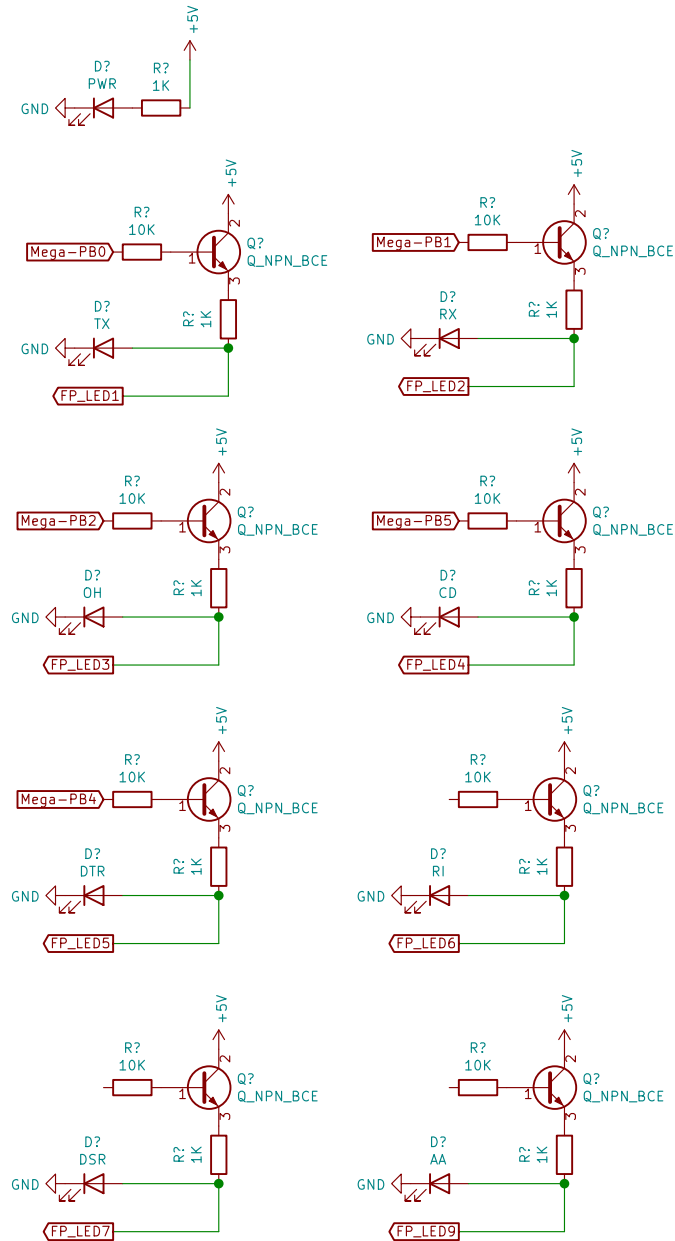
KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1

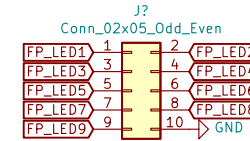
Id: 6/12



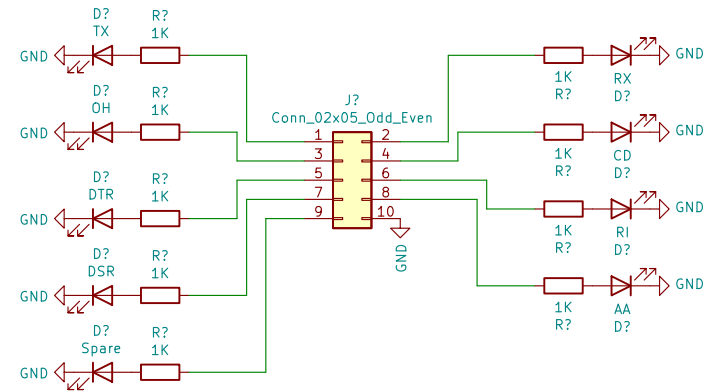
Inboard Indicators



To Outboard Connector



Outboard Indicators



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 IN DEVELOPMENT
<https://pcb.hegars.com/>

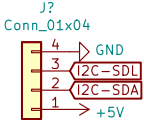
Sheet: /Indications/
 File: Indications.sch

Title: 300 Baud Audio FSK Modem

Size: A4
 Date: 2021-07-08
 KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1
 Id: 8/12

MAC Address Module

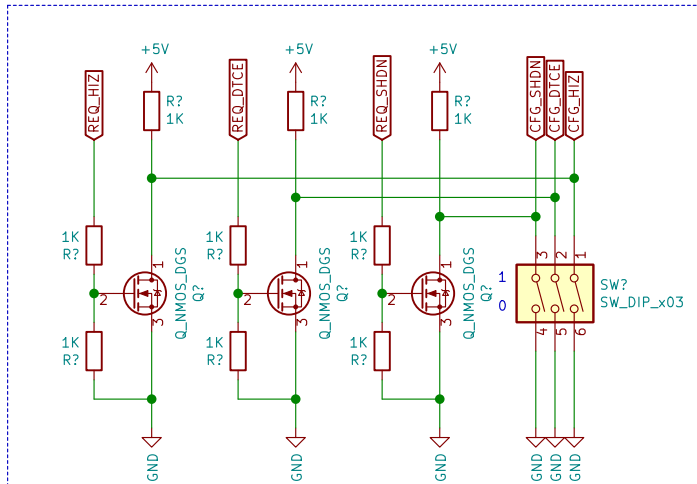


SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /I2C/
File: I2C.sch

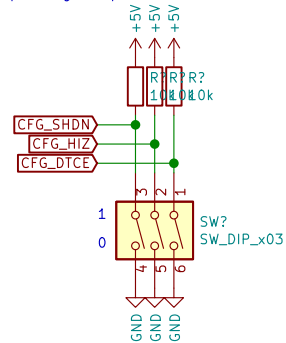
Title: 300 Baud Audio FSK Modem

Size: A4	Date: 2021-07-08	Rev: 0.1
KiCad E.D.A. kicad (5.1.10)-1		Id: 9/12

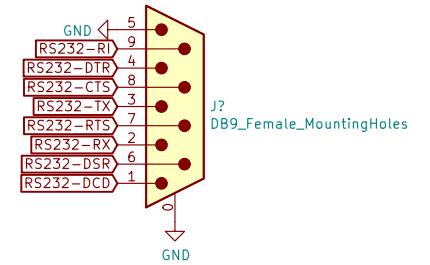
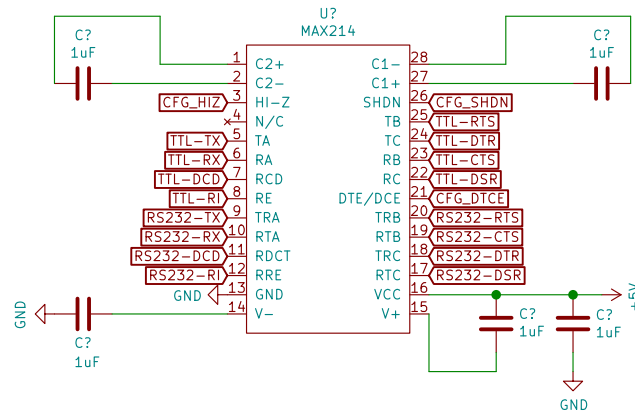


Pullup Config Setup Manual and Auto

Pullup Config Setup



CONTROL INPUTS			RS-232 PINS		
SHUTDOWN	HI-Z	DTE/DCE	TRA, TRB, TRC	RTA, RTB, RTC, RDTC	RRE
0	0	0	Transmit Mode	Receive Mode/5kΩ	Receive Mode/5kΩ
0	0	1	Receive Mode/5kΩ	Transmit Mode	Receive Mode/5kΩ
0	1	0	Transmit Mode	Receive Mode/HI-Z	Receive Mode/HI-Z
0	1	1	Receive Mode/HI-Z	Transmit Mode	Receive Mode/HI-Z
1	0	0	Disabled/HI-Z	Slow Receive/HI-Z	Slow Receive/HI-Z
1	0	1	Slow Receive/HI-Z	Disabled/HI-Z	Slow Receive/HI-Z
1	1	0	Disabled/HI-Z	Slow Receive/HI-Z	Slow Receive/HI-Z
1	1	1	Slow Receive/HI-Z	Disabled/HI-Z	Slow Receive/HI-Z



SOFTWARE: <https://github.com/cstroie/Arabell300>
<https://github.com/hegars/300BaudModem>
 IN DEVELOPMENT
<https://pcb.hegars.com/>

Sheet: /RS232/
 File: RS232.sch

Title: 300 Baud Audio FSK Modem

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.10)-1

Rev: 0.1

Id: 10/12

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Conn_01x15_Female

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U?
NodeMCUAmicaBoard

1	ADC_EX/A0	D0/GPIO16	30
2	ADC	D1/GPIO5	29
3	RESV	D2/GPIO4	28
4	GPIO10/D12	D3/GPIO0	27
5	GPIO9/D11	TXD1/D4/GPIO2	26
6	SPI_INIT	VDD3V3	25
7	SPI_MOSI	GND	24
8	SPI_MISO	D5/GPIO14	23
9	SPI_CLK	D6/GPIO12	22
10	GND	RXD2/D7/GPIO13	21
11	VDD3V3	TXD2/D8/GPIO15	20
12	EN	RXD0/D9/GPIO3	19
13	nRST	TXD0/D10/GPIO1	18
14	GND	GND	17
15	VDD5V	VDD3V3	16

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Conn_01x15_Female

