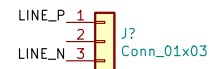
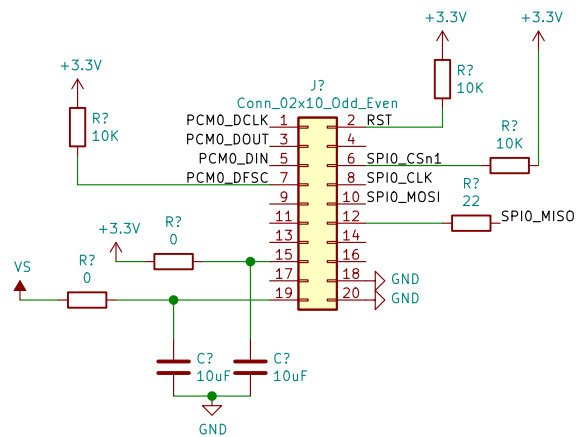


	1	2	3	4	5	6
A	<div>Sheet: Power File: Power.sch</div>	<div>Sheet: EDBus File: EDBus.sch</div>				
B	<div>Sheet: Indications File: Indications.sch</div>	<div>Sheet: PCMBus File: PCMBus.sch</div>	<div>Sheet: uController File: uController.sch</div>			
C	<div>Sheet: Baseband File: Baseband.sch</div>	<div>Sheet: IO File: IO.sch</div>				
D	<div>Sheet: I2C File: I2C.sch</div>	<div>Sheet: RS232 File: RS232.sch</div>		<div>SOFTWARE: <a href="https://github.com/cstroie/Arabell300">https://github.com/cstroie/Arabell300</a> <a href="https://github.com/hegars/300BaudModem">https://github.com/hegars/300BaudModem</a> IN DEVELOPMENT <a href="https://pcb.hegars.com/">https://pcb.hegars.com/</a></div> <div>Sheet: / File: 300BaudModem.sch</div> <div>Title: 300 Baud Audio FSK Modem</div> <div>Size: A4Date: 2021-07-08KiCad E.D.A. kicad (5.1.9)-1</div> <div>Rev: 0.1Id: 1/10</div>		
	1	2	3	4	5	6



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /PCMBus/  
 File: PCMBus.sch

**Title: 300 Baud Audio FSK Modem**

Size: A4 Date: 2021-07-08

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**Rev: 0.1**

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	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /ECBus/  
File: ECBus.sch

**Title: 300 Baud Audio FSK Modem**

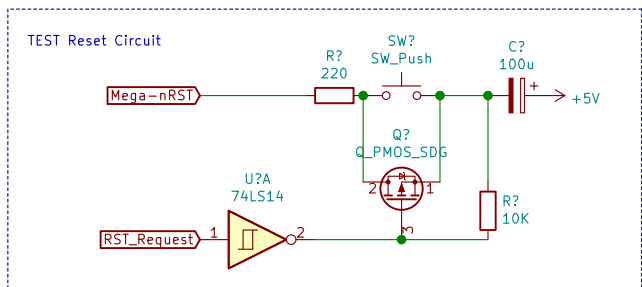
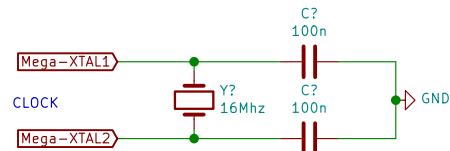
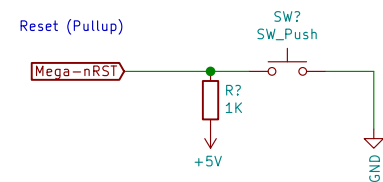
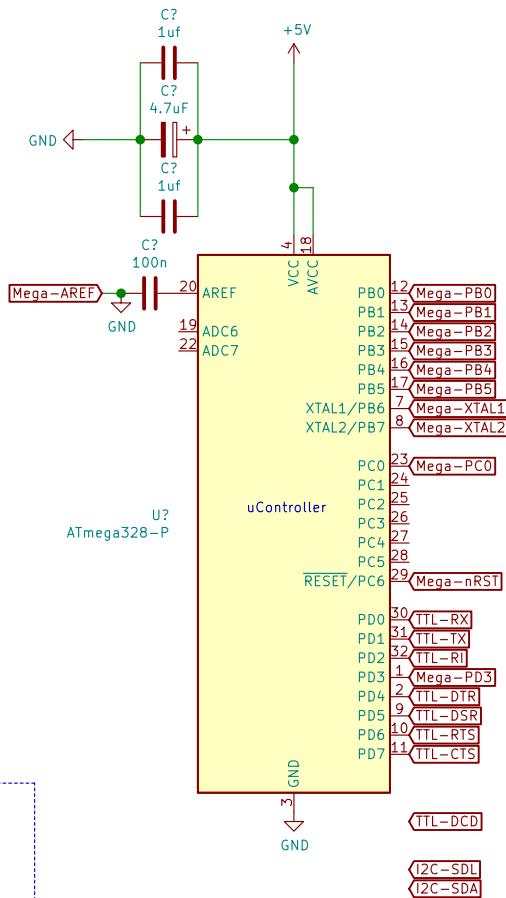
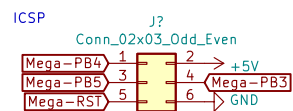
Size: A4

Date: 2021-07-08

Rev: 0.1

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Id: 3/10



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /uController/  
 File: uController.sch

**Title: 300 Baud Audio FSK Modem**

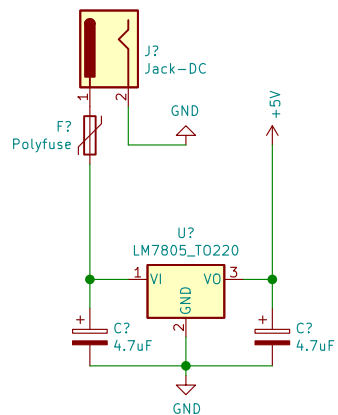
Size: A4  
 KiCad E.D.A. kicad (5.1.9)-1

Date: 2021-07-08

Rev: 0.1

Id: 4/10

# Power Regulator (CHECK)



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /Power/  
 File: Power.sch

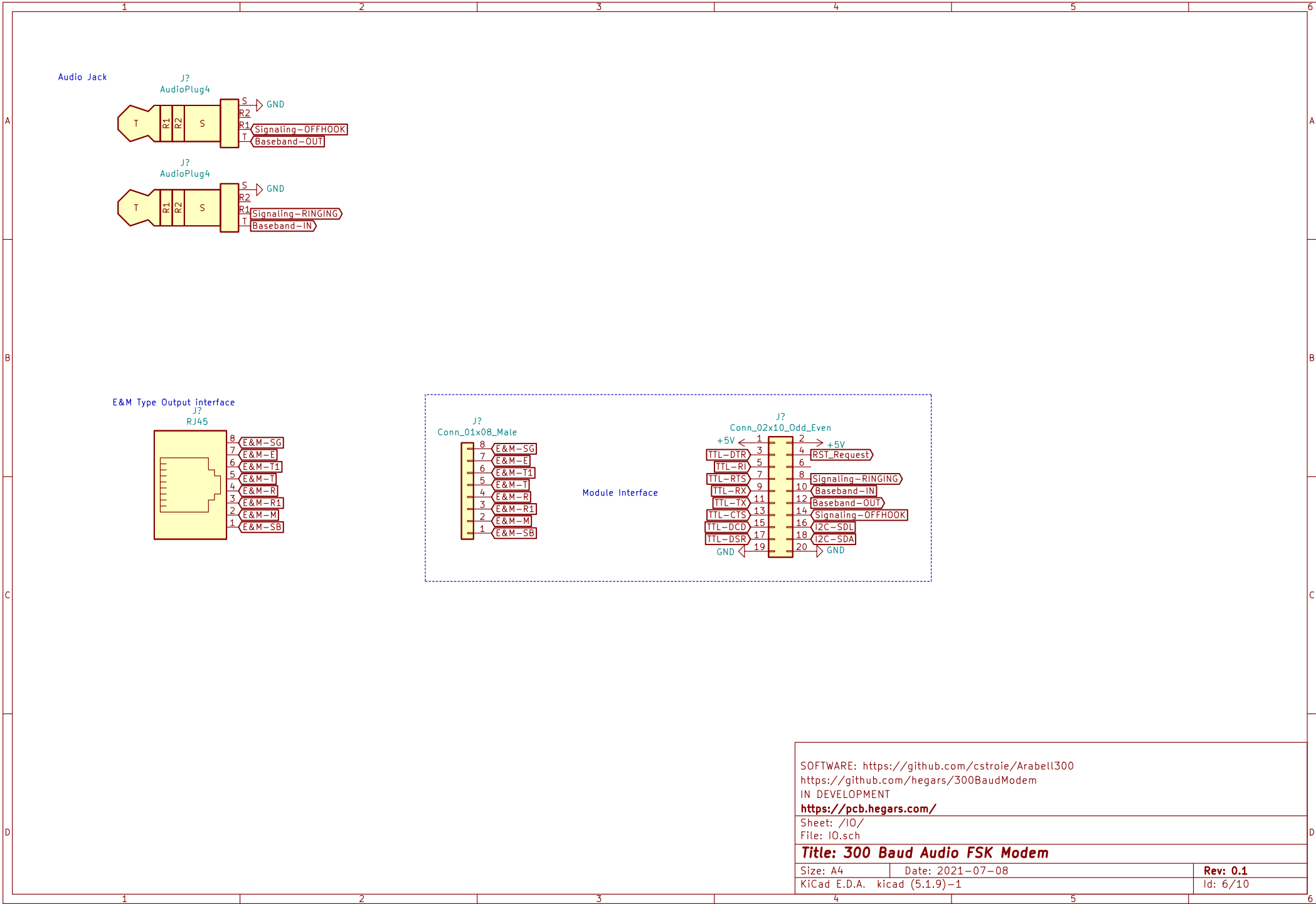
**Title: 300 Baud Audio FSK Modem**

Size: A4 Date: 2021-07-08

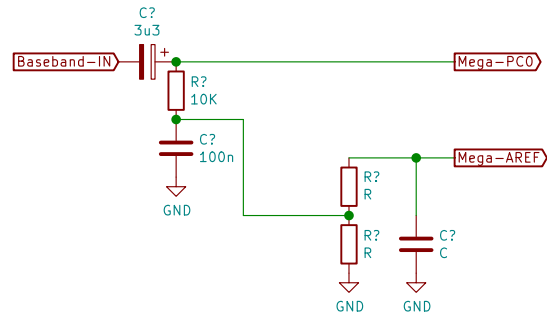
KiCad E.D.A. kicad (5.1.9)-1

**Rev: 0.1**

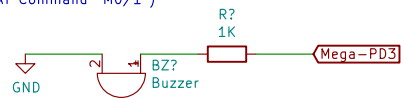
Id: 5/10



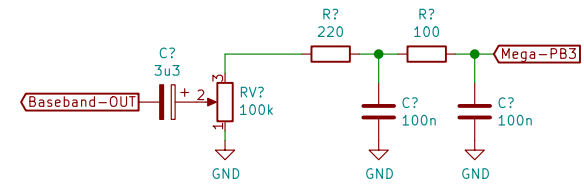
# Baseband Input (Needs Work)



# Baseband Monitor (via AT Command "M0/1")



# Baseband Output



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
 IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /Baseband/  
 File: Baseband.sch

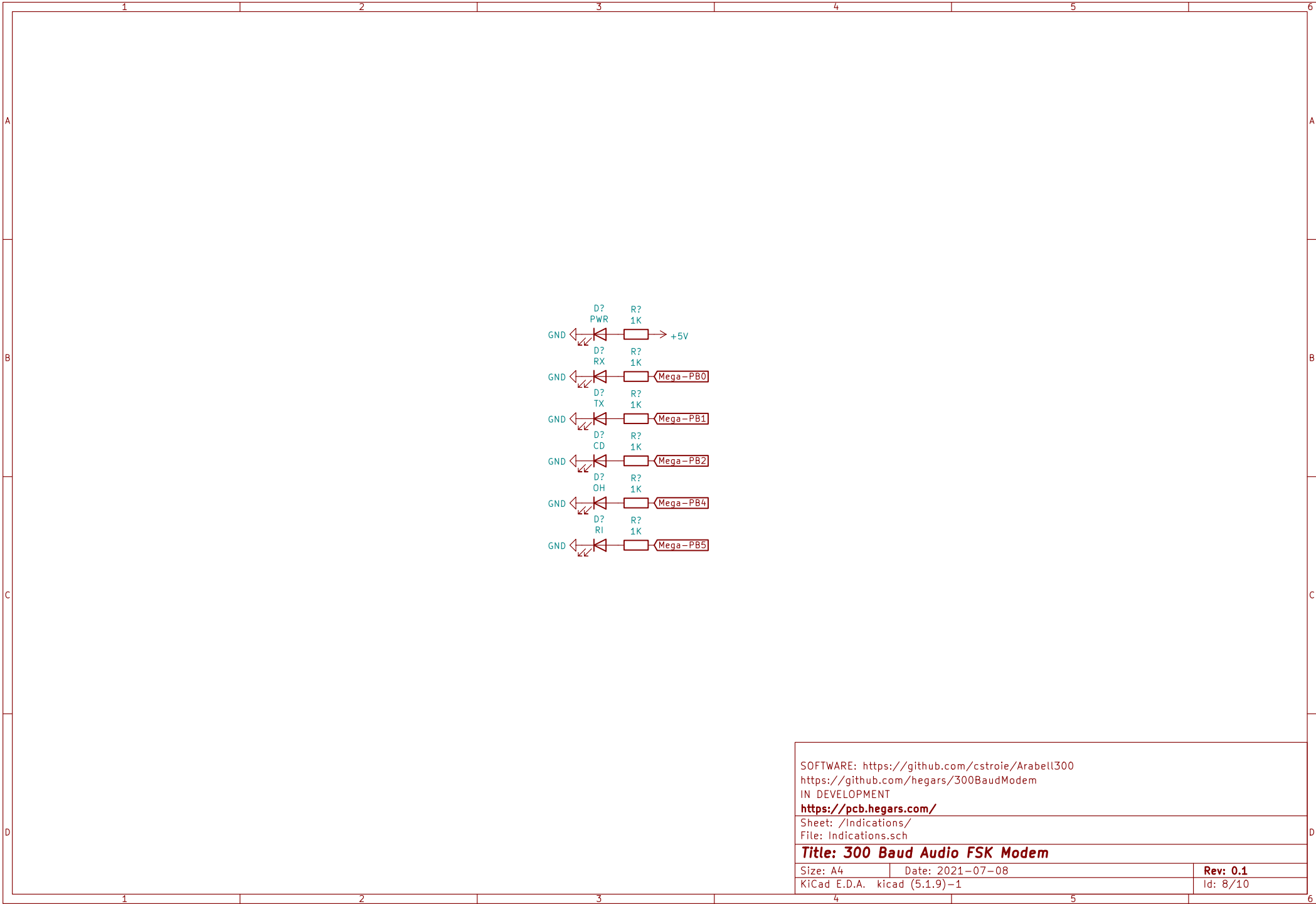
## Title: 300 Baud Audio FSK Modem

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.9)-1

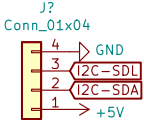
Rev: 0.1

Id: 7/10





MAC Address Module

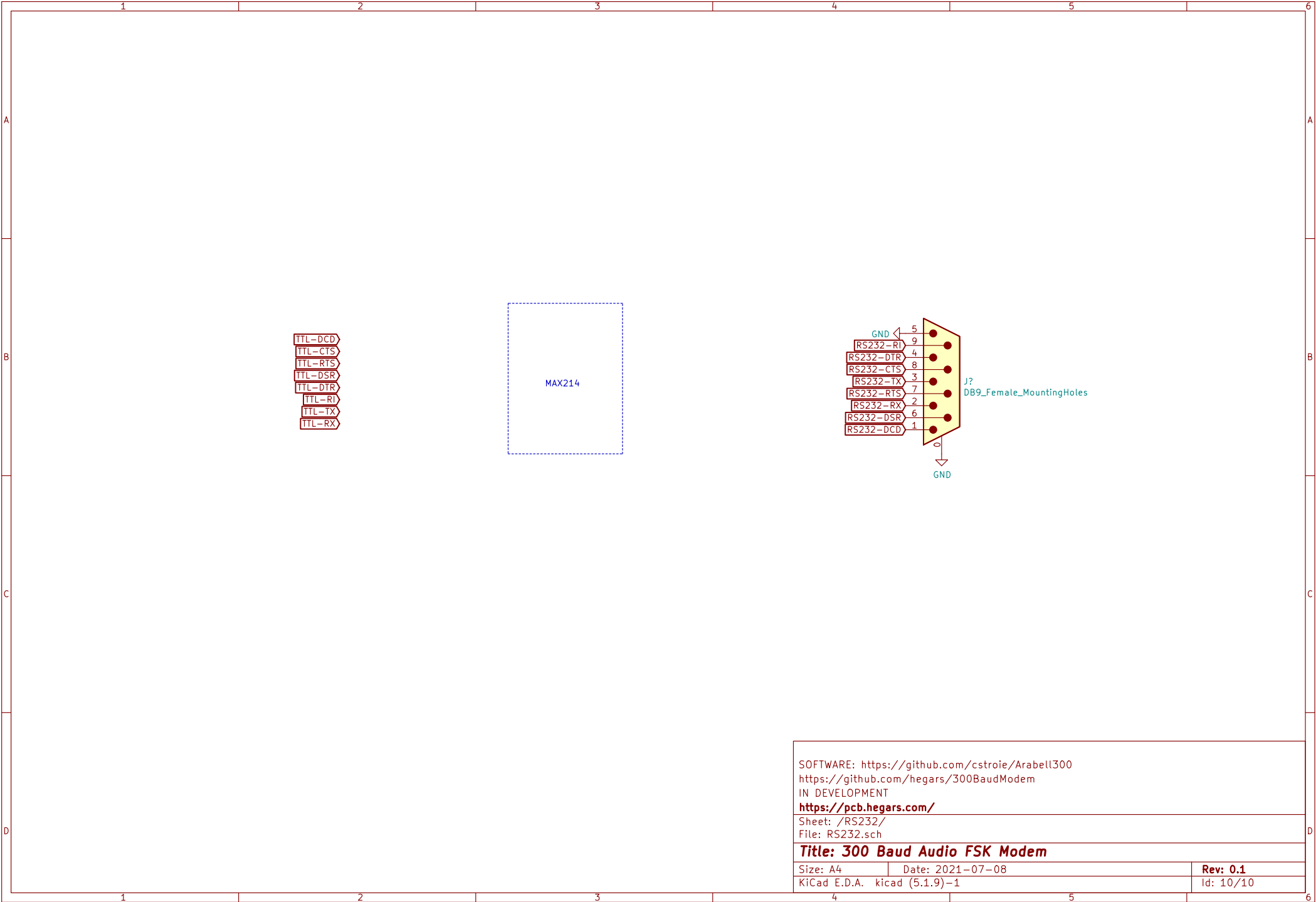


SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /I2C/  
File: I2C.sch

**Title: 300 Baud Audio FSK Modem**

Size: A4	Date: 2021-07-08	Rev: 0.1
KiCad E.D.A. kicad (5.1.9)-1		Id: 9/10



SOFTWARE: <https://github.com/cstroie/Arabell300>  
<https://github.com/hegars/300BaudModem>  
IN DEVELOPMENT  
<https://pcb.hegars.com/>

Sheet: /RS232/  
File: RS232.sch

**Title: 300 Baud Audio FSK Modem**

Size: A4 Date: 2021-07-08

KiCad E.D.A. kicad (5.1.9)-1

Rev: 0.1

Id: 10/10