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1. a. Bob Computer just bought a fancy new graphics display for his LC-3. In order to test out how fast it is, he rewrote the OUT trap handler so it would not check the DSR before outputting. Sadly he discovered that his display was not fast enough to keep up with the speed at which the LC-3 was writing to the DDR. How was he able to tell?

Because his input and display don't match.

- b. Bob also rewrote the handler for GETC, but when he typed ABCD into the keyboard, the following values were input:

AAAAAAAAAAAAAAAABBBBBBBBBBBBBBBBBBBBCCCCCCCCCCCCCCCCDDDDDDDDDDDDDDDDDDDDDD

What did Bob do wrong?

He did not set the KBSR[15] in time.

2. (Adapted from 8.1)

What are the defining characteristics of a stack? Give two implementations of a stack and describe their differences.

The characteristic of a stack is LIFO.

1. **Hardware Stack.** With POP and PUSH operations, the pointer does not move, other data items move
2. **Memory Stack.** With POP and PUSH operations, the pointer moves, other data items do not move

3. (Adapted from 8.9)

The input stream of a stack is a list of all the elements we pushed onto the stack, in the order that we pushed them. The input stream from Exercise 8.8 on page 304 of the book for example is ABCDEFGHIJKLM The output stream is a list of all the elements that are popped off the stack in the order that they are popped off.

- a. If the input stream is ZYXWVUTSR, create a sequence of pushes and pops such that the output stream is YXVUWZSRT.

*PUSH Z—PUSH Y—POP Y—PUSH X—POP X—PUSH W—PUSH V—POP V—PUSH U—
POP U—POP W—POP Z—PUSH T—PUSH S—POP S—PUSH R—POP R—POP T*

- b. If the input stream is ZYXW, how many different output streams can be created? Note: only consider output streams that are 4 characters long.

$$\frac{\binom{2 \times 4}{4}}{4+1} = 14$$

4. (Adapted from 8.6) Rewrite the PUSH and POP routines such that the stack on which they operate holds elements that take up two memory locations each. Assume we are writing a program to simulate a stack machine that manipulates 32-bit integers with the LC-3. We would need PUSH and POP routines that operate with a stack that holds elements which take up two memory locations each. Rewrite the PUSH and POP routines for this to be possible.

```
1  PUSH    LD    R2,MAX
2         ADD   R3,R6,R2
```

3		BRz OVERFLOW
4		ADD R6, R6, #-2
5		STR R0, R6, #0
6		STR R1, R6, #1
7		RET
8	POP	LD R2, EMPTY
9		ADD R3, R6, R2
10		BRz FAILURE
11		LDR R0, R6, #0
12		LDR R1, R6, #1
13		ADD R6, R6, #2
14		RET

5. A zero-address machine is a stack-based machine where all operations are done by using values stored on the operand stack. For this problem, you may assume that the ISA allows the following operations:

PUSH M - pushes the value stored at memory location M onto the operand stack.

POP M - pops the operand stack and stores the value into memory location M.

OP - Pops two values off the operand stack and performs the binary operation OP on the two values. The result is pushed back onto the operand stack.

Note 1: OP can be *ADD*, *SUB*, *MUL*, or *DIV* for parts a and b of this problem.

Note 2: To perform *DIV* and *SUB* operations, the top element of the stack is considered as the second operand. i.e. If we first push "A" and then push "B" followed by a "*SUB*" operation, "A" and "B" will be popped from stack and "A-B" will be pushed into stack.

a) Draw a picture of the stack after each of the instructions below are executed. What is the minimum number of memory locations that have to be used on the stack for the purposes of this program? Also write an arithmetic equation expressing u in terms of v, w, x, y, and z. The values u, v, w, x, y, and z are stored in memory locations U, V, W, X, Y, and Z.

PUSH V

PUSH W

PUSH X

PUSH Y

MUL

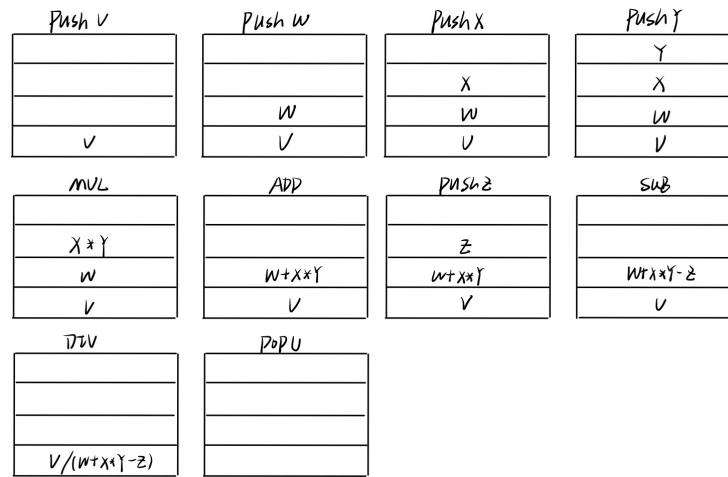
ADD

PUSH Z

SUB

DIV

POP U



$U = \frac{V}{W+X+Y-Z}$, We need 4 memory locations.

b) Write the assembly language code for a zero-address machine (using the same type of instructions from part a) for calculating the expression below. The values a, b, c, d, and e are stored in memory locations A, B, C, D, and E. $e = ((a * ((b - c) + d)) / (a + c))$

PUSH a PUSH b PUSH c SUB PUSH d ADD MUL PUSH a PUSH c ADD DIV POP e

6. Assume that you have the following table in your program:

```

MASKS .FILL x0001
      .FILL x0002
      .FILL x0004
      .FILL x0008
      .FILL x0010
      .FILL x0020
      .FILL x0040
      .FILL x0080
      .FILL x0100
      .FILL x0200
      .FILL x0400
      .FILL x0800
      .FILL x1000
      .FILL x2000
      .FILL x4000
      .FILL x8000

```

a) Write a subroutine CLEAR in LC-3 assembly language that clears a bit in R0 using the table above. The index of the bit to clear is specified in R1, R0 and R1 are inputs to the subroutine.

```

1  CLEAR    ST    R2,SAVE_1
2          LEA   R2,MASKS
3          ADD   R2,R2,R1
4          LDR   R2,R2,#0
5          NOT   R2,R2
6          AND   R0,R0,R2
7          LD    R2,SAVE_1
8          RET
9  SAVE_1   .FILL x0000

```

b) Write a similar subroutine SET that sets the specified bit instead of clearing it.

Hint: You should remember to save and restore any registers your subroutine uses (the "callee save" convention). Use the RET instruction as the last instruction in your subroutine (R7 contains the address of where in the caller to return to.)

```

1  SET     ST   R2,SAVE_1
2          LEA  R2,MASKS
3          ADD  R2,R1,R2
4          LDR  R2,R2,#0
5          NOT  R2,R2
6          NOT  R0,R0
7          AND  R0,R0,R2
8          NOT  R0,R0
9          LD   R2,SAVE_1
10         RET
11 SAVE_1  .FILL x0000

```

7. Suppose we are writing an algorithm to multiply the elements of an array (unpacked, 16-bit 2's complement numbers), and we are told that a subroutine "mult_all" exists which multiplies four values, and returns the product. The mult_all subroutine assumes the source operands are in R1, R2, R3, R4, and returns the product in R0. For purposes of this assignment, let us assume that the individual values are small enough that the result will always fit in a 16-bit 2's complement register.

Your job: Using this subroutine, write a program to multiply the set of values contained in consecutive locations starting at location x6001. The number of such values is contained in x6000. Store your result at location x7000. Assume there is at least one value in the array(i.e., M[x6000] is greater than 0).

Hint: Feel free to include in your program

PTR .FILL x6001

CNT .FILL x6000

```

1      .ORIG x3000
2      LD   R5, PTR
3      LDI  R6, CNT
4      BRz  END_1      ;没有数需要相乘
5  LOOP  LDR  R1,R5,#0
6          ADD  R5,R5,#1
7          ADD  R6,R6,#-1
8      BRz  END_2      ;只有一个数
9          LDR  R2,R5,#0
10         ADD  R5,R5,#1
11         ADD  R6,R6,#-1
12     BRz  END_3      ;只有两个数
13         LDR  R3,R5,#0
14         ADD  R5,R5,#1
15         ADD  R6,R6,#-1
16     BRz  END_4      ;只有三个数
17         LDR  R4,R5,#0
18         ADD  R5,R5,#1
19         ADD  R6,R6,#-1
20     BRnzp MULT
21  END_1  AND  R0,R0,#0
22         ADD  R0,R0,#1
23     BRnzp END
24  END_2  AND  R2,R2,#0
25         ADD  R2,R2,#1      ;R2 = 1
26         ADD  R3,R2,#0      ;R3 = 1
27         ADD  R4,R2,#0      ;R4 = 1
28     BRnzp MULT
29  END_3  AND  R3,R3,#0
30         ADD  R3,R3,#1      ;R3 = 1
31         ADD  R4,R4,#0      ;R4 = 1

```

```

32      BRnzp MULT
33  END_4  AND R4,R4,#0
34      ADD R4,R4,#1
35  MULT   JSR mult_all
36      ADD R6,R6,#0
37      BRz  END           ;如果没有数需要相乘了
38      ADD R5,R5,#-1      ;把mult_all返回在R0的结果存入内存
39      STR R0,R5,#0
40      ADD R6,R6,#1
41      BRnzp LOOP
42  END    ST R0,RESULT
43      HALT
44  RESULT .FILL x7000
45  mult_all
46      ...
47      RET
48  PTR   .FILL x6001
49  CNT   .FILL x6000
50      .END

```

8. (9.26) The following program is supposed to print the number 5 on the screen. It does not work. Why? Answer in no more than ten words, please.

```

                .ORIG x3000
                JSR  A
                OUT                ;TRAP x21
                BRnzp DONE
A               AND  R0,R0,#0
                ADD  R0,R0,#5
                JSR  B
                RET
DONE           HALT
ASCII         .FILL x0030
B             LD    R1,ASCII
                ADD  R0,R0,R1
                RET
                .END

```

JSR in subroutine A overrides R7 ,PC can't return correctly

9. (9.19) The following LC-3 program is assembled and then executed. There are no assemble time or run-time errors. What is the output of this program? Assume all registers are initialized to 0 before the program executes.

```

                .ORIG x3000
                ST R0, #6 ; x3007

                LEA R0, LABEL
                TRAP x22
                TRAP x25
LABEL          .STRINGZ "FUNKY"
LABEL2         .STRINGZ "HELLO WORLD"
                .END

```

FUN

10. The memory locations given below store students' exam scores in form of a linked list. Each node of the linked list uses three memory locations to store

1. Address of the next node
2. Starting address of the memory locations where name of the student is stored
3. Starting address of the memory locations where the his/her exam score is stored in the given order. The first node is stored in locations x4000 ~ x4002. The ASCII code x0000 is used as a sentinel to indicate the end of the string. Both the name and exam score are stored as strings.

Write down the students' names and scores in the order that they appear in the list.

Address	Contents
x4000	x4016
x4001	x4003 x4012 x0000
x4002	x4008 x4013 x0031
x4003	x004D x4014 x0038
x4004	x0061 x4015 x0000
x4005	x0072 x4016 x400B
x4006	x0063 x4017 x400E
x4007	x0000 X4018 x4013
x4008	x0039 x4019 x004D
x4009	x0030 x401A x0069
x400A	x0000 x401B x006B
x400B	x0000 x401C x0065
x400C	x4019 x401D x0000
x400D	x401E x401E x0037
x400E	x004A x401F x0036
x400F	x0061 x4020 x0000
x4010	x0063
x4011	x006B

Marc 90

Jack 18

Mike 76

11. The main program below calls a subroutine, F. The F subroutine uses R3 and R4 as input, and produces an output which is placed in R0. The subroutine modifies registers R0, R3, R4, R5, and R6 in order to complete its task. F calls two other subroutines, SaveRegisters and RestoreRegisters, that are intended handle the saving and restoring of the modified registers (although we will see in part b that this may not be the best idea!).

```

; Main Program
.ORIG x3000
.....
.....
JSR F
.....
.....
HALT

; R3 and R4 are input.
; Modifies R0, R3, R4, R5, and R6
; R0 is the output

F      JSR SaveRegisters
.....
.....
.....
      JSR RestoreRegisters
      RET

.END

```

Part a) Write the two subroutines SaveRegisters and RestoreRegisters.

```

2          ST R3,SAVE_1
3          ST R4,SAVE_2
4          ST R5,SAVE_3
5          ST R6,SAVE_4
6          RET
7 RestoreRegisters LD R0,SAVE_0
8          LD R3,SAVE_1
9          LD R4,SAVE_2
10         LD R5,SAVE_3
11         LD R6,SAVE_4
12         RET
13 SAVE_0      .FILL x0000
14 SAVE_1      .FILL x0000
15 SAVE_2      .FILL x0000
16 SAVE_3      .FILL x0000
17 SAVE_4      .FILL x0000

```

Part b) When we run the code we notice there is an infinite loop. Why? What small change can we make to our program to correct this error. Please specify both the correction and the subroutine that is being corrected.

JSR RestoreRegisters in subroutine F overrides R7.

```

1 F          JSR SaveRegisters
2          ....
3          ST R7,SAVE_5
4          JSR RestoreRegisters
5          LD R7,SAVE_5
6          RET
7 SAVE_5     .FILL x0000

```

12. Suppose we want to make a 10 item queue starting from location x4000. In class, we discussed using a HEAD and a TAIL pointer to keep track of the beginning and end of the queue. In fact, we suggested that the HEAD pointer could point to the first element that we would remove from the queue and the TAIL pointer could point the last element that we have added the queue. It turns out that our suggestion does not work.

a) What is wrong with our suggestion? (Hint: how do we check if the queue is full? How do we check if it is empty?)

We can't distinguish the full queue and the empty queue.

b) What simple change could be made to our queue to resolve this problem?

We only allow n-1 items to be place in a queue with n memory spaces.

c) Using your correction, write a few instructions that check if the queue is full. Use R3 for the HEAD pointer and R4 for the TAIL pointer.

```

1 ISFULL     NOT R5,R4
2          ADD R5,R5,R3
3          BRz FULL
4          LD R5,NEGSTART
5          ADD R5,R5,R3
6          BRnp NOTFULL
7          LD R5,NEGEND
8          ADD R5,R5,R4
9          BRz FULL
10 NOTFULL   LABEL_NOTFULL
11          ...
12 FULL      LABEL_FULL
13          ...
14          RET

```

d) Using your correction, write a few instructions that check if the queue is empty. Again, using R3 for the HEAD pointer and R4 for the TAIL pointer.

```
1 IEMPTY NOT R5,R4
2      ADD R5,R5,#1
3      ADD R5,R5,R3
4      RET
```

13. The following nonsense program is assembled and executed.

```
                .ORIG x4000
                LD  R2,BOBO
                LD  R3,SAM
AGAIN           ADD  R3,R3,R2
                ADD  R2,R2,#-1
                BRnzp SAM
BOBO            .STRINGZ "Why are you asking me this?"
SAM            BRnp AGAIN
                TRAP x25
                .BLKW 5
JOE            .FILL x7777
                .END
```

How many times is the loop executed? When the program halts, what is the value in R3? (If you do not want to the arithmetic, it is okay to answer this with a mathematical expression.)

87 times, R3 is x1AD4(6868).
