



# Conception numérique (DiD)

## Introduction

## IND

Filière Systèmes industriels

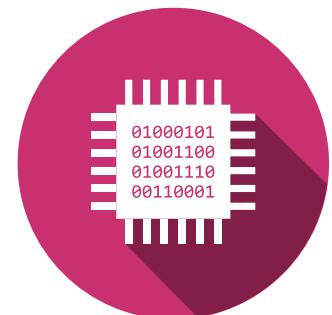
Filière Energie et techniques environnementales

Filière Informatique et systèmes de communications

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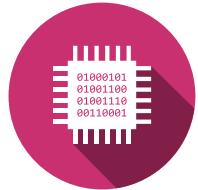
Christophe Bianchi [christophe.bianchi@hevs.ch](mailto:christophe.bianchi@hevs.ch)

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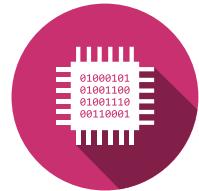
# ISC Modules 1<sup>ère</sup> année

<https://www.hevs.ch/media/document/20/plan-etude-isc-v1-02.pdf>



Cours	Heures	Crédits
<b>Semestre 1</b>	<b>32</b>	<b>28</b>
Algèbre linéaire 1	4	4
Analyse 1	6	5
Communication 1	2	2
Ethique et aspects juridiques	2	2
Langues 1	4	3
Programmation impérative	8	7
<i>Systèmes numériques</i>	6	5
<b>Semestre 2</b>	<b>36</b>	<b>32</b>
Algèbre linéaire 2	4	4
Analyse 2	6	5
<i>Architecture des ordinateurs</i>	4	3
Communication 2	2	2
Intro. à la sécurité	4	3
Langues 2	4	3
Programmation objet	8	6
Réseaux	4	6
Projet 1	Summer school	3

# But du cours



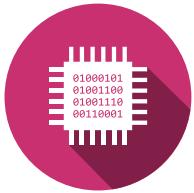
Savoir interpréter le cahier des charges fonctionnel d'un système simple (C) et le représenter sous forme numérique (A)

Appliquer les principes de base de la conception numérique selon les méthodologies proposées (A)

Réaliser la fonction logique qui en découle (A)

Valider la conception numérique réalisée selon les méthodologiques de simulation et de test (J)

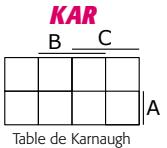
# Contenu du cours



Cahier des charges

**NUM**  
11110101  
Binary digit

a	b	y
0	0	0
0	1	1
1	0	1
1	1	0

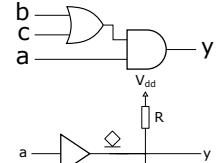


a	b	y
0	0	0
0	1	1
1	0	1
1	1	0

**COM**  
**MUX**

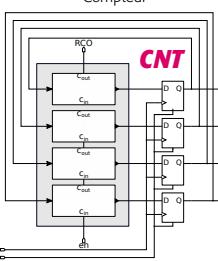
Eléments de logique combinatoire

Circuit numérique combinatoire

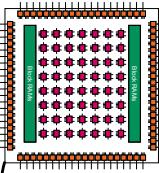


**LST**

Compteur



**FPGA**



Equation polynomiale

$$Q_0^+ = \overline{Q_0}$$

$$Q_1^+ = Q_0 \oplus Q_1$$

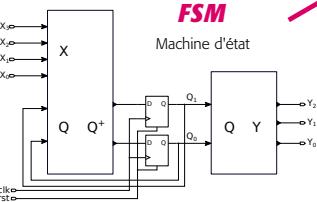
**LAT**

Eléments de logique séquentielles

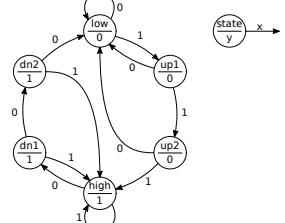
DiD IND

**FSM**

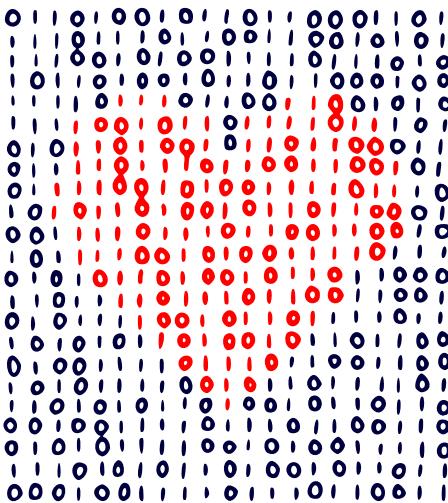
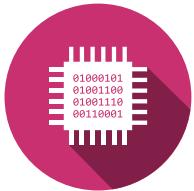
Machine d'état



**MET**



# Blague



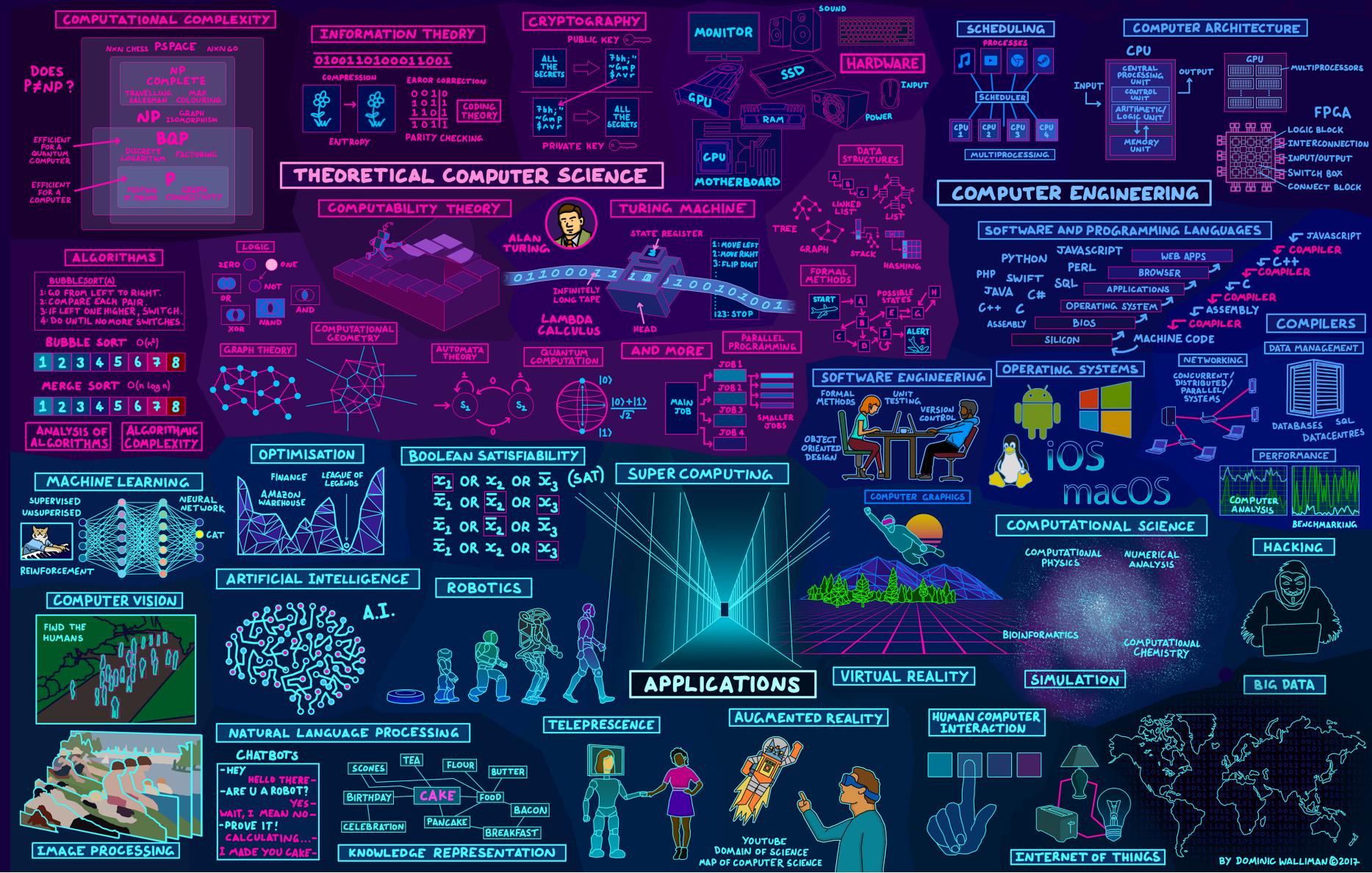
There are 10 types of people in this world. Those who understand binary and those who don't.

Il y a 10 types de personnes dans ce monde. Ceux qui comprennent le binaire et ceux qui ne le comprennent pas.

# Map of Computer Science

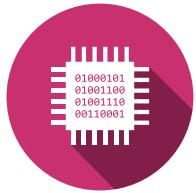


# Computer Science



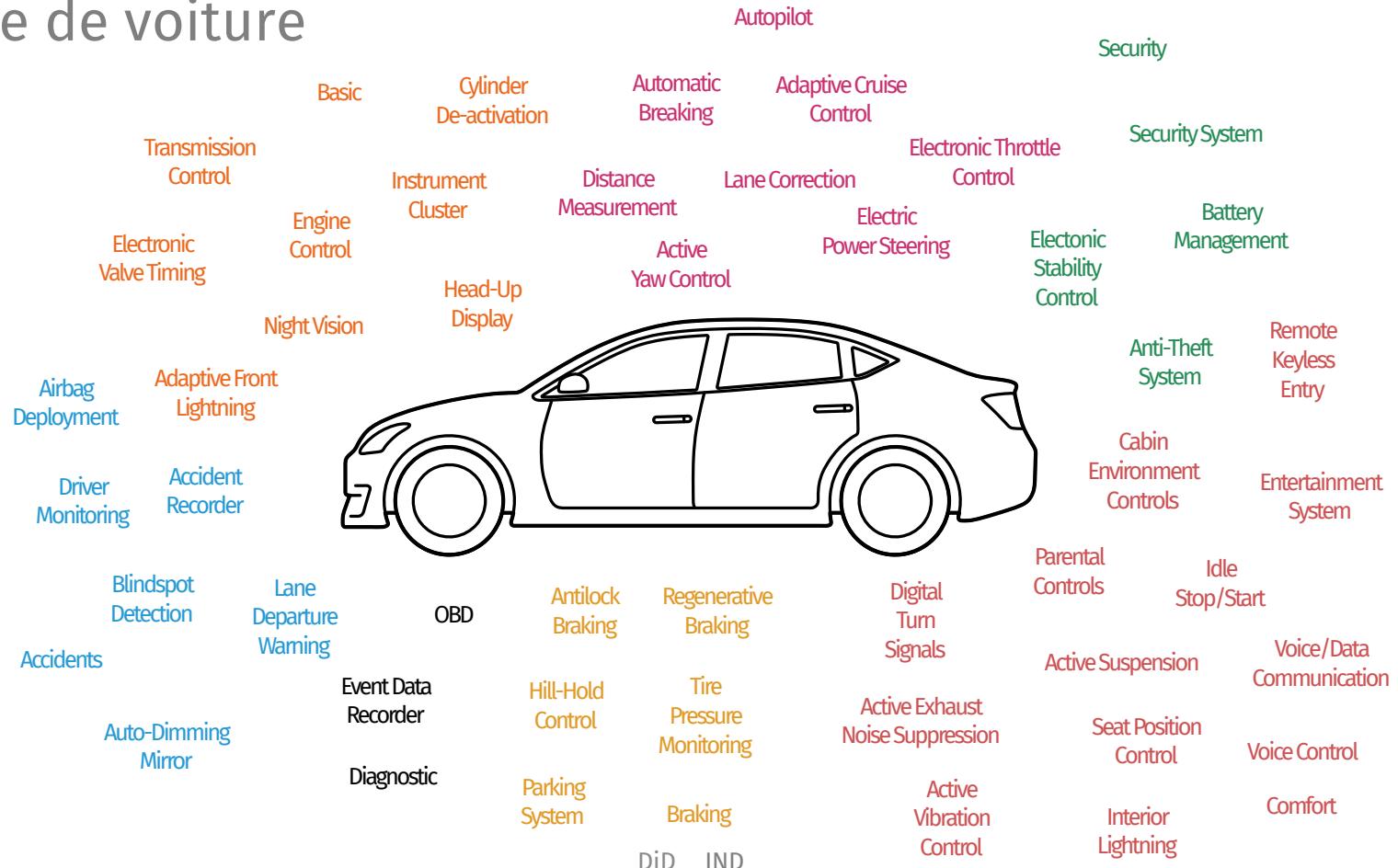
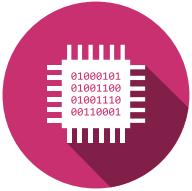
# Electronique numérique

## Domaines d'application



# Electronique numérique

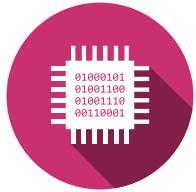
## Exemple de voiture



# Electronique numérique

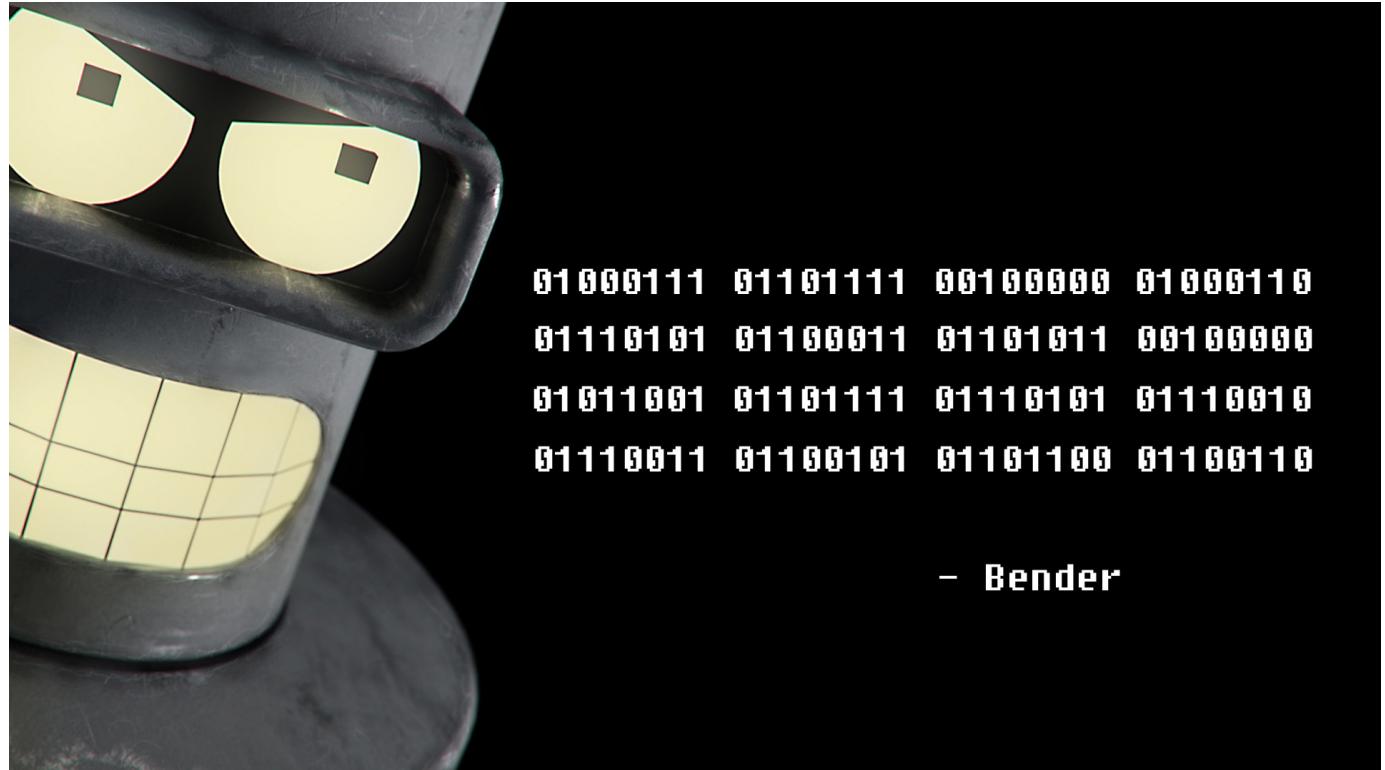
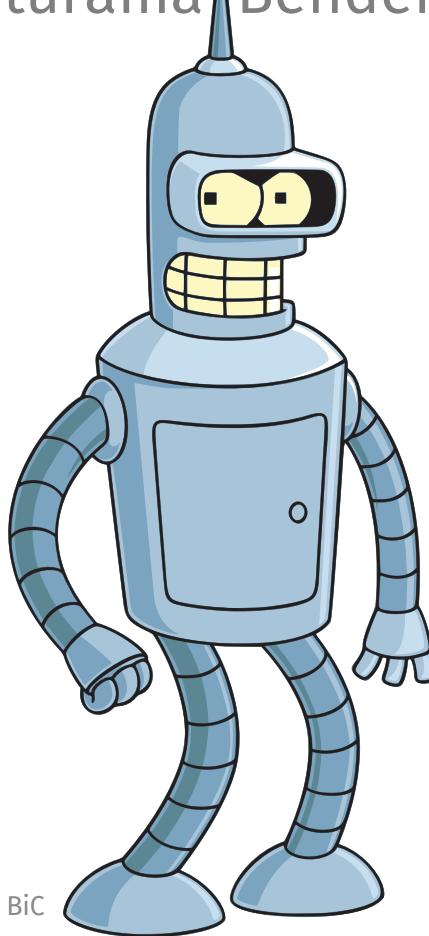
## Ariane 5

[https://youtu.be/PK\\_yguLapgA](https://youtu.be/PK_yguLapgA)

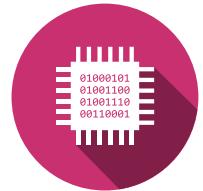


# Electronique numérique

## Futurama Bender

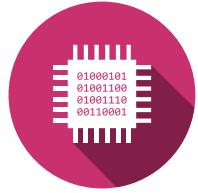


[https://youtu.be/\\_4TPlwwHM8Q](https://youtu.be/_4TPlwwHM8Q)

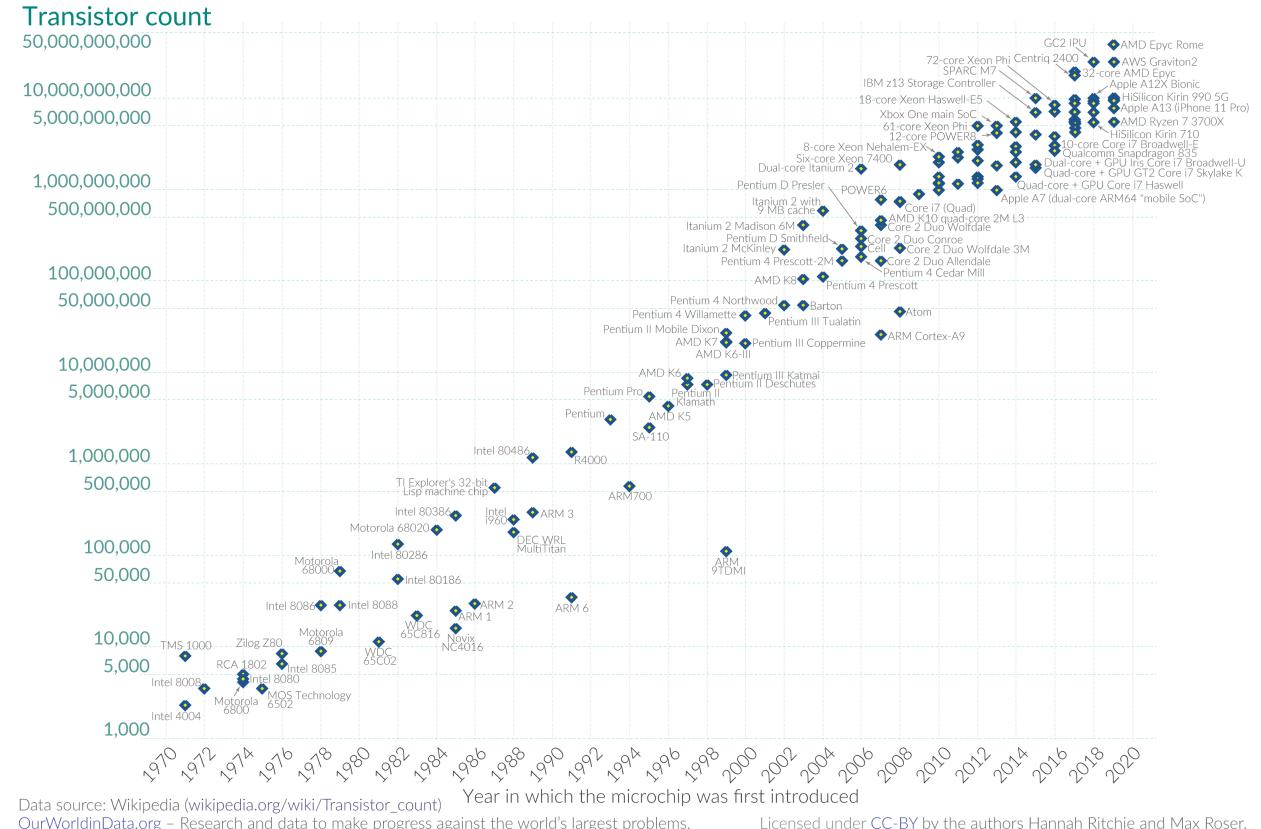


# Electronique numérique

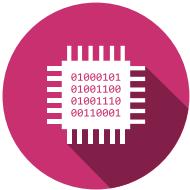
## Moore's Law



*Number of transistors double  
every two years*  
- Gordon Moore -



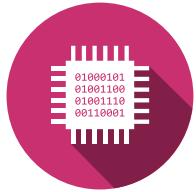
# ISC Organisation



- Cours ( $2^{Heures/Semaine}$ )
- Labore ( $4^{Heures/Semaine}$ )
- Projet (Display  $\approx 3 - 5$  Semaine)
- Examen
  - 22.11.2024 10h10 – 11h45

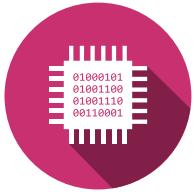
	Fallsemester (DiD)			Springsemester (CAr)		
Evaluation	Exa 1	Project	Exa Sem	Labo	Project	Exa Sem
Coefficient	0.5	0.5	1	0.5	0.5	1
Semestergrade	1		1	1		1
Modulegrade	5/9			4/9		

# Coopération Générale



- Présence au cours obligatoire
  - Seuls les professeurs peuvent donner une dispense
    - Si absentéisme grandissant ⇒ information au RF
  - Prise de notes indispensable, notamment en cas d'exemples et exercices
  - Exercices complémentaires à faire à la maison
    - Travail personnel indispensable
  - Utilisation des téléphones mobiles
    - Interdit en classe ⇒ utiliser le temps des pauses
  - Nourriture et boisson en classe
    - Interdit en classe

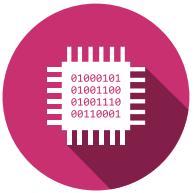
# Appui



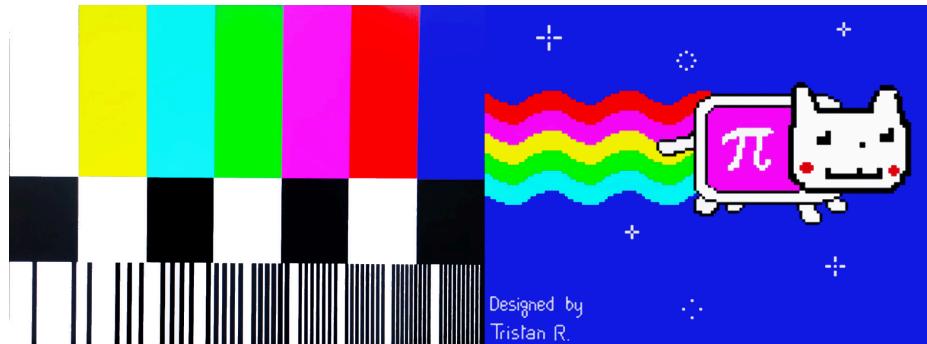
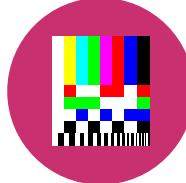
- Selon descriptif de module
- Obligatoire jusqu'au premier examen qui permet de connaître votre niveau
- Une excellente occasion de faire des exercices

# Projet de semestre

## Informatique et systèmes de communication

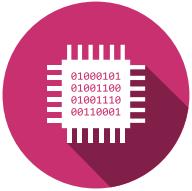


### Display



# Organisation

## Professeurs



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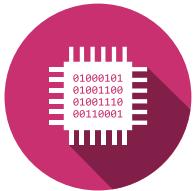
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# Organisation

## Collaborateurs



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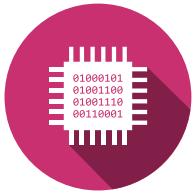
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Rémy Borgeat (BoR)  
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# Serveurs et fichiers

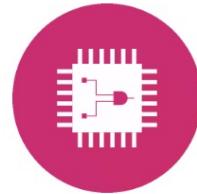
## Informatique et systèmes de communication



- **Moodle ISC Learn**

- 102.1 – Digital design
- Password: *welcome*

INFORMATIONS DU MODULE –  
MODULINFORMATIONEN  
MODULE DID – KURS DID



- **Microsoft Teams**

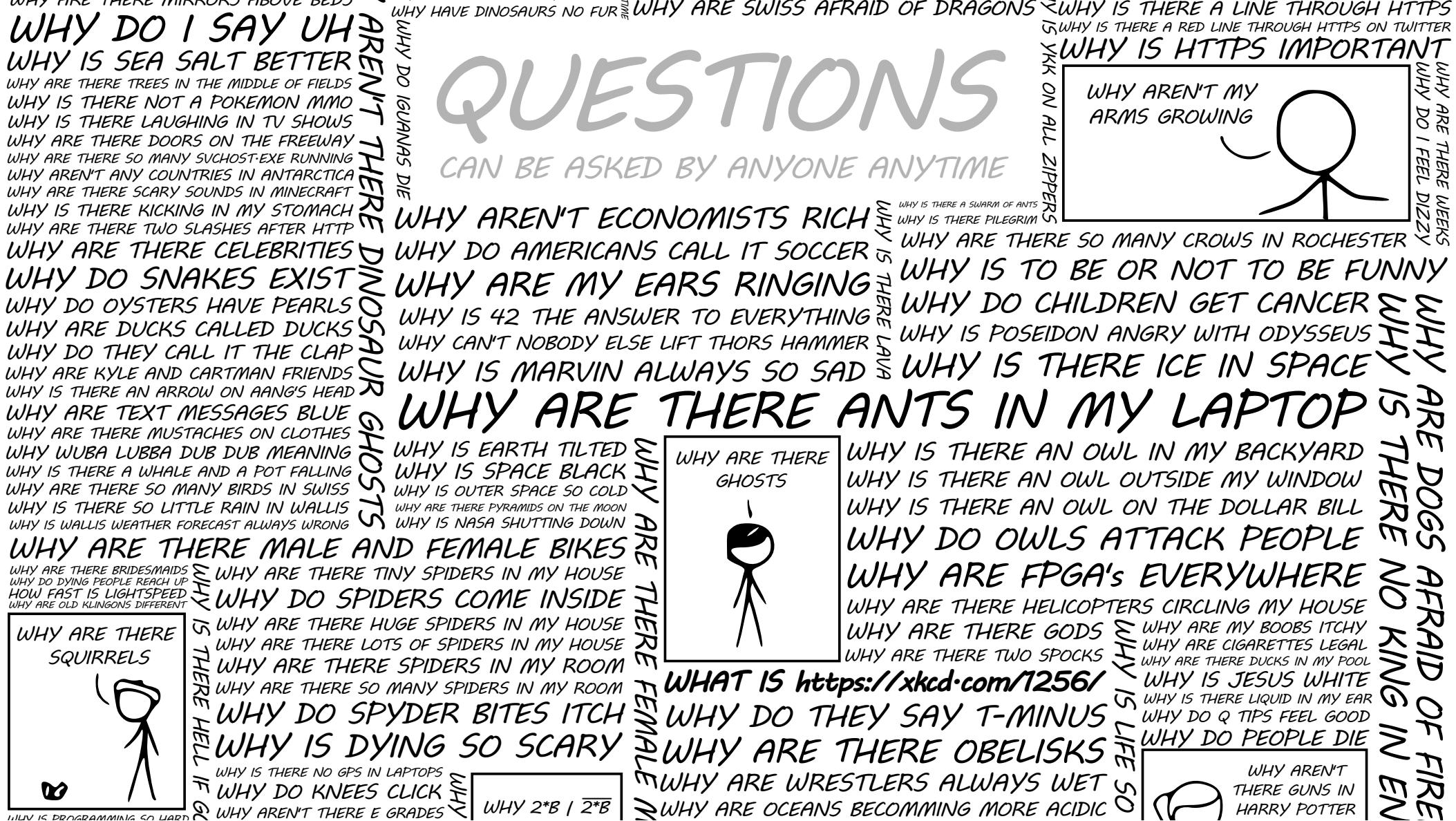
- 24\_ISC\_Car
- Access Code: **ih8wehx**

Le module "102 Architecture materielle" est composé des deux cours suivants: 102.1 Systèmes numériques et 102.2 Architecture des ordinateurs.

Vous trouverez tous les documents de cours sur le repo Git.

Das Modul "102 Materielle Architektur" besteht aus den beiden folgenden Kursen:  
102.1 Digitale Systeme und 102.2 Computerarchitektur.

The screenshot shows the Microsoft Teams desktop application. On the left, there's a sidebar with 'Activity', 'Chat', 'Teams' (which is selected), 'Assignments', and 'App'. The main area shows a team named '20\_eln\_se1d' with 'General' and 'Announcements' channels. To the right, there are two large buttons: 'Join or create a team' and 'Join a team with a code'. Below these buttons, it says 'Bring everyone together and get to work!' and has a 'Join team' button.





Haute Ecole d'Ingénierie  
Hochschule für Ingenieurwissenschaften

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