

## Programcounter (CNT)

Labor Digital Design

## **Contents**



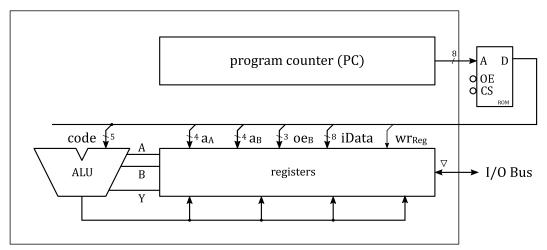


Figure 1:

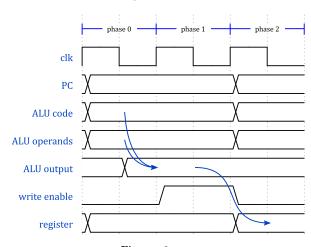
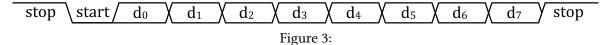


Figure 2:



```
; load stop bit
LOAD
            s3, FF
                                   ; output stop bit
OUTPUT
            s3
                                   ; no operation
LOAD
            s3, s3
                                  ; no operation
LOAD
            s3, s3
LOAD
            s3, s3
                                  ; no operation
LOAD
            s3, s3
                                  ; no operation
LOAD
            s0, 00
                                  ; load start bit
OUTPUT
            s0
                                  ; output start bit
INPUT
            s1
                                  ; load word to send
                                  ; output word, LSB is considered
OUTPUT
            s1
SR0
            s1
                                  ; shift word, bit 1 -> LSB
OUTPUT
                                  ; output bit 1
            s1
SR0
                                  ; bit 2 -> LSB
            s1
                                  ; output bit 2
OUTPUT
            s1
SR0
            s1
                                  ; bit 3 -> LSB
OUTPUT
            s1
                                  ; output bit 3
SR0
            s1
                                  ; bit 4 -> LSB
OUTPUT
            s1
                                   ; output bit 4
SR0
            s1
                                   ; bit 5 -> LSB
```



```
OUTPUT
                                ; output bit 5
SR0
                                ; bit 6 -> LSB
OUTPUT
           s1
                                ; output bit 6
SR0
           s1
                                ; bit 7 -> LSB
OUTPUT
           s1
                                ; output bit 7
           s3, s3
                                ; no operation
LOAD
OUTPUT
           s3
                                ; output stop bit
```

```
LOAD
           s3, FF
                               ; load stop bit
                              ; output stop bit
OUTPUT
          s3
          s2, 04
                              ; initialize loop counter 3
LOAD
                              ; decrement loop counter 4
SUB
          s2, 01
                              ; loop back if not end of count 5
JUMP NZ
          03
                              ; load start bit 6
          s0, 00
LOAD
                              ; output start bit 7
OUTPUT
          s0
                              ; initialize loop counter 8
LOAD
          s2, 08
                             ; load word to send 9
INPUT
          s1
                             ; no operation
LOAD
          s3, s3
                              ; output word, LSB is considered
OUTPUT
          s1
                              ; next bit -> LSB
SR0
          s1
                              ; decrement loop counter
SUB
          s2, 01
JUMP NZ
                              ; loop back if not end of count
          0A
OUTPUT
          s3
                               ; output stop bit
```