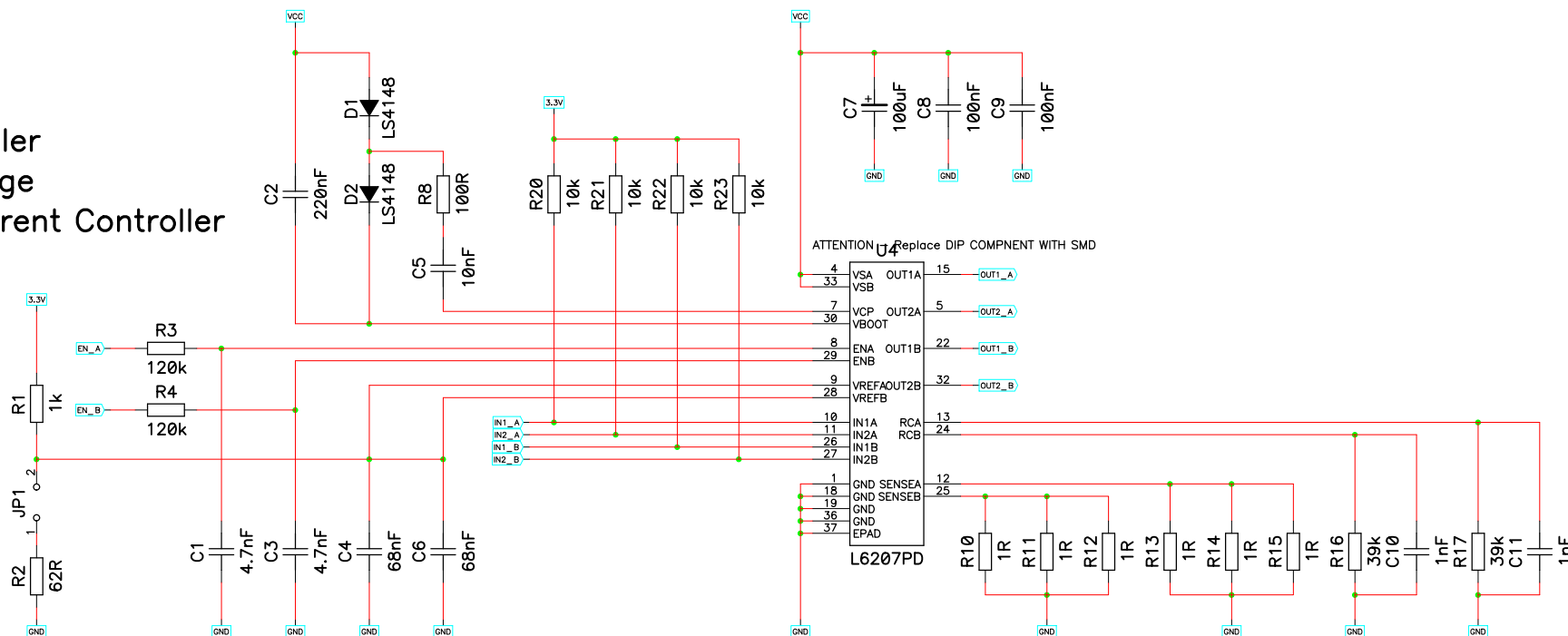
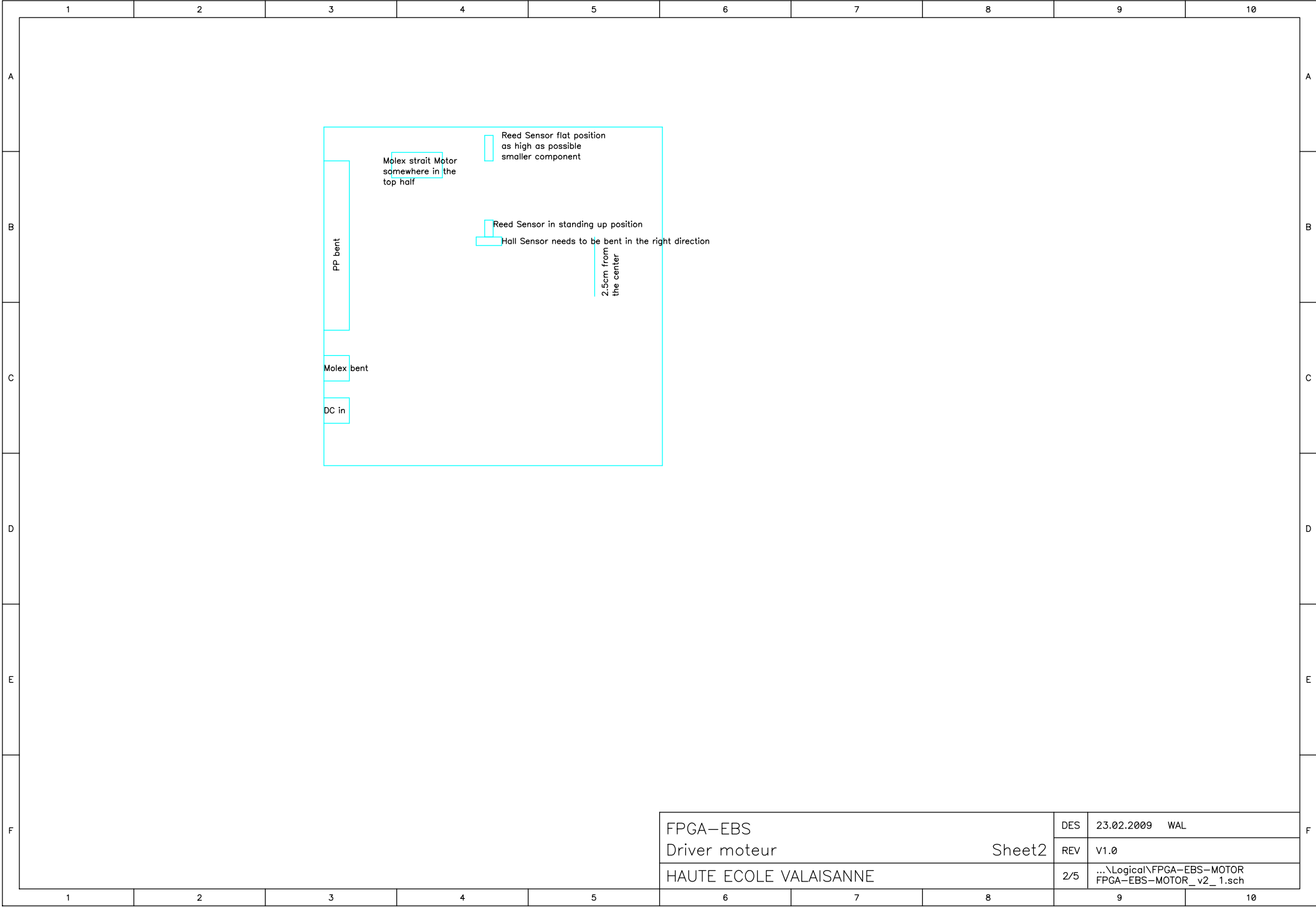


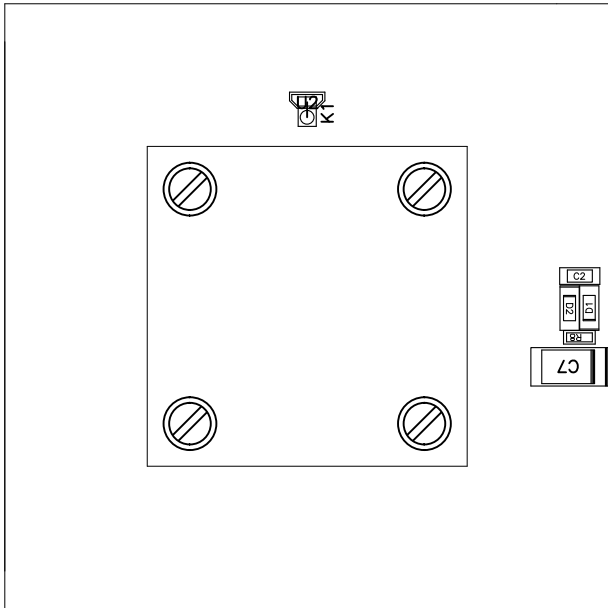
Motor Controller Dual Full Bridge with PWM Current Controller



The diagram shows a square wave signal labeled J1. The signal is connected to VCC (high) and GND (low). The signal transitions from low to high and back to low.



FPGA-EBS Driver moteur HAUTE ECOLE VALAISANNE	DES	23.02.2009 WAL
	REV	V1.0
	2/5	...\\Logical\FPGA-EBS-MOTOR FPGA-EBS-MOTOR_v2_1.sch

	0	1	2	3	4	5	6	7	8	9	
A											A
B											B
C											C
D											D
E											E
F	FPGA-EBS-MOTOR_v2							DES	15.11.2013		F
								REV	V2.0		
	HAUTE ECOLE VALAISANNE							PCB/ FPGA-EBS-MOTOR_v2_1.pcb			
	0	1	2	3	4	5	6	7	8	9	

[illegible]