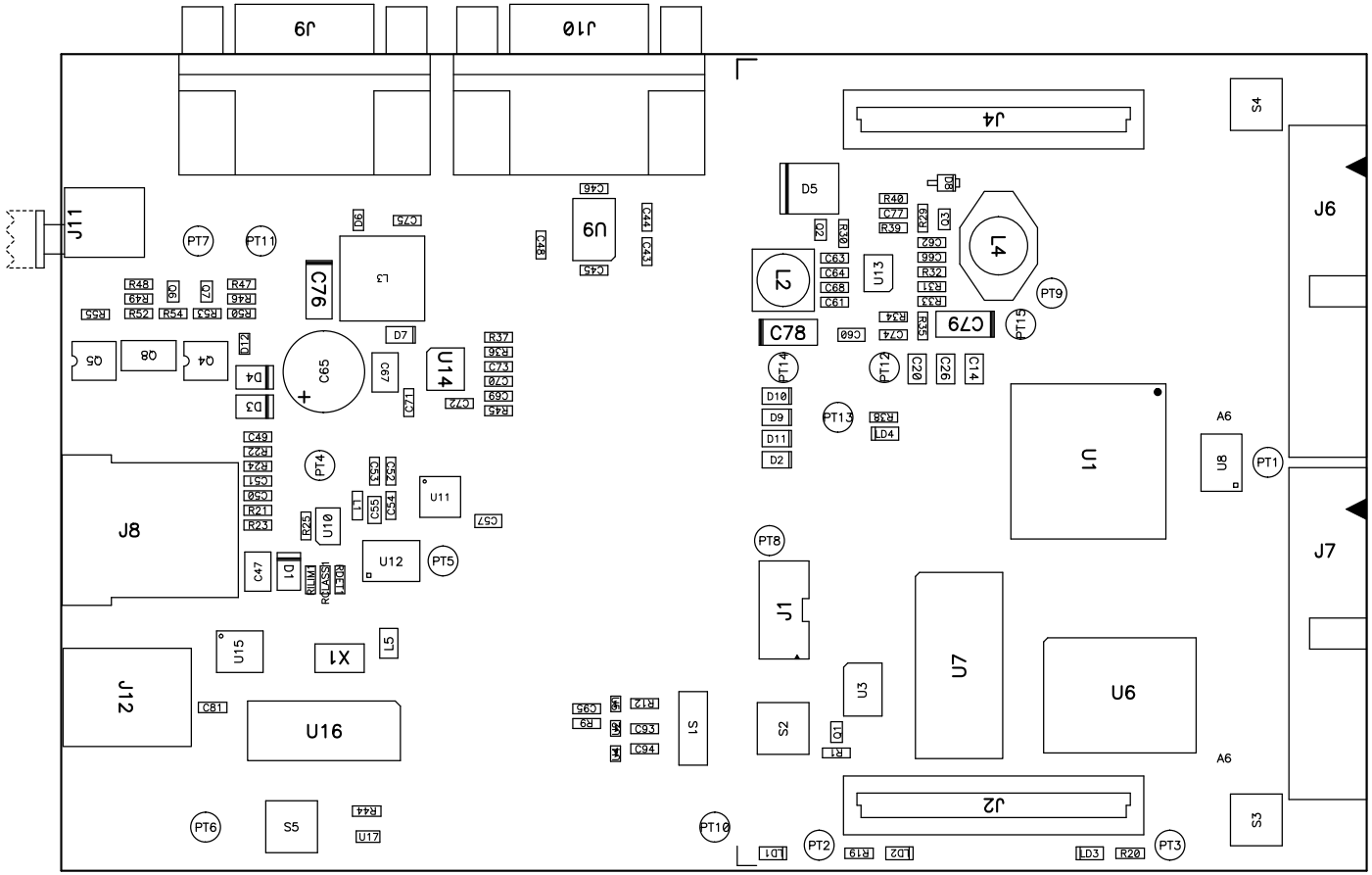

FPGA_ EBS	DES	26.09.2008 WAL
	REV	V2.0
HAUTE ECOLE VALAISANNE – ECOLE D'INGENIEURS	...\FPGA-EBS\FPGA_ EBS_ V2.0 FPGA_ EBS_ V2_ 0.pcb	