## **Project Documentation**

File: CtrlX\_Module\_01\_2025.project

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Profile: ctrlX PLC 1.20.7

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## 1 POU: PRG\_Student

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Practical Work 01 / Write your first PLC Code here
             www.hevs.ch
             Institut Systemes Industriels
             Project: HEVS Pack 2022
                           Cedric Lenoir / Christophe Truffer
 7
             Author:
             Date:
                              2025 January 29
    PROGRAM PRG_Student
10
11
      VAR
       iCount : UINT := 3;
13
         startFromNodeRed : BOOL;
14
         stopFromNodeRed : BOOL;
15
          amplitude : REAL := 10.0;
          frequency Hz : REAL := 0.1;
17
         sampleTime_s : REAL := 0.04;
18
         t s : REAL := 0.0;
19
         sinusSignal : REAL;
21
22
         rTrigStart : R_TRIG;
23
         rTrigStop : R_TRIG;
          tonConveyorStop : TON;
25
    END_VAR
26
27
     VAR CONSTANT
         PI : REAL := 3.14159;
29
     END_VAR
30
     IF iCount >= 100 THEN
          iCount := 0;
      END_IF ;
 3
      iCount := iCount + 1;
 7
      // Sinus generator
 8
      t_s := t_s + sampleTime_s;
 9
      sinusSignal := amplitude * SIN (2 * PI * frequency_Hz * t_s);
10
      // Start conveyor
11
12
      IF rTrigStart . Q THEN
13
          GVL Abox . uaAboxInterface . uaDigitalOut . Output 0 4 := TRUE;
14
     END_IF
15
      // Stop conveyor
17
      IF rTrigStop . Q OR tonConveyorStop . Q THEN
18
       GVL Abox . uaAboxInterface . uaDigitalOut . Output 0 4 := FALSE;
      END_IF
19
20
```

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