Product Name Igloo (Pin Type)	Standard (Pin Type)	Top	Bottom	Standard (Pin Type)	Product Name Igloo (Pin Type)
+3.3V	+3.3V	1	2	GND	GND
GND	GND	3	4	+5V MTB	+5V MTB
+5V MTB	+5V MTB	5	6	+5V MTB	+5V MTB
+5V MTB	+5V MTB	7	8	GND	GND
GND	GND	9	10	JTAG_SEL	JTAG_SEL
POR/	POR/	11	12	GND	GND
TDO	TDO	13	14	RESET/	RESET/
GND	GND	15	16	TDI	TDI
TMS	TMS	17	18	GND	GND
TRST	TRST	19	20	IO0 / CTS1	
GND	GND	21	22	+5V DTB	+5V DTB
+5V DTB	+5V DTB	23	24	GND	GND
SCL1	IO1 / SCL1	25	26	TCK	TCK
GND	GND	27	28	GND	GND
Tx1	IO2 / Tx1	29	30	IO3 / SDA1	SDA1
Rx1	IO4 / RX1	31	32	IO5 / RTS1	
+5V MTB	+5V MTB	33	34	+5V MTB	+5V MTB
+5V MTB	+5V MTB	35	36	+5V MTB	+5V MTB
+5V MTB	+5V MTB	37	38	+5V MTB	+5V MTB
GND	GND	39	40	GND	GND
GND	GND	41	42	GND	GND
P1_1	IO6	43	44	IO7	P1_2
P1_3	IO8	45	46	IO9	P1_4
GND	IO10	47	48	IO110	GND
P1_5	IO12 / SCLK1	49	50	IO13 / MOSI1 / QS1_0	P1_6
P1_7	IO14 / MISO1 / QS1_1	51	52	IO15 / QS1_2	P1_8
GND	GND	53	54	GND	GND
P2_1	IO16 / QS1_3	55	56	IO17	P2_2
P2_3	IO18	57	58	IO19	P2_4
GND	IO20	59	60	IO21	GND
P2_5	IO22	61	62	IO23	P2_6
P2_7	IO24	63	64	IO25	P2_8
GND	GND	65	66	GND	GND
P3_1	IO26	67	68	IO27	P3_2
P3_3	IO28	69	70	IO29	P3_4
GND	IO30	71	72	IO31	GND
P3_5	IO32	73	74	IO33	P3_6
P3_7	IO34	75	76	IO35	P3_8
GND	GND	77	78	GND	GND
	DIFF1-A / CANL	79	80	DIFF1-B / CANH	
+3.3V	+3.3V	81	82	+3.3V	+3.3V
P4_1	IO36	83	84	IO37	P4_2
P4_3	IO38	85	86	IO39	P4_4
+3.3V	+3.3V	87	88	+3.3V	+3.3V
P8_6	IO40	89	90	IO41	P4_6
P4_7	IO42	91	92	IO43	P4_8
	IO44	93	94	IO45	
+3.3V	+3.3V	95	96	+3.3V	+3.3V
	IO46	97	98	IO47	
	IO48	99	100	IO49	
	IO50	101	102	IO51	
+3.3V	+3.3V	103	104	+3.3V	+3.3V
	IO52	105	106	IO53	
	IO54	107	108	IO55	
	IO56	109	110	IO57	
+3.3V	+3.3V	111	112	+3.3V	+3.3V
	IO58	113	114	IO59	
	IO60	115	116	IO61	
+3.3V	+3.3V	117	118	+3.3V	+3.3V
	DIFF2-A	119	120	DIFF2-B	
GND	GND	121	122	GND	GND
P5_1	IO62 / MDC	123	124	IO63 / MDIO	P5_2
P5_3	IO64 / INTR	125	126	IO65 / nRESET	P5_4
GND	IO66 / CRS	127	128	IO67 / COL	GND
P5_5	IO68 / TX_CLK	129	130	IO69 / GTX_CLK	P5_6
P5_7	IO70 / TX_EN	131	132	GND	GND
GND	GND	133	134	IO71 / TX_ER	P5_8
	IO72 / TXD0	135	136	IO73 / TXD1	
	IO74 / TXD2	137	138	GND	GND
GND	GND	139	140	IO75 / TXD3	
	IO76 / TXD4	141	142	IO77 / TXD5	
	IO78 / TXD6	143	144	GND	GND
GND	GND	145	146	IO79 / TXD7	
P6_1	IO80 / RX_CLK	147	148	IO81 / RX_DV	P6_2
GND	IO82 / RX_ER	149	150	IO83 / RXD0	GND
P6_3	IO84 / RXD1	151	152	IO85 / RXD2	P6_4
P6_5	IO86 / RXD3	153	154	IO87 / RXD4	P6_6
GND	GND	155	156	GND	GND
P6_7	IO88 / RXD5	157	158	IO89 / RXD6	P6_8
	IO90 / RXD7	159	160	IO91	
GND	GND	161	162	GND	GND
	DIFF3-A / SDA2	163	164	DIFF3-B /SCL2	
GND	GND	165	166	DIFF4-A / Tx2	
	DIFF4-B / Rx2	167	168	GND	GND
	DIFF5-A /RTS2	169	170	DIFF5-B / CTS2	
GND	GND	171	172	GND	GND
P7_1	IO92 / SCLK2	173	174	IO93 / MOSI2 / QS2_0	P7_2
P7_3	IO94 / MISO2 / QS2_1	175	176	IO95 / QS2_2	P7_4
GND	GND	177	178	GND	GND
P7_5	IO96 / QS2_3	179	180	IO97	P7_6
P7_7	IO98	181	182	IO99	P7_8
GND	IO100	183	184	IO101	GND
P8_1	IO102	185	186	IO103	P8_2
GND	GND	187	188	DIFF6-A	P8_3
P8_4	DIFF6-B	189	190	GND	GND
P8_5	IO104	191	192	IO105	P4_5
GND	IO106	193	194	IO107	P8_7
P8_8	IO108	195	196	IO109	GND
	IO110	197	198	IO111	
+3.3V	+3.3V	199	200	GND	GND

FREE PINS (no NC) : 0
FREE PINS (NC comprised) : 42

Signals names as seen from the daughterboard if not specified otherwise (e.g. Tx is the output of FPGA's UART block)

GND	Common ground
+3.3V	Daughterboard 3.3V output (max 1.5 [A])
+5V DTB	Daughterboard 5V output (LOW CURRENT CONSUMPTION IF RUNNING ON USB !)
+5V MTB	Motherboard 5V output
Txx	JTAG signals /JTAG_SEL : 1 = no extra chip, 0 = extra JTAG line used)
RESET/	Daughterboard reset input (active low)
POR/	Daughterboard PowerOnReset signal output (active low)
DIFFx-A/B	Differential pair I/O preferred pins (can be used as standard I/O)
IOx	Input / output pin
TX / Rx	3.3V EUSART signals, any I/O for complementary (CTR, DTS …)
SCL / SDA	3.3V I2C signals
SCLK / MOSI / MISO	3.3V SPI signals, any I/O as slave select
QS	SPI dual/quad interface
CANL / CANH	3.3V CAN signals (REQUIRES A TRANSCEIVER ON THE MOTHERBOARD !)
TXD, RXD …	Gigabit ethernet signals ((G)MII, MDC/MDIO, interrupt)

Jedec 4.20.11-1 link :
- Power pins, except pin 200 (added as GND), 199 and 1 as standard +3.3V
- 69, 83, 120 and 163 (test pins) as standard I/Os
- 30, 32, 164 and 166 (clock outputs) as standard I/Os
- Fixed JTAG signals, + RESET/ and POR/