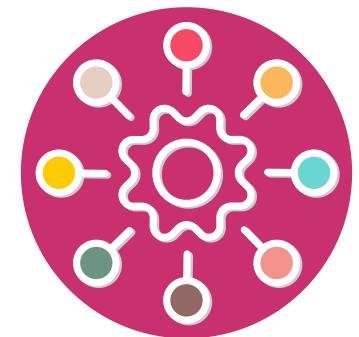
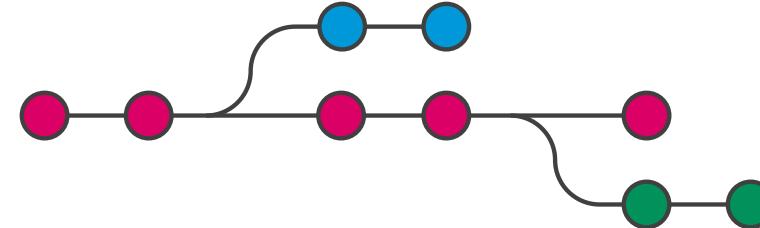




# System Design Version Control

Systems Engineering program



Silvan Zahno [silvan.zahno@hevs.ch](mailto:silvan.zahno@hevs.ch)



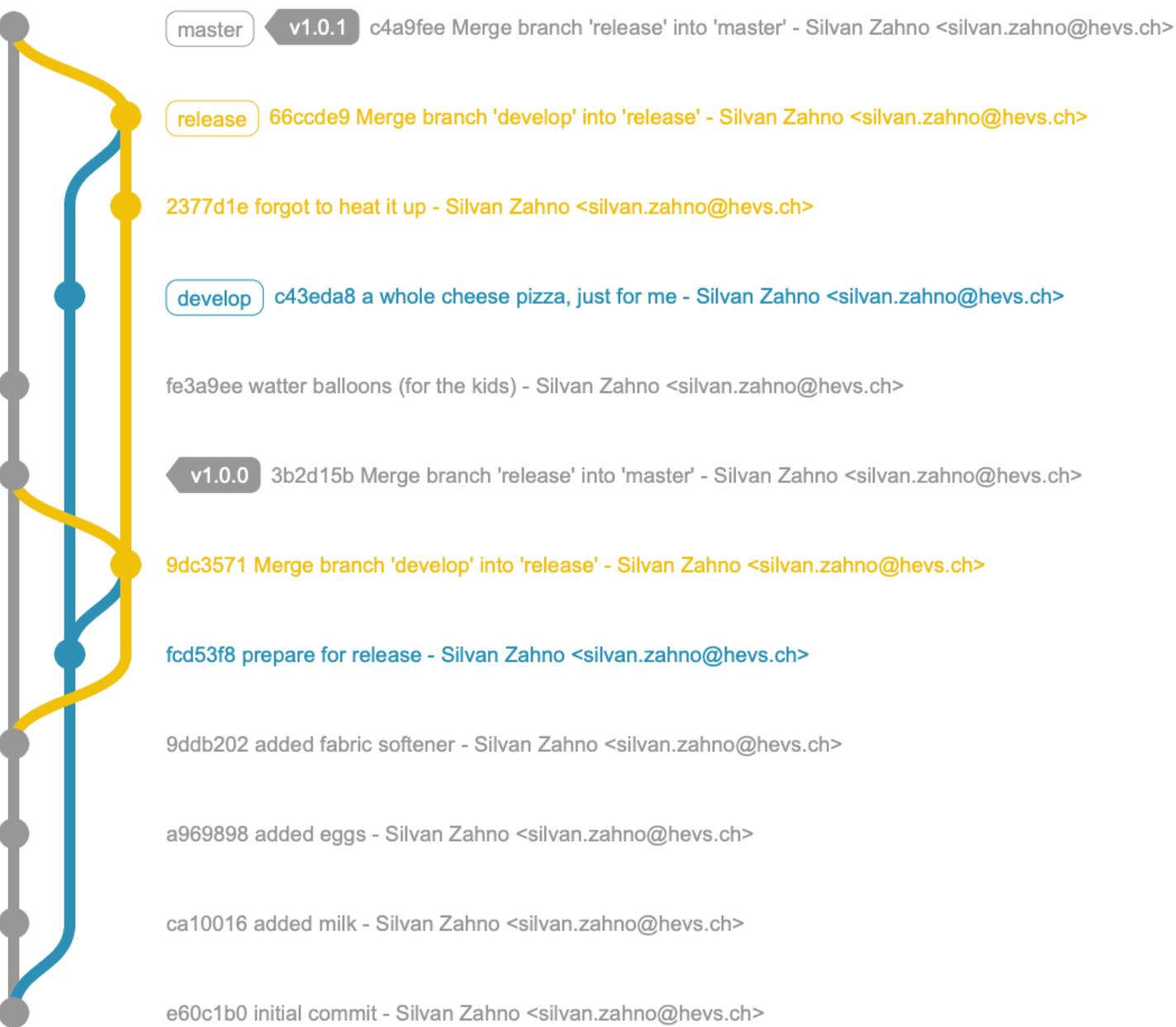
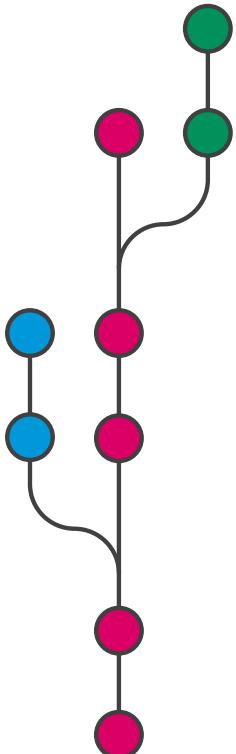
# Your current system

```
.
```

- └── important-file copy.txt
- └── important-file v1.txt
- └── important-file v2.1 backup.txt
- └── important-file v2.1 backup.txt.old
- └── important-file v2.1.txt
- └── important-file v2.txt
- └── important-file.txt

1 directory, 7 files

# Version control





# Possible Tools

Git (git)



Subversion (svn)



Mercurial (hg)



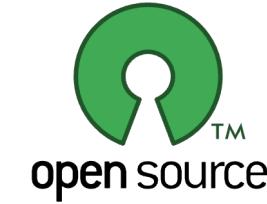
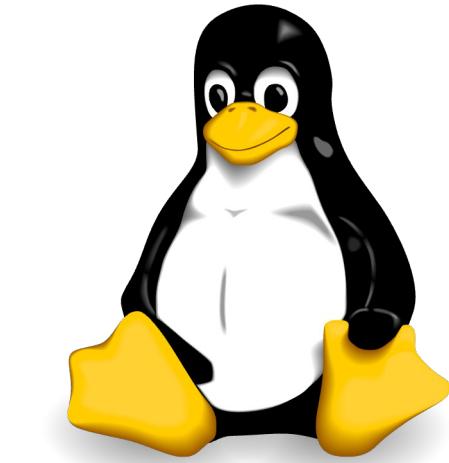
mercurial

SyD Version Control

Linus Torvalds

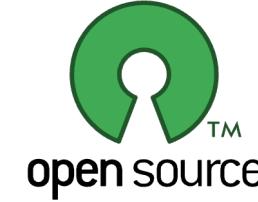
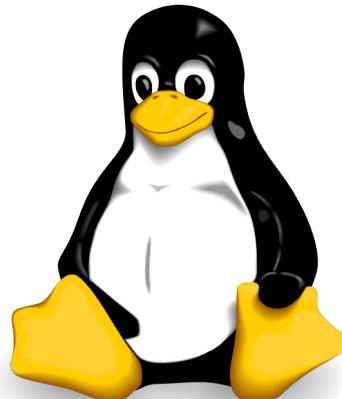
Linux (1991)

Git (2005)



# Why Linux needs git

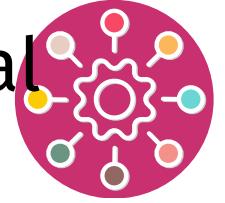
Linux has become the largest collaborative development project in the history of computing over the last 30 years.



- 600 active Linux distribution
- 85% of all Smartphones
- 500 top supercomputers
- >27.8 million lines of code
- > 12'000 contributors
- > 1 million commits

<https://truelist.co/blog/linux-statistics/>

# Why you need git in Power&Control or Design&Material



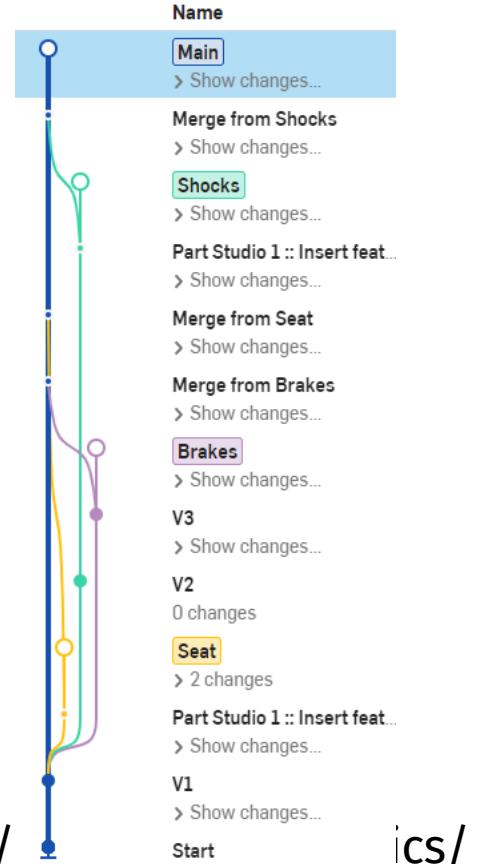
## Versioning

- Tracking Changes
- Collaboration
- Rollback Capabilities
- Documentation
- Deployment



**AUTODESK**  
Vault

<https://truelist.co/>





# Git Platforms

## Gitlab

<https://gitlab.com>

<https://gitlab.hevs.ch>

## Github

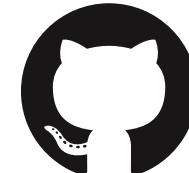
<https://github.com>

## Bitbucket

<https://bitbucket.com>



GitLab



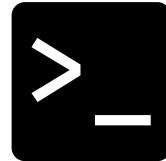
GitHub



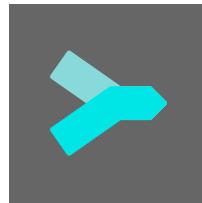
Bitbucket

# Git Tools

Commandline



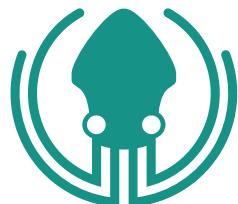
Sublime Merge



Git Cola



Git Kraken



Fork



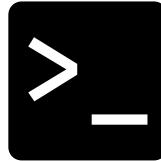
Tower



SmartGit



# Git Commandline



git status

git diff

git add <file>

git diff --staged

git reset <file>

git commit -m "<commit message>"

git fetch <remote>

git merge <remote> <branch>

git push <remote> <branch>

git pull

```
Last login: Tue Mar  8 09:26:26 on ttys004
[zas@zac ~] (base)
$ git --help
usage: git [--version] [--help] [-C <path>] [-c <name>=<value>]
           [--exec-path=<path>] [--html-path] [--man-path] [--info-path]
           [-p | --paginate | -P | --no-pager] [--no-replace-objects] [--bare]
           [--git-dir=<path>] [--work-tree=<path>] [--namespace=<name>]
           [--super-prefix=<path>] [--config-env=<name>=<envvar>]
           <command> [<args>]

These are common Git commands used in various situations:

start a working area (see also: git help tutorial)
clone   Clone a repository into a new directory
init    Create an empty Git repository or reinitialize an existing one

work on the current change (see also: git help everyday)
add     Add file contents to the index
mv     Move or rename a file, a directory, or a symlink
restore Restore working tree files
rm     Remove files from the working tree and from the index

examine the history and state (see also: git help revisions)
bisect  Use binary search to find the commit that introduced a bug
diff    Show changes between commits, commit and working tree, etc
grep    Print lines matching a pattern
log     Show commit logs
show    Show various types of objects
status  Show the working tree status

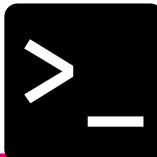
grow, mark and tweak your common history
branch  List, create, or delete branches
commit  Record changes to the repository
merge   Join two or more development histories together
rebase  Reapply commits on top of another base tip
reset   Reset current HEAD to the specified state
switch  Switch branches
tag     Create, list, delete or verify a tag object signed with GPG

collaborate (see also: git help workflows)
fetch   Download objects and refs from another repository
pull    Fetch from and integrate with another repository or a local branch
push    Update remote refs along with associated objects

'git help -a' and 'git help -g' list available subcommands and some
concept guides. See 'git help <command>' or 'git help <concept>'
to read about a specific subcommand or concept.
See 'git help git' for an overview of the system.

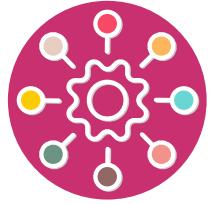
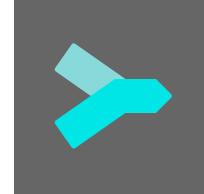
[zas@zac ~] (base)
$
```

# git Commands



Command	Description	Command	Description
<b>Start a working area</b>		<b>Grow, mark and tweak your common history</b>	
clone	Clone a repository into a new directory	branch	List, create, or delete branches
init	Create an empty Git repository or reinitialize an existing one	checkout	Switch branches or restore working tree files
<b>Work on the current change</b>		commit	Record changes to the repo
add	Add file contents to the index	diff	Show changes between commits, commit and working tree, etc
mv	Move or rename a file, a directory, or a symlink	merge	Join two or more development histories together
reset	Reset current HEAD to the specified state	rebase	Reapply commits on top of another base tip
rm	Remove files from the working tree and from the index	tag	Create, list, delete or verify a tag object
<b>Examine the history and state</b>		<b>Collaborate</b>	
log	Show commit logs	fetch	Download objects and refs from another repo
show	Show various types of objects	pull	Fetch from and integrate with another repo or a local branch
status	Show the working tree status	push	Update remote refs along with associated objects

# Sublime Merge



~/work/repo/edu/car/car-course

LICENSE UPGRADE REQUIRED

master

Commits Files Summary

LOCATIONS

BRANCHES (1) master

REMOTES (1) origin

TAGS (0) master

STASHES (0)

SUBMODULES (0)

1 unstaged file Commit Changes

Merge remote-tracking branch 'origin/master' 16

CHG: updated planning zas master origin/master Thu 08:11

ADD: ALU and ImmSrc doc Axam Thu, 13 Apr 15:19

ADD: EBS2/EBS3 specs Axam Tue, 11 Apr 15:25

FIX: memory stack images zas Tue, 4 Apr 11:00

FIX: errors in immediate and type images zas Tue, 4 Apr 07:46

ADD: files in arc exercises zas Mon, 3 Apr 13:30

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CHG: Planning zas Fri, 31 Mar 08:45

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zас Tue, 28 Mar 07:29

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FIX: add scripts folder Axel Amand Thu, 16 Mar 09:31

REM: car-heirv and car-labs doc deployment Axel Amand Thu, 16 Mar 09:18

CHG: BEM labo from geekbench 5 to 6 zас Tue, 14 Mar 14:25

UPD: all PDFs Axam Wed, 8 Mar 19:10

FIX: errors in ARC, ISA, FUN and PER slides zас Tue, 7 Mar 09:09

Commit Hash 702c8ff178adbf6639884c48b72e4bc68361d13c f163cea8c5f992b7c78549993694b6670e0da8b

Tree zas<silvan.zahn@nev.ch>

Author zas<silvan.zahn@nev.ch>

Date Thu, 20 Apr 2023 08:12

Parents 6e927ef6, 68810079

Branches master origin/master

Stats 16 files changed: 15 +10

Merge remote-tracking branch 'origin/master'

Collapse all

labo/latex/b2-content/scr/00-solution.tex -1 +5

Subsection: Simulation:

```
164 done;
165    beq x2, x2, main      # infinite loop
166  end(minted)
167 \newline\nullnewline
168 Each instruction takes one clock cycle => 19 are executed (addi x5, x0, 0 not
executed because of previous beq; addi x2, x0, 1 not executed because of jal).
=> 19/6M = 287.9 ns.
```

Subsection: Simulation:

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```

On the EBS2 board @ 66MHz \rightarrow \\$T\_{exec} = \frac{nb\\_cycles}{F\_{sys}} = \frac{19}{(19)(6M)} = 287.9 ns.

On the EBS3 board @ 50MHz \rightarrow \\$T\_{exec} = \frac{nb\\_cycles}{F\_{sys}} = \frac{19}{(19)(5M)} = 380 ns.

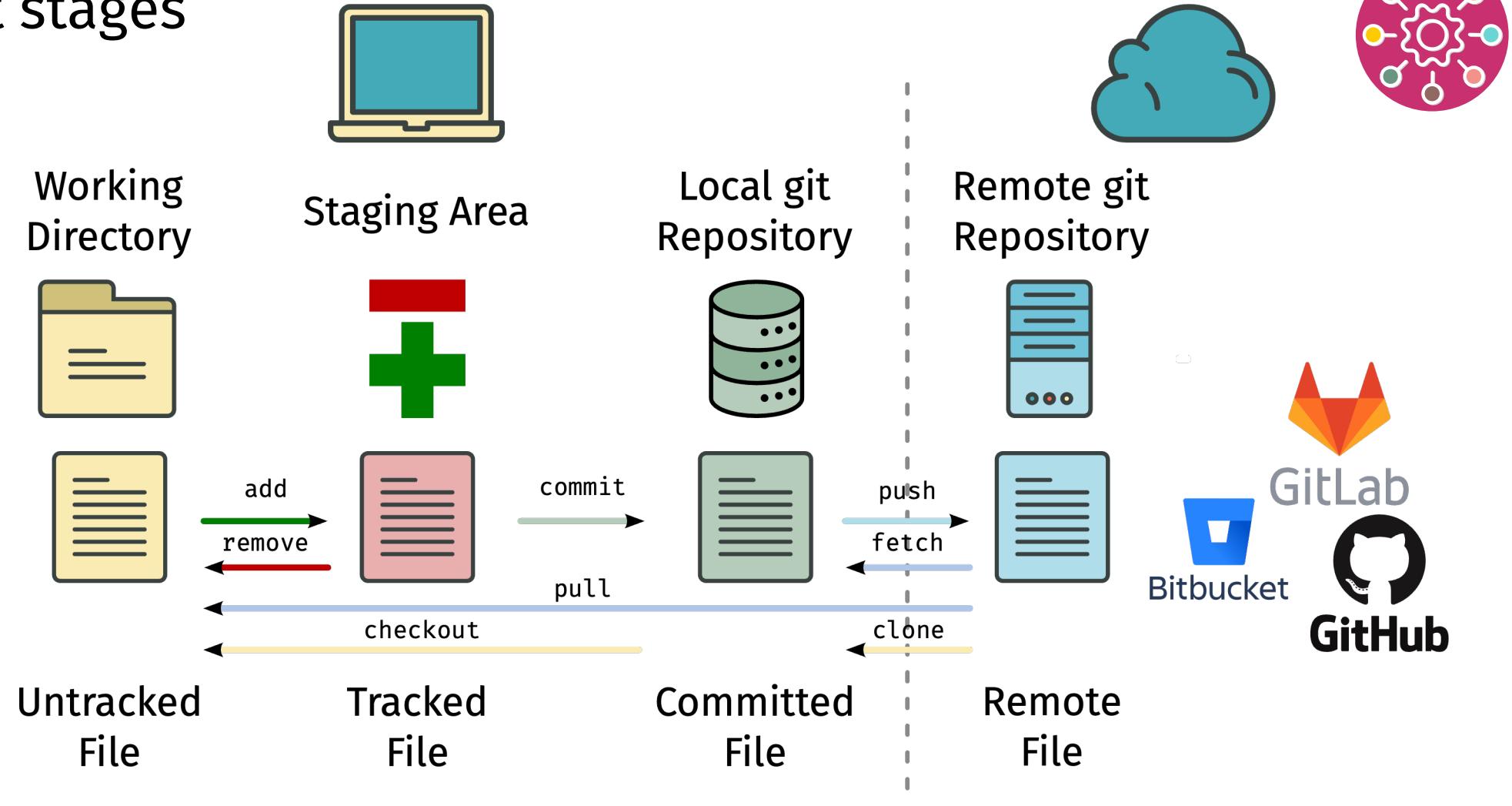
Subsection: Umsetzung:

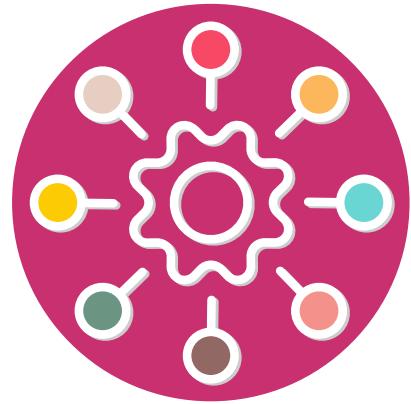
```
164 Le \textbf{mainDecoder} peut être écrit en VHDL. Pour cela, vous pouvez analyser
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165
166 \textbf{Note:} Dans HDL Designer, lorsque vous sélectionnez le type de contenu d'un
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167 }
168 \opt{d}{%
169 Schreiben Sie hierzu für beide Subblöcke, \textbf{mainDecoder} sowie \textbf{ALUDecoder}, eine Wahrheitstabelle für alle benötigten Instruktionen.
170 \opt{f}{\caption{figure}{Exemple de code MainDecoder}}
171 \opt{d}{\caption{figure}{MainDecoder Code-Beispiel}}
172 \label{fig:riscv-mainDecoder-code}
173
174 \subsubsection{ALU}
175 \opt{f}{%
176 L'ALU réalise les fonctions arithmétiques et logiques selon la table suivante:
177 }
178 \opt{d}{%
179 Die ALU realisiert die arithmetischen und logischen Funktionen gemäß der folgenden
Tabelle:
180 }
181
182 \begin{table}[h]
```

g:riscv-mainDecoder-code:

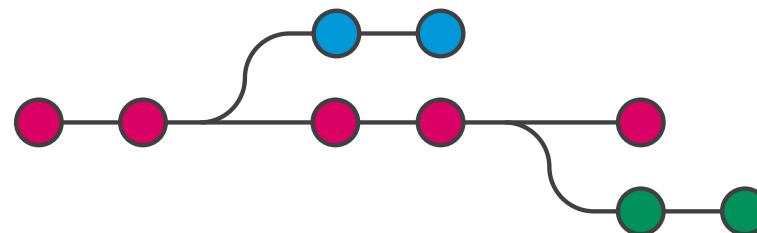
```
110 \opt{f}{\caption{figure}{Exemple de code MainDecoder}}
111 \opt{d}{\caption{figure}{MainDecoder Code-Beispiel}}
112 \label{fig:riscv-mainDecoder-code}
113
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119 Die ALU realisiert die arithmetischen und logischen Funktionen gemäß der folgenden
Tabelle:
120 }
121
122 \begin{table}[h]
```

# Git stages





## Git Branch and Merge Example

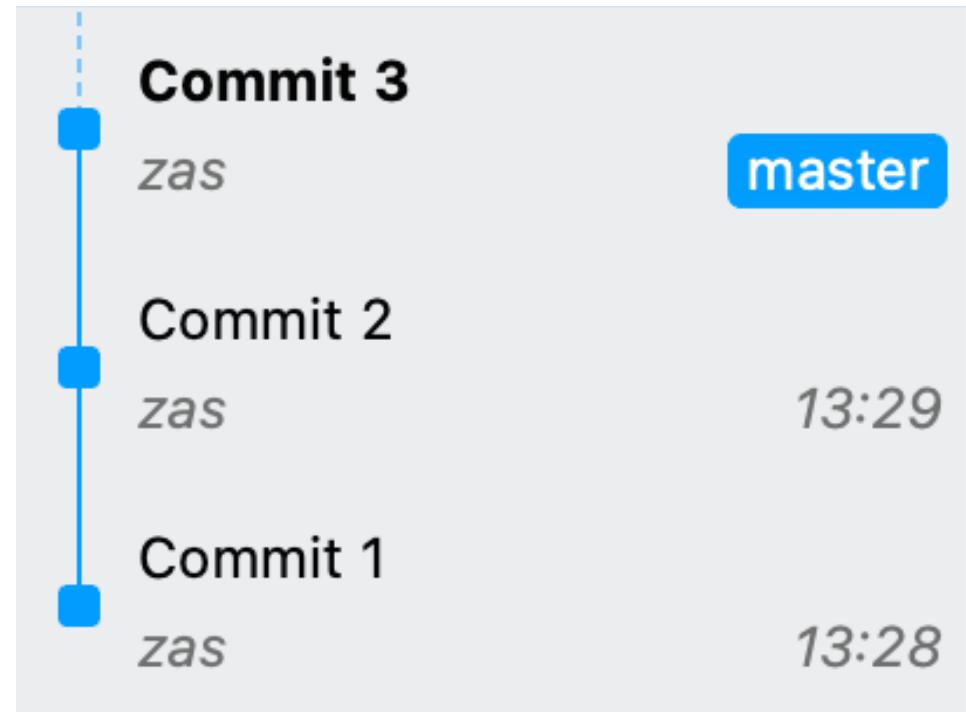




# Branch and Merge

## Initial repo state

Every repo has either a **main** or **master** branch as default branch



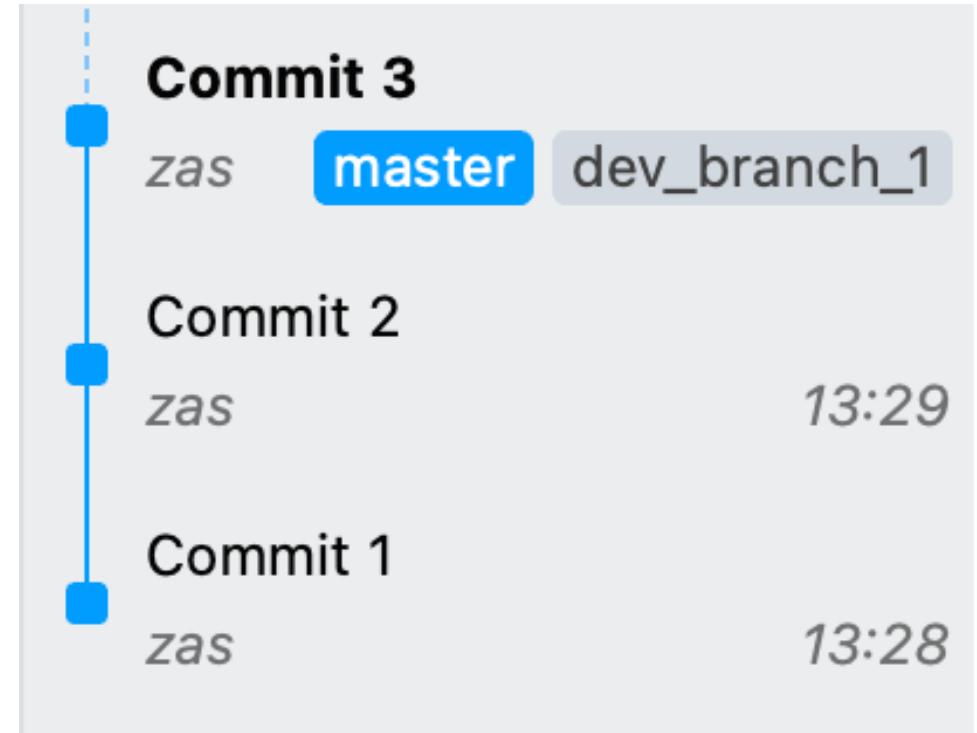


# Branch and Merge

Create branch dev\_branch\_1

Create branch dev\_branch\_1

```
$ git branch dev_branch_1
```





## Branch and Merge

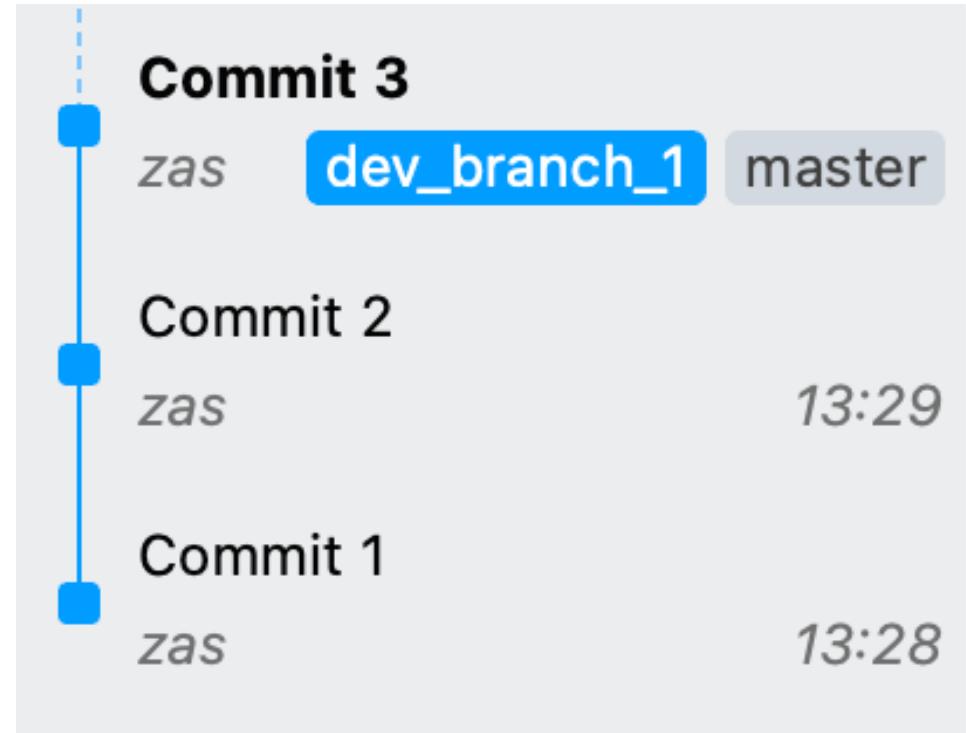
**Checkout** branch dev\_branch\_1

Check on which branch we are on

```
$ git branch
```

Checkout branch dev\_branch\_1

```
$ git checkout dev_branch_1
```





# Branch and Merge

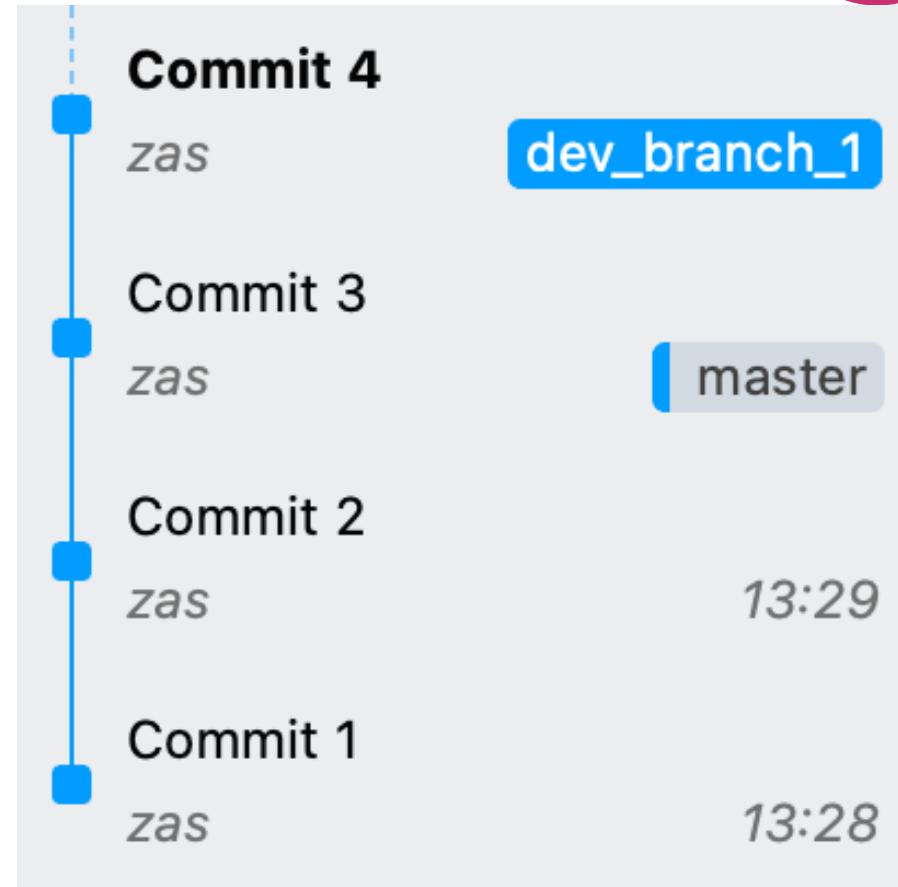
**Commit** on dev\_branch\_1

Stage new file

```
$ git add file.md
```

Commit stages files

```
$ git commit -m "Commit 4"
```





# Branch and Merge

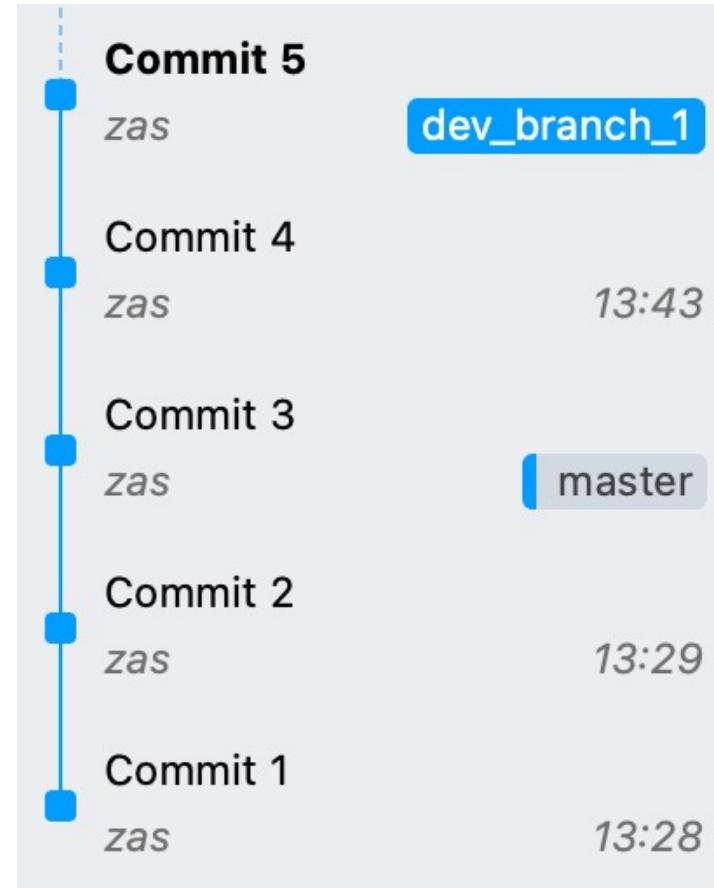
**Commit** on dev\_branch\_1

Stage new file

```
$ git add file.md
```

Commit stages files

```
$ git commit -m "Commit 5"
```





# Branch and Merge

**Commit** on master branch

Checkout master branch

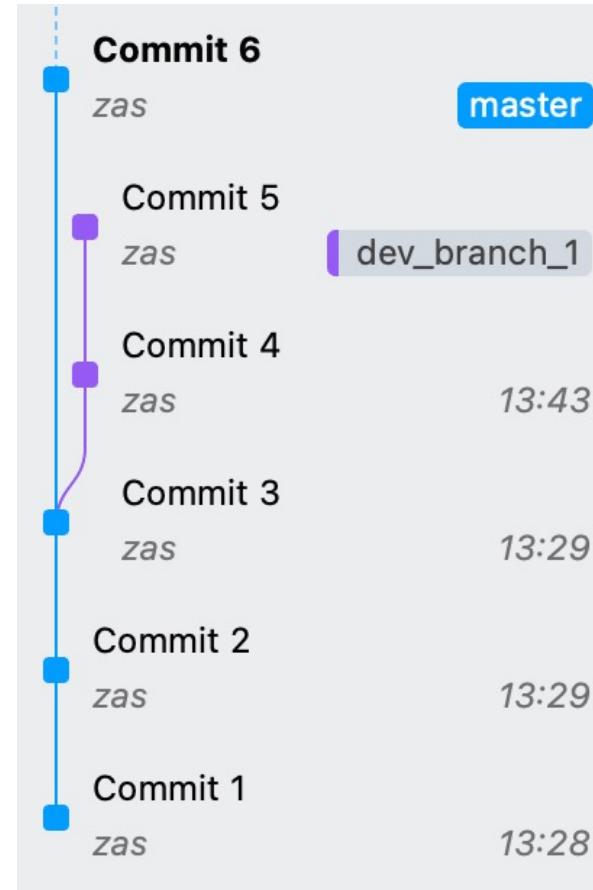
```
$ git checkout master
```

Stage new file

```
$ git add file.md
```

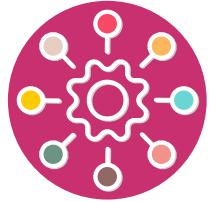
Commit stages files

```
$ git commit -m "Commit 6"
```



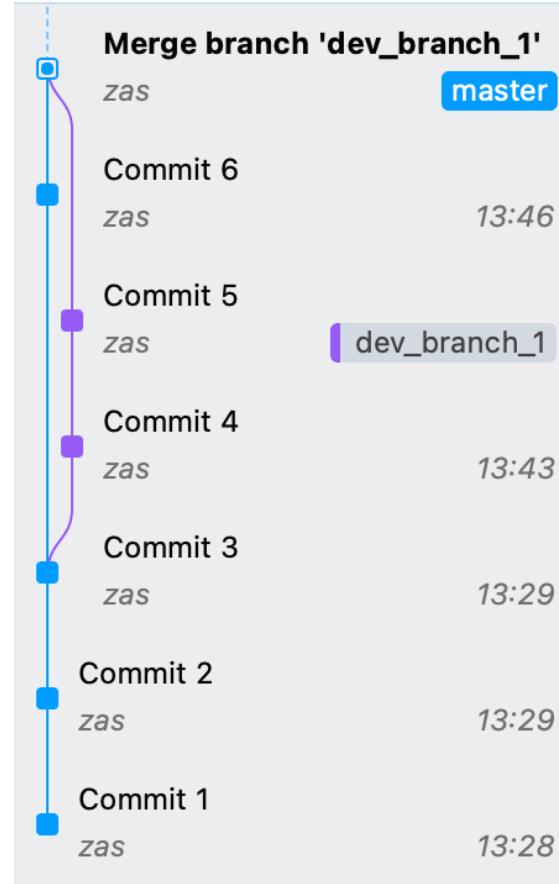
# Branch and Merge

Three way merge master and dev\_branch\_1



Merge dev\_branch\_1 into master

```
$ git merge dev_branch_1
```



# Demo



~/work/repo/edu/car/car-course

LICENSE UPGRADE REQUIRED

master

Commits Files Summary

BRANCHES (1) master

REMOTES (1) origin

TAGS (0) master

STASHES (0)

SUBMODULES (0)

1 unstaged file Commit Changes

Merge remote-tracking branch 'origin/master' 16

CHG: updated planning zas master origin/master Thu 08:11

ADD: ALU and ImmSrc doc Axam Thu, 13 Apr 15:19

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Tree zas+silvan.zahn@nevs.ch

Author zas+silvan.zahn@nevs.ch

Date Thu, 20 Apr 2023 08:12

Parents 6e927ef6, 68810079

Branches master origin/master

Stats 16 files changed: 15 +10

...Collapse all

Subsection: Simulation:

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Le \textbf{mainDecoder} peut être écrit en VHDL. Pour cela, vous pouvez analyser l'exemple de code \ref{fig:riscv-mainDecoder-code} ci-dessous et l'adapter en conséquence.

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...  
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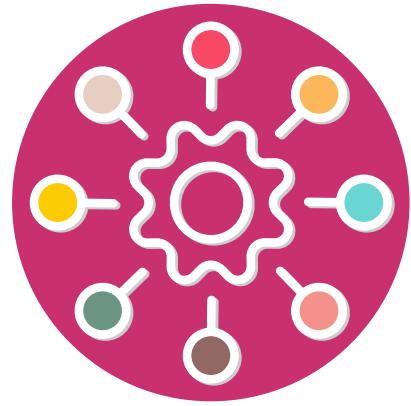
```
110 \opt{f}{\caption{figure}{Exemple de code MainDecoder}}
111 \opt{d}{\caption{figure}{MainDecoder Code-Beispiel}}
112 \label{fig:riscv-mainDecoder-code}
```

...  
Subsubsection: ALU

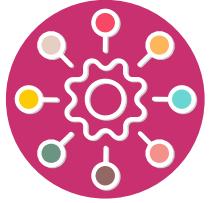
(opt{f})  
The ALU réalise les fonctions arithmétiques et logiques selon la table suivante:

(opt{d})  
Die ALU realisiert die arithmetischen und logischen Funktionen gemäß der folgenden Tabelle:

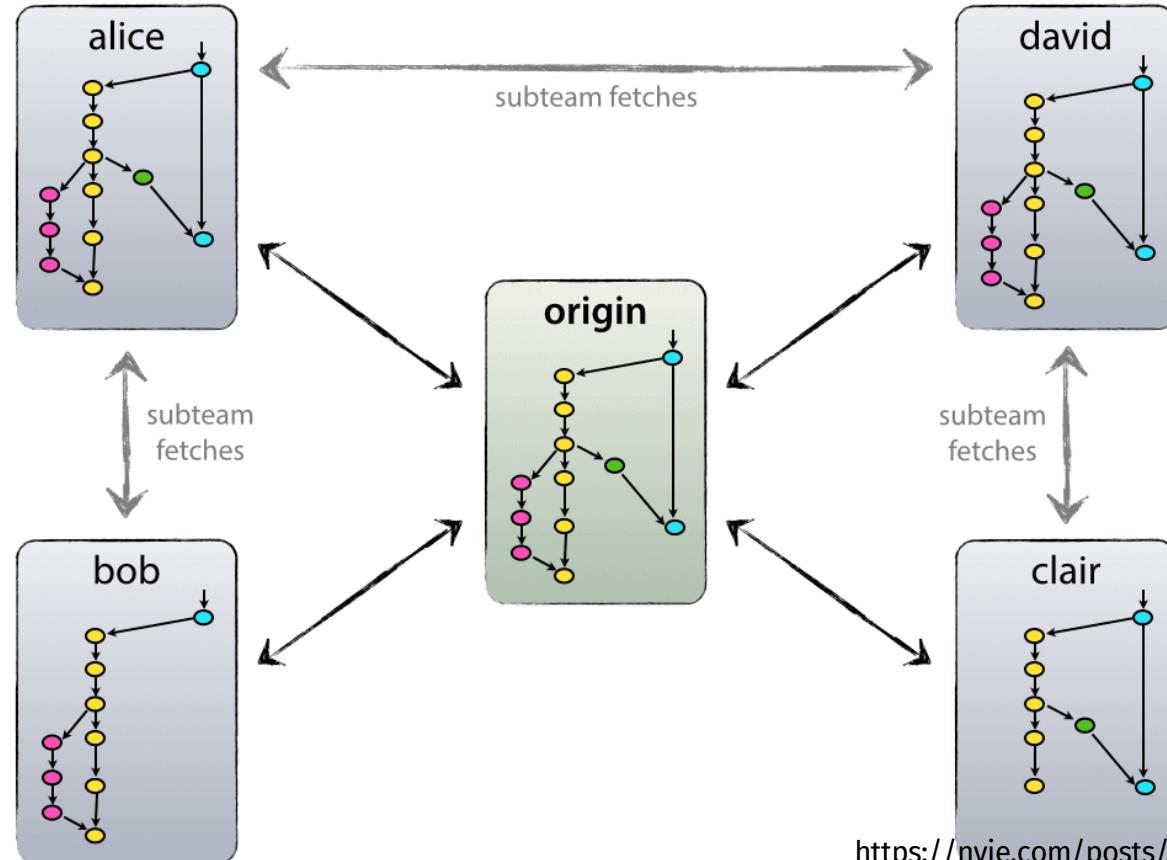
...  
begin\_table | h



# Gitflow

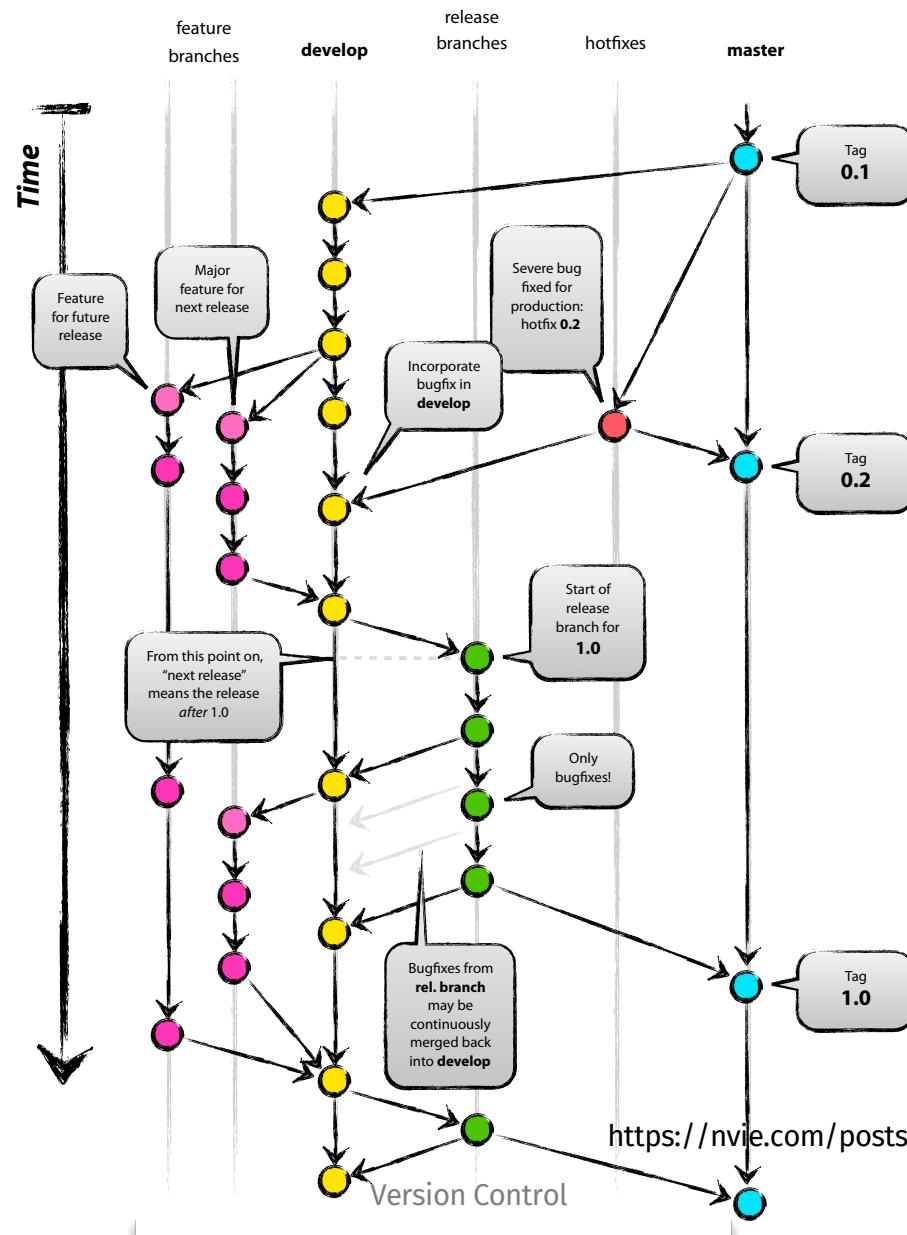


# Gitflow Collaboration

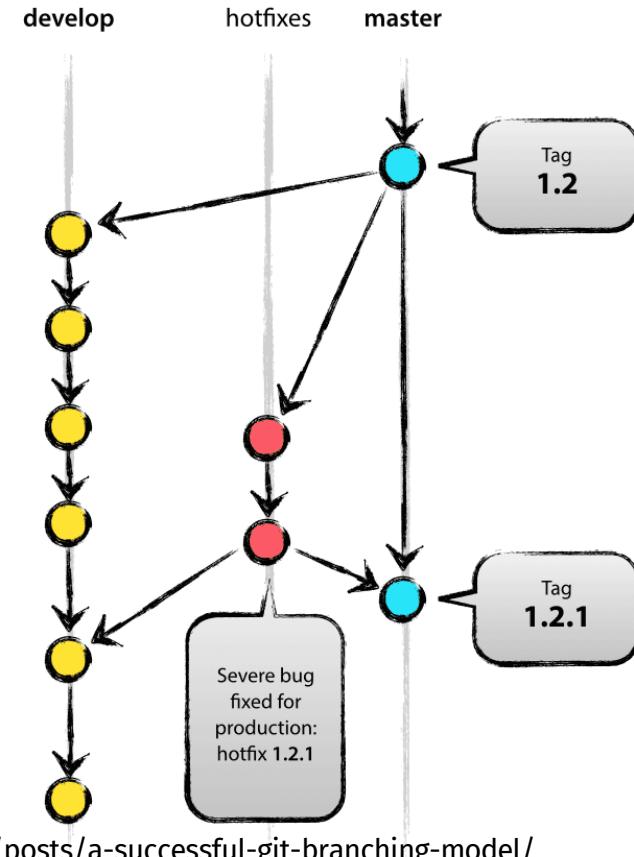
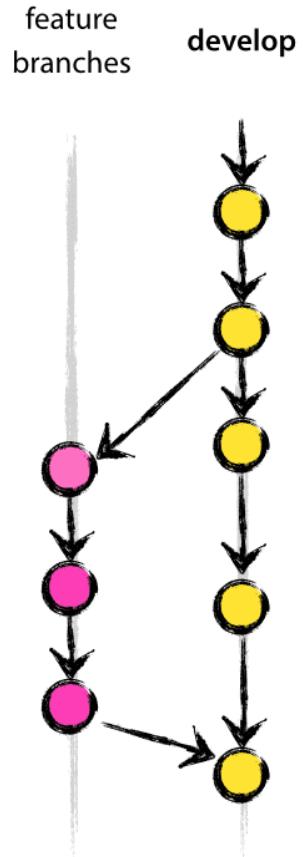
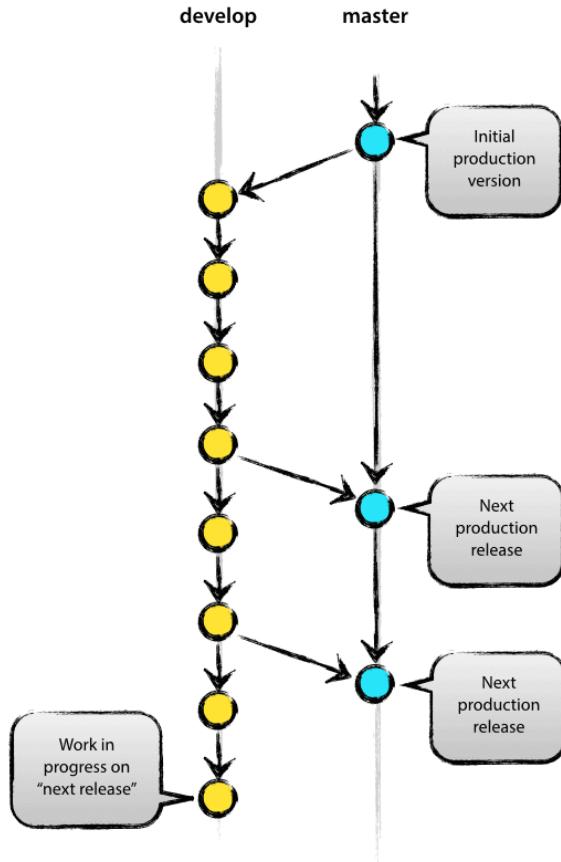


<https://nvie.com/posts/a-successful-git-branching-model/>

# Gitflow



# Gitflow Branching

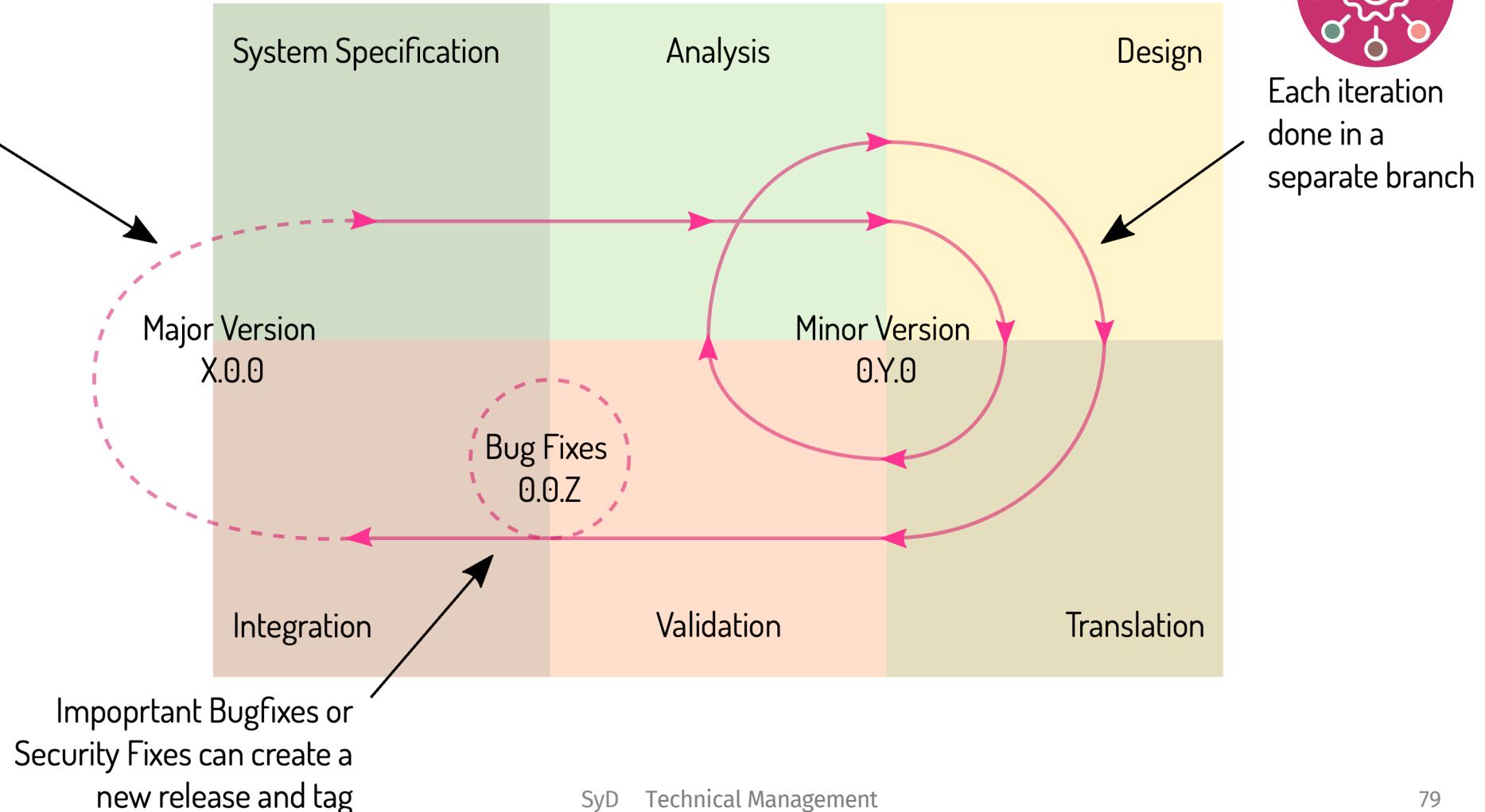


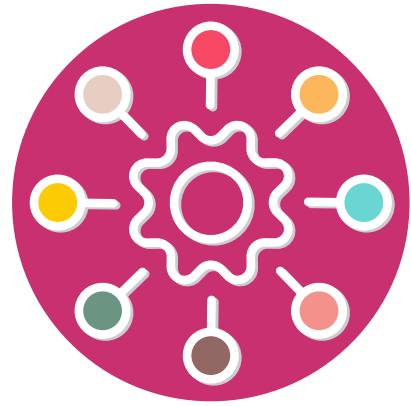
<https://nvie.com/posts/a-successful-git-branching-model/>

# Gitflow vs 6q



Each major version change creates a tag



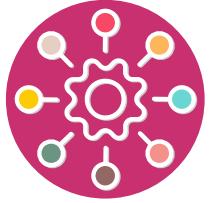


# Git CI/CD

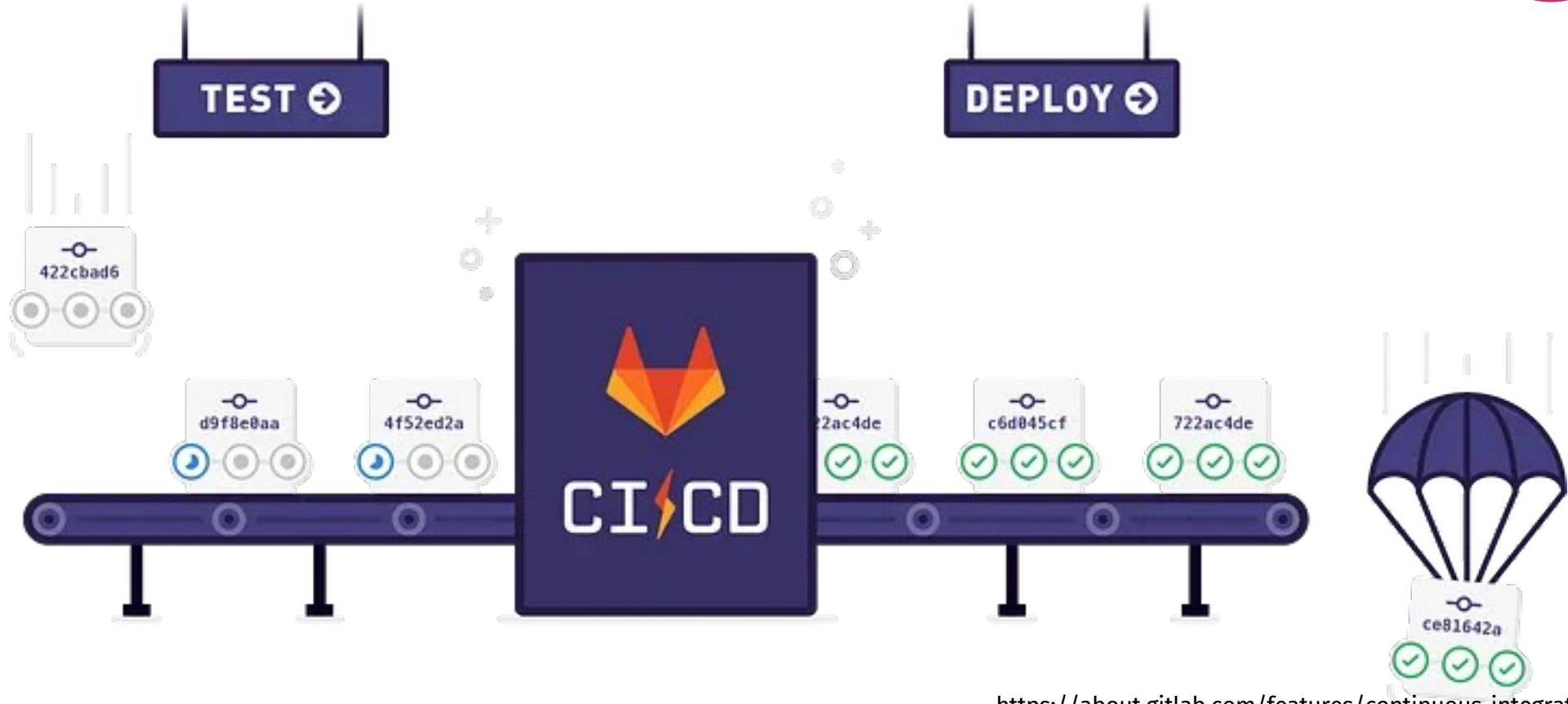
# What is CI/CD?



- Continuous Integration (CI) is the practice of frequently integrating code changes into a shared repository, which is then automatically built and tested.
- Continuous Delivery (CD) takes CI a step further by automatically deploying code changes to production-like environments for further testing and validation.
- Automated testing is a critical component of CI/CD, as it helps catch bugs and other issues early in the development process.
- Popular tools are GitLab CI/CD, Github Actions, Jenkins, CircleCI, and Travis CI.



# What is CI/CD?

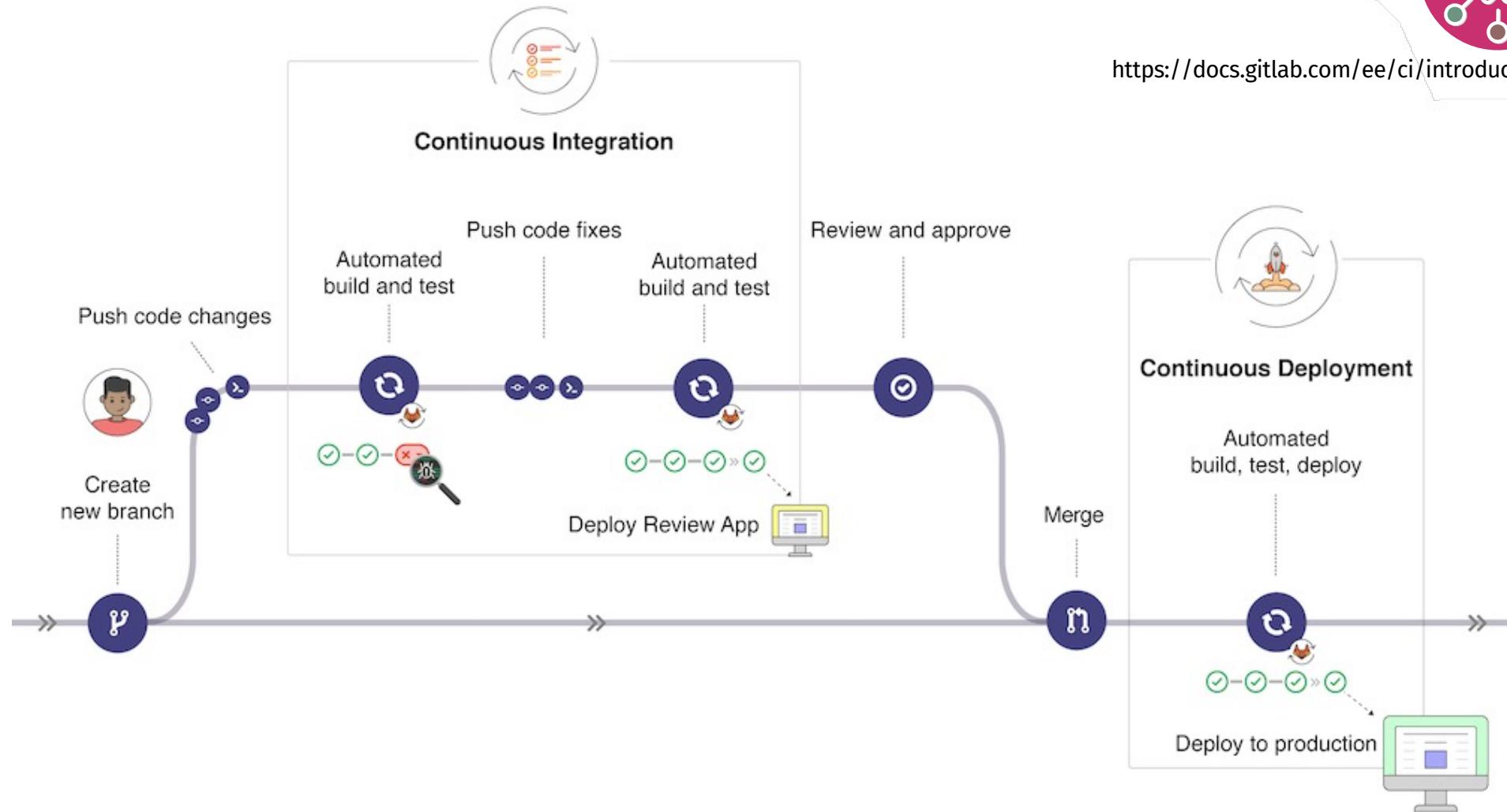


<https://about.gitlab.com/features/continuous-integration/>

# Gitlab Workflow



<https://docs.gitlab.com/ee/ci/introduction/>



# Task Estimation

## Estimation 4



Create a Star Wars  
Star Destroyer (4784pcs)

X Point(s)



0	$\frac{1}{2}$	1	2
3	5	8	13
20	40	100	$\infty$
?	☕		

# Task Estimation

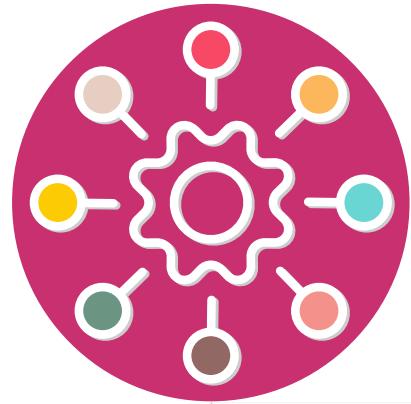
## Estimation 5

Create a Star Wars  
Star Destroyer (37pcs)

X Point(s)



0	$\frac{1}{2}$	1	2
3	5	8	13
20	40	100	$\infty$
?	☕		



SYSTEMS DESIGN / INTRODUCTION TO GIT - PART A

## Introduction to git - Part A

Installation & Setup



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SYSTEMS DESIGN / INTRODUCTION TO GIT - PART B

## Introduction to git - Part B



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