

NIOS-II Processor

This system was designed as coursework for my Sophomore year Embedded Systems 3 course. The Objectives of this course included designing peripherals in VHDL and creating C drivers for said peripherals. Note that not all of the peripherals were custom designed, as a decent portion of them were created by Intel/Altera for use with their University Program. We were also instructed to create a program written in C to demonstrate all of the peripherals implemented for our system.

Also included in this class was the creation of a document outlining the details of the system we had designed. In my case, this is the document named "System Overview." The system overview document also states which peripherals were custom-designed and which ones are Intel/Altera IP from the University Program.