

**Circuit Board Defect Detection Using Image Registration**

**B.E. Project Report**

**Submitted in partial fulfillment of the requirements of the Degree of**

**Bachelor of Engineering in Electronics and Telecommunication Engineering**

By

**Vihang Patil (Roll No.)**

**Dinkal Solanki (Roll No.)**

**Sahil Tikke (Roll No.)**

**Shashikumar Vishwakarma (Roll No.)**

**Supervisor:**

**Prof. Priya Hankare**



**Department of Electronics and Telecommunication Engineering**

**K.J Somaiya Institute of Engineering and Information Technology**

**Ayurvihar, Sion, Mumbai -400022**

**YEAR 2015**