```
/*
/*
                                 */
   ScanI.c: Mercury電 流 制 御 プログラム
/*
                                                    */
/*
                               /*
                                 */
・ JL-086 向 け 電 流 制 御 C 言 語 化 用 にバー ジョン取り直し
/*
   Rev. 0. 00 : 2012. 08. 06 Y. Tanaka

構造体、
等軸対応

                                     ロ ー カル変数 見直し
   Rev. 0. 01 : 2012. 08. 17 Y. Tanaka
                                     ローカル変数 見直し
   Rev. 0. 02 : 2012. 08. 20 Y. Tanaka
                                        コ ンパイラ 確認用
   Rev. 0. 03 : 2012. 11. 20 Y. Tanaka
/*
                        トリンシック関数に変更
/*
   <1> 2013.05.07 T. Yamada
   <2> 2013.05.07 T. Yamada
                   記述見直し
                   MpSQRT修 正
   <3> 2013.05.09 T. Yamada
   <4> 2013, 05, 13 T. Yamada
                    ア セ ン ブ ラ との 違い修正
//#include "Basedef.h"
#include "IxInst.h"
#include "MprgStruct.h"
#include "MpConstTbl.h"
                /* 定数テーブル読み込み
#if defined(WIN32)
#include "IlibSvc.h" /* VC版 の み で使用 */
#include "MprgLmtChkVCMacro.h" /* 加 減 算 リ ミ ッ ト 検 出用マクロ定義
#include "IlibSvc.h"
```

#endif //#define DEBUG OUTPT /* for debug Romsimの 実 行 簡 所確認用 Definitions ト バ ー ジョン設定 #define MSW VER 0x0001/* テストバージョン設定 #define TST VER 0x0000*/ /* Y什 様 バ ー ジョン設定 #define YSP_VER 0x0000軸 処 理有効 #define MULTI AXIS */ /* 多 #define USE CMOVE //<2> t-yamada /* 周 辺 レ ジ ス タ 定 義 (暫定処理?) */ #ifdef PREG DEF #include "eau. h" /* read reg */ int chess storage (PFREG: 0x6BD) FCCST; int chess storage (PFREG: 0x6D0) IuAD; int chess storage (PFREG: 0x6D1) IvAD; int chess storage (PFREG: 0x6D9) HSURO; int chess_storage(PFREG: 0x6DA) HSUR1; int chess storage (PFREG: 0x6DD) CTSTR; int chess storage (PFREG: 0x6DF) FLTSTAT; /* write reg */ int chess storage (PFREG: 0x6D0) OUTPT; int chess storage (PFREG: 0x6D1) WDT1L; int chess storage (PFREG: 0x6D2) BBSET; int chess storage (PFREG: 0x6D3) CRST; int chess storage (PFREG: 0x6D8) SDMECLR; int chess storage (PFREG: 0x6D9) ADSYNC; int chess_storage(PFREG: 0x6DB) PWMOS;

int chess storage (PFREG: 0x6DC) CRSET1;

```
int chess storage (PFREG: 0x6DD) CTSTW;
int chess storage (PFREG: 0x6DF) CRFRQ;
int chess storage (PFREG: 0x6F9) DIVSET;
int chess storage (PFREG: 0x6FA) PCVS0;
int chess storage (PFREG: 0x6FB) PCVS1;
int chess_storage(PFREG:0x6FC) PCVS2;
int chess storage (PFREG: 0x6E7) PwmT0;
int chess storage (PFREG: 0x6E8) PwmT1;
int chess storage (PFREG: 0x6E9) PwmT2;
#endif //#ifdef PREG DEF
/* read reg */
extern int chess_storage(PFREG:0x6D9) HSUR0;
                                          //<2> //軸
                                                      共 诵, tanaka21
extern int chess storage (PFREG: 0x6DA) HSUR1;
                                          //<2> //軸
                                                      共通, tanaka21
                                           //<2>
extern int chess storage (PFREG: 0x6D0) IuAD;
extern int chess storage (PFREG: 0x6D1) IvAD;
                                           //<2>
extern int chess storage (PFREG: 0x6DD) CTSTR;
extern int chess storage (PFREG: 0x7D0) IuAD 2; //<2>
extern int chess_storage(PFREG:0x7D1) IvAD_2; //<2>
/* write reg */
extern int chess_storage(PFREG:0x6D0) OUTPT;
                                          //<2> //軸
                                                      共 通, tanaka21
extern int chess_storage(PFREG:0x6D1) WDT1L;
                                           //<2> //軸
                                                     共通, tanaka21
extern int chess storage (PFREG: 0x6DD) CTSTW;
extern int chess_storage(PFREG:0x6E7) PwmT0;
                                          //<2>
extern int chess storage (PFREG: 0x6E8) PwmT1;
extern int chess storage (PFREG: 0x6E9) PwmT2;
extern int chess_storage(PFREG:0x7E7) PwmT0 2; //<2>
extern int chess_storage(PFREG:0x7E8) PwmT1_2;
extern int chess storage (PFREG: 0x7E9) PwmT2 2;
INITWK IniWk; /* for dubug */
ProtoType
                                  *****************
```

```
void MpDataClear (MICRO_AXIS_HANDLE *AxisRsc ); /* マ イ ク ロ 用 データクリア
void MpIntHost( void );
void MpIntAD( void ) property(isr);
void MpIntEnc( void );
inline USHORT MpSQRT(ULONG src); /* 2013.05.06 tanaka21 コ ー ド整理<020>
inline void MpOVMMODK (INTADP *IntAdP, INTADV *IntAdV, CSHORT* pCtbl); /* 2013.05.06 tanaka21 コ ー ド整理<020>
inline void ADConvDataLoad( MICRO AXIS HANDLE *AxisRsc ); //<2>
inline void SetPWM( MICRO AXIS HANDLE *AxisRsc ); //<2> /* <S015> */
#if defined(WIN32) /* VC用 ダ ミ ー レ ジスタ定義 */
SVIP READ REG SvIpReadReg;
SVIP_WRITE_REG SvIpWriteReg;
#endif
/*機能レジスタ/周辺レジスタ(0x5F0以降)を使用するために定義が必要-->コンパイラ変更により不要、
#define FREG DEF /* 機能 レ ジ ス タ定義有効 */
//#define PREG DEF /* 周 辺 レ ジ ス タ定義有効
/*機能レジスタ定義(暫定処理?)
#ifdef FREG DEF
int chess storage (ISAO) ISAO;
int chess_storage(ISA1) ISA1;
int chess_storage(IL) INTLVWR;
int chess_storage(EIX) EIX;
int chess storage (DIX) DIX;
#endif //#ifdef FREG DEF
初期化処理
/*
#ifdef ASIP CC
#ifndef IPD_SIM
             /* IPDesigner用 シ ミ ュ レ ー ションスイッチ */
void main(void) /* JL-086に 搭 載_す る プ ロ グ ラ ム を 作 成 する 場 合 はこちら で定義する
#else //#ifndef IPD SIM /* IPDesigner用 シ ミ ュ レ ー ションスイッチ */
void MpStart(void) /* コンパイラのみでシミュレーションを行なう場合はこちらで定義する*/
#endif //#ifndef IPD_SIM /* IPDesigner用 シ ミュ レー ションスイッチ
```

```
#elif defined(WIN32)
                                 /* VC用
                                                         */
void MpStart( void )
#endif
 USHORT
             ax noR;
 MICRO AXIS HANDLE *AxisRscR;
                 /* 2013.05.06 tanaka21 コ ー ド 整理<020>
 SHORT DivSetW;
               /* 2013.05.06 tanaka21 コ ー ド 整理<020>
 SHORT PoSet1W;
                /* 2013.05.06 tanaka21 コ ー ド整理<020>
 SHORT PoSet2W;
                 /* 2013.05.06 tanaka21 コ ー ド 整理〈020〉
 USHORT uswk;
     interupt set
     バージョン設定
                                                           */
 VerInfo.MswVer = MSW_VER;
                           /* ソ フ ト バ ー ジョン設定
                           /* テストバージョン設定
 VerInfo. TstVer = TST VER;
                          /* Y仕 様 バ ー ジョン設定
 VerInfo. YspVer = YSP VER;
    Get Axis Num from CPU
 AxisNum = AxisHdl[0]. AxisInfo. AxisNum;
     Set H/W Register Address Pointer
#ifdef MULTI_AXIS /* 多 軸 処 理有効
                                                                */
 for( ax_noR = 0; (SHORT) ax_noR < AxisNum; ax_noR++ )</pre>
#else //#ifdef MULTI AXIS
 ax noR = 0;
#endif //#ifdef MULTI AXIS
   AxisRscR = &(AxisHdl[ax_noR]);
   if(ax noR == 0)
```

```
AxisRscR->SvIpRegR = (SVIP READ REG*) (0x600);
     AxisRscR->SvIpRegW = (SVIP WRITE REG*)(0x600);
   else if (ax noR == 1)
     AxisRscR->SvIpRegR = (SVIP READ REG*)(0x700);
     AxisRscR->SvIpRegW = (SVIP WRITE REG*)(0x700);
     Set Interrupt Level
 /* level(AD=3, INT1=4, HOST=0) */
 /* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!O軸目っ て書くのが格好 悪い★
#ifdef FREG DEF
 INTLVWR = 0x0004;
#else //#ifdef FREG DEF
 AxisHd1[0]. SvIpRegW->INTLVWR = 0x0004;
#endif //#ifdef FREG_DEF
   Initialize variables
#ifdef MULTI_AXIS
                         /* 多 軸 処 理有効
                                                                   */
 for (ax noR = 0; (SHORT) ax noR < AxisNum; ax noR++)
#else //#ifdef MULTI_AXIS
 ax noR = 0;
#endif //#ifdef MULTI_AXIS
   AxisRscR = &AxisHdl[ax noR];
   AxisRscR->StsFlg.BbSetW = 0x2004; /* INT1=Encoder0, BB
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW; /* INT1=Encoder0, BB
   ISA0 = (int)MpIntAD;
```

```
ISA1 = (int)MpIntEnc;
   AxisRscR->SvIpRegW->PCVS0 = AxisRscR->EncIfV. DivPls. s[0]; /* パ ル ス 変 換位置
   PoSet1W = AxisRscR->DivPlsV. PoSet1In; /* MpUPDATE_DIVPOS()で 比 較 処 理 が あ るため残
   PoSet2W = AxisRscR->DivPlsV. PoSet2In; /* MpUPDATE_DIVPOS()で 比 較 処 理 が あ るため残
   AxisRscR->SvIpRegW->PCVS1 = PoSet1W; /* パ ル ス 変 換 原点補正1 (bit15-0)
   AxisRscR->SvIpRegW->PCVS2 = PoSet2W; /* パ ル ス 変 換 原点補正2 (bit15-0)
   DivSetW = AxisRscR->DivPlsV. DivSetIn; /* MpUPDATE DIVPOS()で 比 較 処 理 が あ る た め 残 しておく
   AxisRscR->SvIpRegW->DIVSET = DivSetW; /* 分 周 機 能設定
/* 2013.05.06 tanaka21 コ ー ド 整 理 (マクロ 化) <022>
   ZERO = 0x0000; //\langle 2 \rangle
   ONE = 0x0001; //\langle 2 \rangle
   /* 2012.12.21 Y.0ka 現 状 初 期 化必要 */
   AxisRscR->SinTb1. SinT = 0x0000; /* SinTb1. SinT= \sin(\theta) \sin(0) = 0.000 \rightarrow 0000h
   AxisRscR->SinTb1. CosT = 0x4000; /* SinTb1. CosT= cos(\theta) cos(0)= 1.000 \rightarrow 4000h
   AxisRscR->SinTb1. SinT2 = 0x376D; /* SinTb1. SinT2=sin(\theta +2 \pi/3) sin(2\pi/3) = 0.866 \rightarrow 376Dh
   AxisRscR->SinTb1. CosT2 = 0xE000; /* SinTb1. CosT2=cos(\theta +2 \pi/3) cos(2\pi/3) = -0.500 \rightarrow E000h
   AxisRscR->SinTb1. SinT3 = 0xC893; /* SinTb1. SinT3=sin(\theta -2 \pi/3) sin(-2\pi/3)=-0. 866 \rightarrow C893h
   AxisRscR->SinTb1. CosT3 = 0xE000; /* SinTb1. CosT3=cos(\theta -2 \pi/3) cos(-2\pi/3)=-0. 500 \rightarrow E000h
     PWM set
   AxisRscR->SvIpRegW->PWMOS = 0x0A0; /* 2level, triangle, servo (bit7: no-Saw mode for JL-056)
   AxisRscR->IntAdV. CrFreqW = AxisRscR->IntAdP. CrFreq; /* Carrier set(IntAdP. CrFreq must be set before starts)
   AxisRscR->SvIpRegW->CRSET1 = 0x10; /* CLA=Both (unavailable on JL-056)
   AxisRscR->SvIpRegW->CRFRQ = AxisRscR->IntAdV. CrFreqW; /* Carrier 10. 667kHz
   uswk = (AxisRscR->IntAdV. CrFreqW >> 1); /* TMPO <-- IntAdV. CrFreqW /2 (50p dutv)
   AxisRscR->PwmV. PwmCntT2 = uswk;
   AxisRscR->PwmV. PwmCntT1 = uswk;
   AxisRscR->PwmV. PwmCntT0 = uswk;
```

```
/* T2(W) = (dutv:50p)
//<2>
       AxisRscR->SvIpRegW->PwmT2 = uswk;
       AxisRscR->SvIpRegW->PwmT1 = uswk;
                                           /* T1(V) = (dutv:50p)
       AxisRscR->SvIpRegW->PwmT0 = uswk;
                                           /* T0(U) = (duty:50p)
     SetPWM(uswk, uswk, uswk);
     Clear Register
   MpDataClear( AxisRscR );
     input CPORT, DLIM = QLIM = 0, output CPORT
//<2>#ifdef PREG DEF
     AxisRscR->StsFlg.CtrlStsRW = CTSTR; /* StsFlg.CtrlStsRW <- Control register
   AxisRscR->StsFlg.CtrlStsRW = AxisRscR->CtrlStsIn; /* StsFlg.CtrlStsRW <- Control register *//* <Y.OkaO1> */
   AxisRscR->StsFlg.CtrlStsRW = (AxisRscR->StsFlg.CtrlStsRW & DLIMI); /* StsFlg.CtrlStsRW <-- StsFlg.CtrlStsRW & DLIMI
     CTSTW = AxisRscR->StsFlg.CtrlStsRW; /* Status Set
   AxisRscR->CtrlStsOut = AxisRscR->StsFlg.CtrlStsRW; /* Status Set
                                                                                 *//* <Y. 0ka01> */
     START: INTERRUPT, PWM
                                                               */
   EIX = 0x0;
                           /* Interuput start
     AxisRscR->SvIpRegW->CRST = 0x1;
                                                 /* Carrier(PWM) start
   AxisRscR->SvIpRegW->CRST = 0x3;
                                              /* Carrier(PWM) start JL-086の 設定
   AxisRscR->StsFlg. BbSetW = (AxisRscR->StsFlg. BbSetW & OxFFFB); /* Reset soft_BB
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW;
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW;
                                                                 <S015>
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW;
                                                                 <S015>
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW;
                                                           /*
                                                                 <S015>
   AxisRscR->SvIpRegW->BBSET = AxisRscR->StsFlg.BbSetW;
                                                                 <S015>
  /* Output PWM Data */
 SetPWM( &AxisHdl[0] );
```

```
ROUND Procedure
/*
#if !defined(WIN32)
              /* IPDesigner用 シ ミ ュ レ ー ションスイッチ
#ifndef IPD SIM
 while (1)
#endif //#ifndef IPD SIM /* IPDesigner用 シ ミ ュ レ ー ションスイッチ */
#endif
#ifdef MULTI AXIS /* 多 軸 処 理有効
  for( ax_noR = 0; (SHORT) ax_noR < AxisNum; ax_noR++ )</pre>
#else //#ifdef MULTI AXIS
   ax noR = 0;
#endif //#ifdef MULTI AXIS
    AxisRscR = &AxisHdl[ax_noR];
                                               */
   A/D error check and clear
    AxisRscR->StsFlg.FccStsMon = AxisRscR->SvIpRegR->FCCST;
    AxisRscR->StsFlg.FltStsW = AxisRscR->SvIpRegR->FLTSTAT & 0x7FFF;
   Host port check for host INT */現在、WREG100~WREG104までは未使用のため、削除。
  Host port check for host INT
   /* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!0軸目って書くのが格好悪い★ */
//<2>#ifdef PREG DEF
  if ( HSURO != 0x0 )
    MpIntHost();
```

```
Host port check for host INT2
   /*★H/Wアクセスが共通のものをまと
//<2>#ifdef PREG DEF
   if (HSUR1 \stackrel{!}{=} 0x0)
     DIX = 0x0;
               /* disable interupt <V112>
#ifdef MULTI AXIS
                            /* 多 軸 処 理有効
     for( ax_noR = 0; (SHORT) ax_noR < AxisNum; ax_noR++ )</pre>
#else //#ifdef MULTI AXIS
     ax noR = 0;
       //#ifdef MULTI AXIS
#endif
       AxisRscR = &AxisHdl[ax noR];
       AxisRscR->PhaseV. PhaseH = AxisRscR->AdinV. PhaseHIn;
       AxisRscR->PhaseV. PhaseIp = AxisRscR->PhaseV. PhaseIpIn;
       AxisRscR->PhaseV. PhaseIpF = AxisRscR->PhaseV. PhaseIpFIn;
                                                              /* 位
                                                                        ラグセット〈V112〉
       AxisRscR->PhaseV.PhaseIpFIn = 1;
       AxisRscR->WeakFV. WfKpV.s[0] = AxisRscR->WeakFV. WfKpVLIn;
                                                                              例 ゲ イン(下位16bit) <V214>
                                                              /* 雷
       AxisRscR->WeakFV. WfKpV. s[1] = AxisRscR->WeakFV. WfKpVHIn;
                                                                                    イン(上位16bit) <V214>
                                                              /* 電 EF B 積
       AxisRscR->WeakFV. WfKiV. s[0] = AxisRscR->WeakFV. WfKiVLIn;
                                                                                    イン(下位16bit) <V214>
                                                                    圧F B 積
       AxisRscR->WeakFV. WfKiV. s[1] = AxisRscR->WeakFV. WfKiVHIn;
                                                              /* 電
                                                              /* 電 圧 指 令制限値
       AxisRscR->WeakFV. WfV1Max = AxisRscR->WeakFV. WfV1MaxIn;
                                                                                           <V214>
                                                                                  会 リミット
       AxisRscR->WeakFV.WfIdRefLim = AxisRscR->WeakFV.WfIdRefLimIn; /* d軸 電 流 指
                                                                                                 <V214>
     EIX = 0x0;
                  /* enable interupt
                                      <V112>
 return;
                               *********************
/*
                                               */
```

```
*/
    HOST Interupt Procedure
void MpIntHost( void )
#ifdef WIN32
 DWREG 1mtBuf; /* 加 減 演 算 用 リ ミ ッ ト判断用バッファ */
UCHAR 1mtBufsign[2]; /* リ ミ ッ ト バ ッ ファ入力値符号 0:前 項 、1:後項 */
 UCHAR lmtBufSw; /* リ ミ ッ ト バ ッ フ ァ 入力値スイッチ 0:前 項 、 1:後項 */
#endif
 USHORT
            ax noH;
 USHORT
           ActiveAxis;
 INT64
           dlwk;
 MICRO AXIS_HANDLE *AxisRscH;
 SHORT swk0;
             /* 2013.05.06 tanaka21 コ ー ド 整理<020>
             /* 2013.05.06 tanaka21 コ ー ド 整理<020>
 SHORT swk1;
             /* 2013.05.06 tanaka21 コード整理〈020〉
 LONG lwk1;
 LONG 1wk2;
             /* 2013.05.06 tanaka21 コ ー ド 整理<020>
             /* 2013.05.06 tanaka21 コ ー ド 整理〈020〉
 LONG 1wk3;
 IniWk. IN_WKO++; /* for debug counter tanaka21 */
 /* ★ H/W ア ク セ ス が 共 通 の もの を
                                          ま と め た い !!0軸目っ て書く のが格好悪い★
 WDT1L = 0x1; /* Watch dog set
                                           */
// OUTPT = 0x1; /* 1.13
#ifdef MULTI AXIS /* 多 軸 処 理有効
 for (ax_noH = 0; (SHORT)ax_noH < AxisNum; ax_noH++)
#else //#ifdef MULTI AXIS
 ax noH = 0;
#endif //#ifdef MULTI AXIS
  AxisRscH = &AxisHdl[ax_noH];
```

```
AxisRscH->IntAdV. IqMon = AxisRscH->IntAdV. IqRef; /* for CPU monitor
     キャリア周波数切り替え処理
                                                                  < V057> < V075>
   if (AxisRscH->IntAdP. CrFreq != AxisRscH->IntAdV. CrFreqW)
     AxisRscH->IntAdV. CrFreqW = AxisRscH->IntAdP. CrFreq; /* Carrier Buffer Change
     AxisRscH->SvIpRegW->CRFRQ = AxisRscH->IntAdV. CrFreqW; /* Carrier Freq. Change
    input from host
 /* Check Current Ajust Request */
 ActiveAxis = 0;
                   /* 多 軸 処 理有効
#ifdef MULTI AXIS
 for (ax_noH = 0; (SHORT)ax_noH < AxisNum; ax_noH++)
#else //#ifdef MULTI AXIS
 ax noH = 0;
#endif //#ifdef MULTI_AXIS
   AxisRscH = &AxisHdl[ax_noH];
//<2>#ifdef PREG DEF
// if ( CTSTR & RLOCK ) == 0 )
   if ( ( AxisRscH->CtrlStsIn & RLOCK ) == 0 )
     ActiveAxis = 0x01 << ax_noH; /* ビ ッ ト 登録 */
 if( ActiveAxis != 0 )
  { /* 電 流 検 出 調 整要求あり */
   /* \star H/W ア ク セ ス が 共 通 の も の を ま と め た い !!0軸目っ て書くのが格好悪い\star DIX = 0x0; /* disable interupt. \langle V112 \rangle */
   DIX = 0x0; /* disable interupt \langle V112 \rangle
```

```
#ifdef MULTI AXIS
                            /* 多 軸 処 理有効
                                                                   */
   for (ax noH = 0; (SHORT) ax noH < AxisNum; ax noH++)
#else //#ifdef MULTI AXIS
   ax noH = 0;
#endif //#ifdef MULTI AXIS
     AxisRscH = &AxisHdl[ax noH];
     if (0 \mid = (ActiveAxis & (0x01 << ax noH)))
       AxisRscH->IntAdV. IuOffset = AxisRscH->AdinV. IuOffsetIn; /* IntAdV. IuOffset <-- AdinV. IuOffsetIn
       AxisRscH->IntAdV. IvOffset = AxisRscH->AdinV. IvOffsetIn; /* IntAdV. IvOffset <-- AdinV. IvOffsetIn
                                                 /* IntAdP.Kcu <-- AdinV.KcuIn
       AxisRscH->IntAdP. Kcu = AxisRscH->AdinV. KcuIn;
                                                  /* IntAdP.Kcv <-- AdinV.KcvIn
       AxisRscH->IntAdP. Kcv = AxisRscH->AdinV. KcvIn;
                                                                                                  */
   /* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!0軸目っ て書くのが格好悪い★
   EIX = 0x0; /* enable interupt
                                  <V112>
                                                      と め た い !!O軸目っ て書くのが格好悪い★
*/
 /* ★ H/W ア ク セ ス が 共 通 の もの
 DIX = 0x0; /* disable interupt \langle V112 \rangle
#ifdef MULTI AXIS
 for( ax_noH = 0; (SHORT) ax_noH < AxisNum; ax_noH++ )</pre>
#else //#ifdef MULTI AXIS
 ax noH = 0;
#endif //#ifdef MULTI AXIS
   AxisRscH = &AxisHdl[ax noH];
   AxisRscH->PhaseV. PhaseH = AxisRscH->AdinV. PhaseHIn;
   AxisRscH->PhaseV. PhaseIp = AxisRscH->PhaseV. PhaseIpIn;
                                                         /* 位 相 補 間 フラグ
   AxisRscH->PhaseV. PhaseIpF = AxisRscH->PhaseV. PhaseIpFIn;
   AxisRscH->PhaseV. PhaseIpFIn = 1; /* 位 相
                                                            間 フ ラグセット〈V112〉
```

```
AxisRscH->WeakFV. Vel = AxisRscH->AdinV. VelIn;
   AxisRscH->IntAdV. TLimP = AxisRscH->AdinV. TLimPIn;
   AxisRscH->IntAdV. TLimM = AxisRscH->AdinV. TLimMIn;
   AxisRscH->IntAdP. Kvv = AxisRscH->IntAdP. KvvIn;
                                                         /* for AVR
   AxisRscH->VcmpV. VdRef = AxisRscH->AdinV. VdRefIn;
   AxisRscH->VcmpV. VqRef = AxisRscH->AdinV. VqRefIn;
   AxisRscH->IntAdV. IqDist = AxisRscH->IntAdV. IqDistIn;
                                                           /* <V224>
   AxisRscH->WeakFV. WfKpV. s 0 = AxisRscH->WeakFV. WfKpVLIn;
                                                                                    イン(下位16bit) <V214>
   AxisRscH->WeakFV. WfKpV. s[1] = AxisRscH->WeakFV. WfKpVHIn;
                                                                   圧F B 比
                                                                             分ゲ
   AxisRscH->WeakFV. WfKiV. s[0] = AxisRscH->WeakFV. WfKiVLIn;
                                                             /* 電
                                                                   圧F B 積
   AxisRscH->WeakFV. WfKiV. s[1] = AxisRscH->WeakFV. WfKiVHIn;
                                                             /* 電
                                                                   EF B 積
                                                                                    イン(上位16bit) <V214>
                                                                                                                */
                                                                           令 制限值
                                                             /* 雷 圧
   AxisRscH->WeakFV. WfV1Max = AxisRscH->WeakFV. WfV1MaxIn;
                                                                                           <V214>
   AxisRscH->WeakFV. WfIdRefLim = AxisRscH->WeakFV. WfIdRefLimIn; /* d#
                                                                                                            */
 /* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!0軸目っ て書くのが格好 悪い★
 EIX = 0x0; /* enable interupt
                                 <V112>
     Carrier Freq Change check : if ( status & BB ) Carrier Freq. change
 /* Check Current Ajust Request */
 ActiveAxis = 0;
#ifdef MULTI AXIS
                              /* 多 軸 処 理有効
 for (ax noH = 0; (SHORT) ax noH < AxisNum; ax noH++)
#else //#ifdef MULTI AXIS
 ax noH = 0;
      //#ifdef MULTI AXIS
#endif
   AxisRscH = &AxisHdl[ax noH];
   if (AxisRscH->IntAdP.FccRst!= 0)
     ActiveAxis = 0x01 << ax noH; /* ビ ッ ト 登録 */
     IniWk. IN WKOH++; /* for debug counter tanaka21 */
```

```
if( ActiveAxis != 0 )
  { /* 電 流 検 出 調 整要求あり */
#ifdef MULTI AXIS
                            /* 多 軸 処 理有効
   for (ax noH = 0; (SHORT) ax noH < AxisNum; ax noH++)
#else //#ifdef MULTI AXIS
   ax noH = 0;
#endif //#ifdef MULTI AXIS
     AxisRscH = &AxisHdl[ax noH];
     if (0 != (ActiveAxis & (0x01 << ax noH)))
       /* 不 具 合No. 15は0 7 6 A の 不 具 合 の ため対策は省略可能〈00 2〉(tanaka21)*/
       AxisRscH->SvIpRegW->SDMECLR = (AxisRscH->SvIpRegR->FCCST 8);
       AxisRscH->AdStop. ADRst = AxisRscH->IntAdP. FccRst;
       AxisRscH->IntAdP. FccRst = 0;
     AxisRscH->SvIpRegW->ADSYNC = 1;
 /* Check BB Status */
 ActiveAxis = 0;
                             /* 多 軸 処 理有効
#ifdef MULTI AXIS
 for( ax_noH = 0; (SHORT) ax_noH < AxisNum; ax_noH++ )</pre>
#else //#ifdef MULTI AXIS
 ax noH = 0;
#endif //#ifdef MULTI AXIS
   AxisRscH = &AxisHdl[ax_noH];
//<2>#ifdef PREG DEF
// if ( CTSTR & BB )/* <Y. 0ka01> */
   if ( AxisRscH->CtrlStsIn & BB )
     ActiveAxis = 0x01 << ax_noH; /* ビ ッ ト 登録 */
```

```
if (ActiveAxis != 0)
  { /* BB状態の軸がある場合*/
   /* ★ H/W ア ク セ ス が 共 通 の もの を ま と め た い !!O軸目って書くのが格好悪い★
   DIX = 0x0; /* disable interupt \langle V112 \rangle
                             /* 多 軸 処 理有効
#ifdef MULTI AXIS
                                                                    */
   for (ax noH = 0; (SHORT) ax noH < AxisNum; ax noH++)
#else //#ifdef MULTI AXIS
   ax_noH = 0;
#endif //#ifdef MULTI AXIS
     AxisRscH = &AxisHdl[ax noH];
     data clear while BB
     if (0 \mid = (ActiveAxis & (0x01 << ax_noH)))
     { /* BB中 の 軸 の場合 */
       MpDataClear( AxisRscH );
       if (AxisRscH->IntAdP. CrFreq == AxisRscH->IntAdV. CrFreqW)
         AxisRscH->IntAdV. CrFreqW = AxisRscH->IntAdP. CrFreq; /* Carrier Buffer Change
         AxisRscH->SvIpRegW->CRFRQ = AxisRscH->IntAdV. CrFreqW; /* Carrier Freq. Change
   /* \bigstar H/W ア ク セ ス が 共 通 の も の を ま と め た い !!0軸目っ て書くのが格好悪い\bigstar EIX = 0x0; /* enable interupt \langle V112 \rangle */
   EIX = 0x0; /* enable interupt (V112)
                            /* 多 軸 処 理有効
#ifdef MULTI AXIS
 for (ax_noH = 0; (SHORT)ax_noH < AxisNum; ax_noH++)
#else //#ifdef MULTI_AXIS
```

```
ax noH = 0;
#endif //#ifdef MULTI AXIS
   if (0 == (ActiveAxis & (0x01 << ax noH)))
   { /* BB中 で は な い 軸の場合 */
    AxisRscH = &AxisHdl[ax noH];
notch filter 1st (before 2nd filter)
input : AdinV. IqIn (max:15000)
    output : IntAdV. IgOut1L (max:15000, limit:32768)
    parameter: IntAdP. Kf11, IntAdP. Kf12, IntAdP. Kf13, IntAdP. Kf14 (KFx= Kfx * 8192)
          : IntAdV. IaIn1PL, IntAdV. IaIn1PPL, IntAdV. IaOut1PL, IntAdV. IaOut1PPL
if(AxisRscH->IntAdP.CtrlSw & F1DSABL) /* Notch filter1 Disable
      AxisRscH->IntAdV. IqOut1L. s[0] = AxisRscH->AdinV. IqIn; /* フ ィ ル タ 処理なし
    else
    lwk1 = IntAdP.Kf12 * AdinV.IqIn + IntAdP.Kf11 * IntAdV.IqIn1PL + IntAdP.Kf14 * IntAdV.IqIn1PPL
                                                                                               */
      lwk1 = mul(AxisRscH->IntAdP. Kf12, AxisRscH->AdinV. IqIn);
      lwk1 = mac((LONG)AxisRscH->IntAdP.Kf11, AxisRscH->IntAdV.IqIn1PL.1, lwk1);
      lwk1 = mac limitf((LONG)AxisRscH->IntAdP.Kf14, AxisRscH->IntAdV.IqIn1PPL.1, lwk1); /* 符 号 付32b i t 制 限処理
    lwk1 = lwk1 - (IntAdP. Kf11 * IntAdV. IgOut1PL + IntAdP. Kf13 * IntAdV. IgOut1PPL)
      lwk2 = mulshr limitf((LONG)AxisRscH->IntAdP. Kf11, AxisRscH->IntAdV. IaOut1PL. 1, 13);
      lwk3 = mulshr limitf((LONG)AxisRscH->IntAdP. Kf13, AxisRscH->IntAdV. IqOut1PPL. 1, 13); /* AxisRscH->IntAdP. Kf13 *
      AxisRscH->IntAdV. IgOut1PPL. 1 */
      1wk1 = 1wk1 - 1wk2 - 1wk3;
```

```
IntAdV. IqIn1PPL = IntAdV. IqIn1PL, IntAdV. IqIn1PL = AdinV. IqIn, IntAdV. IqOut1PPL = IntAdV. IqOut1PL, IntAdV. IqOut1PL =
1wk1
       AxisRscH->IntAdV. IqIn1PPL. 1 = AxisRscH->IntAdV. IqIn1PL. 1; /* <V388> 追
       AxisRscH->IntAdV. IqIn1PL. 1 = (LONG) AxisRscH->AdinV. IqIn; /* <V388> 追 加
       AxisRscH->IntAdV. IqOut1PPL. 1 = AxisRscH->IntAdV. IqOut1PL. 1; /* <V388> 追 加
       AxisRscH->IntAdV. IqOut1PL. 1 = 1wk1; /* <V388> 追 加
       AxisRscH->IntAdV. IgOut1BufL. 1 = 1wk1; /*
                                                   <V502> 追 加
       AxisRscH->IntAdV. IqOut1L. s[0] = asr limitf(AxisRscH->IntAdV. IqOut1BufL. 1, 13); /*
                                                                                        <V502> 追 加
     notch filter
input : IntAdV. IgOut1L (max:15000)
     output : IntAdV. IqOut3L (max:15000, limit:32768)
     parameter: IntAdP. Kf31, IntAdP. Kf32, IntAdP. Kf33, IntAdP. Kf34 (KF3x = Kf3x * 8192)
             : IQI3P, IQI3PP, IQO3P, IQO3PP
if (AxisRscH->IntAdP.CtrlSw & F3DSABL)
       AxisRscH->IntAdV. IgOut3L. s[0] = AxisRscH->IntAdV. IgOut1L. s[0]; /* 7 / 1 / 9
     else
     HTMPO = IntAdP. Kf32 * IntAdV. IgOut1L + IntAdP. Kf31 * IQI3P + IntAdP. Kf34 * IQI3PP
       lwk1 = mul(AxisRscH->IntAdP. Kf32, AxisRscH->IntAdV. IqOut1L. s[0]);
       lwk1 = mac((LONG)AxisRscH->IntAdP. Kf31, AxisRscH->IntAdV. IqIn3PL. 1, lwk1);
       lwk1 = mac limitf((LONG)AxisRscH->IntAdP. Kf34, AxisRscH->IntAdV. IqIn3PPL. 1, lwk1);
     HTMP0 = HTMP0 - (IntAdP. Kf31 * IQO3P + IntAdP. Kf33 * IQO3PP)
```

```
lwk2 = mulshr limitf((LONG)AxisRscH->IntAdP. Kf31, AxisRscH->IntAdV. IqOut3PL. 1, 13);
      lwk3 = mulshr limitf((LONG)AxisRscH->IntAdP. Kf33, AxisRscH->IntAdV. IgOut3PPL. 1, 13);
      1wk1 = 1wk1 - 1wk2 - 1wk3;
     IQI3PP = IQI3P, IQI3P = IQO1, IQO3PP = IQO3P, IQO3P = HTMPO
                                                                       */
      AxisRscH->IntAdV. IqIn3PPL. 1 = AxisRscH->IntAdV. IqIn3PL. 1;
                                                             /* 前
      AxisRscH->IntAdV. IqIn3PL. 1 = (LONG) AxisRscH->IntAdV. IqOut1L. s[0]; /* 前
      AxisRscH->IntAdV. IqOut3PPL. 1 = AxisRscH->IntAdV. IqOut3PL. 1;
      AxisRscH->IntAdV. IqOut3PL. 1 = 1wk1;
                                           /* 前 回
      AxisRscH->IntAdV. IgOut3BufL. 1 = 1wk1;
      AxisRscH->IntAdV. IqOut3L. s[0] = asr limitf(lwk1, 13);
     Low Pass Filter
IntAdP. TLpf : Time-constant
    IntAdV. IgOut1Lpf: Output (32 bit) .. IQO1F: High 16 bit
    IntAdV. IgOut3 : INPUT
if (AxisRscH->IntAdP. CtrlSw & LPFDSABL)
      AxisRscH->IntAdV. IqOut1Lpf. s[1] = AxisRscH->IntAdV. IqOut3L. s[0]; /* 7
    else
      AxisRscH->IntAdV. IqOut3 = AxisRscH->IntAdV. IqOut3L. s「0」; /* フ ィ ル タ 処理なし
      swk1 = sub limitf(AxisRscH->IntAdV. IgOut3, AxisRscH->IntAdV. IgOut1Lpf. s[1]);
      lwk2 = mul(AxisRscH->IntAdP. TLpf, swk1) << 2;</pre>
      AxisRscH->IntAdV. IqOut1Lpf. 1 = add_limitf(lwk2, AxisRscH->IntAdV. IqOut1Lpf. 1); /* HTMPO <-- limit(HTMPO, 2^15 - 1)
```

```
*/
notch filter (before data input)
: IQO1F (max:15000)
            : IntAdV. IgOut2L (max:15000, limit:32768)
    parameter: IntAdP. Kf21, IntAdP. Kf22, IntAdP. Kf23, IntAdP. Kf24 (KF2x= Kf2x * 8192)
            : IQI2P, IQI2PP, IQ02P, IQ02PP
if (AxisRscH->IntAdP. CtrlSw & F2DSABL)
      AxisRscH->IntAdV. IgOut2L. s[0] = AxisRscH->IntAdV. IgOut1Lpf. s[1]; /* <V388> 追 加
    else
    HTMPO = IntAdP. Kf22 * IQO1F + IntAdP. Kf21 * IQI2P + IntAdP. Kf24 * IQI2PP
      lwk1 = mul(AxisRscH->IntAdP. Kf22, AxisRscH->IntAdV. IqOut1Lpf. s[1]);
      lwk1 = mac((LONG)AxisRscH->IntAdP.Kf21. AxisRscH->IntAdV.IgOut2PL.1. lwk1);
      lwk1 = mac limitf((LONG)AxisRscH->IntAdP. Kf24, AxisRscH->IntAdV. IqIn2PPL. 1, lwk1);
    HTMPO = HTMPO - (IntAdP. Kf21 * IQOP + IntAdP. Kf23 * IQOPH)
      1wk2 = mulshr_limitf((LONG)AxisRscH->IntAdP. Kf21, AxisRscH->IntAdV. IqOut2PL. 1, 13);
      lwk3 = mulshr limitf((LONG)AxisRscH->IntAdP. Kf23, AxisRscH->IntAdV. IgOut2PPL. 1, 13);
      1wk1 = 1wk1 - 1wk2 - 1wk3;
    1012PP = 1012P, 1012P = 1001F, 1002PP = 1002P, 1002P = HTMPO
      AxisRscH->IntAdV. IqIn2PPL. 1 = AxisRscH->IntAdV. IqIn2PL. 1;
                                                         /* 前 々
```

```
AxisRscH->IntAdV. IqIn2PL. 1 = (LONG) AxisRscH->IntAdV. IqOut1Lpf. s[1]; /* 前 回 値 保存
       AxisRscH->IntAdV. IqOut2PPL. 1 = AxisRscH->IntAdV. IqOut2PL. 1; /* 前 々 回 値保存
                                                                                                      */
       AxisRscH->IntAdV. IqOut2PL. 1 = 1wk1; /* 前 回 値 保存
       AxisRscH->IntAdV. IqOut2BufL. 1 = 1wk1;
                                             /* 整数 化前出力今回值保存
                                                                                             */
       AxisRscH->IntAdV. IgOut2L. s[0] = asr limitf(lwk1, 13);
     omega calculation
   swk0 = mulshr(AxisRscH->IntAdP.Ld, AxisRscH->WeakFV.Vel, 15);
   lwk1 = mul(swk0, AxisRscH->IntAdV.KEangle);
   swk0 = asr limitf(lwk1, 0);
   swk1 = mulshr(AxisRscH->IntAdP.Lq, AxisRscH->WeakFV.Vel, 15);
   lwk1 = mul(swk1. AxisRscH->IntAdV.KEangle);
   swk1 = asr_limitf( lwk1, 0 );
     data transmit(2)
 /* ★ H/W ア ク セ ス が 共 通 の もの
                                                      とめたい!!0軸目って書くのが格好悪い★
 DIX = 0x0; /* disable interupt \langle V112 \rangle
#ifdef MULTI AXIS
                                                                    */
 for (ax_noH = 0; (SHORT)ax_noH < AxisNum; ax_noH++)
#else //#ifdef MULTI AXIS
 ax noH = 0;
#endif //#ifdef MULTI AXIS
   AxisRscH = &AxisHdl[ax_noH];
```

```
AxisRscH->VcmpV. MagC = (SHORT) mulshr (AxisRscH->IntAdP. Mag, AxisRscH->WeakFV. Vel, 15); /* VcmpV. MagC <-- ACC >> 15
   AxisRscH->VcmpV. LdC = swk0; /* VcmpV. LdC
   AxisRscH->VcmpV. LqC = swk1; /* VcmpV. LqC
   AxisRscH->WeakFV. IqOut = AxisRscH->IntAdV. IqOut2L. s[0]; /* <V388> 追 加
    if ( (AxisRscH->IntAdP. CtrlSw & V FB) == 0 )
     AxisRscH->WeakFV. IdOut = AxisRscH->AdinV. IdIn; /* WeakFV. IdOut (reference)
/* 分 周 パ ルス は H/W化予定 */
     <\V720> */
     swk1 = EncIfV. BitIprm; /* DivWk0 <-- EncIfV. BitIprm
     if (AxisRscH->EncIfV.BitIprm & UPGDIVOUT)
       MpUPDATE_DIVPOS(); /* → 分 周 パ ル ス更新,etc
//<2>#ifdef PREG DEF
     AxisRscH->StsFlg.CtrlStsRW = CTSTR; /* StsFlg.CtrlStsRW <- Control register
   AxisRscH->StsFlg.CtrlStsRW = AxisRscH->CtrlStsIn; /* StsFlg.CtrlStsRW <- Control register *//* <Y.OkaO1> */
AxisRscH->StsFlg.CtrlStsRW = ( AxisRscH->StsFlg.CtrlStsRW & DLIMI ); /* StsFlg.CtrlStsRW <-- StsFlg.CtrlStsRW & DLIMI
                      *///110525tanaka21,このビット演算は必要なのか?
   AxisRscH->StsFlg.CtrlStsRW = (AxisRscH->StsFlg.CtrlStsRW & TLIMI); /* StsFlg.CtrlStsRW <-- StsFlg.CtrlStsRW & TLIMI
    (imm 16)
 /* ★ H/W ア ク セ ス が 共 通 の もの を ま と め た い !!O軸目っ て書くのが格好悪い★
 EIX = 0x0; /* enable interupt \langle V112 \rangle
 return;
```

```
/*
    AD Interupt Procedure
/*
/*
                                       割 込 (@INT ENC)追加 の た め 割込レベル(INTLVWR)マスク処理変更
                          に てエンコー ダ
void MpIntAD( void ) property(isr)
#ifdef WIN32
             /* 加 減 簿 第 用 リ ミ ッ ト判断用バッファ
 DWREG 1mtBuf;
 UCHAR lmtBufsign[2]; /* リミットバッファ入力値符号
                                              0:前 項 、1:後項 */
             /* リ ミ ッ ト バ ッ フ ァ 入力値スイッチ 0:前 項 、 1:後項 */
 UCHAR 1mtBufSw;
#endif
 USHORT
           ax_noI;
 INT64
         dlwk;
 MICRO_AXIS_HANDLE *AxisRscI;
 SHORT swk0;
                            レ ジスタ0 2013.05.06 tanaka21 コ ー
              /* 16bitワ ー ク
 SHORT swk1;
             /* 16bitワ ー ク
                           レ ジスタ1 2013.05.06 tanaka21 コ ー
                                                        ド 整理(021)
 SHORT swk2;
              /* 16bitワ ー
                            レ ジスタ2
                                    2013. 05. 06 tanaka21 = -
                                                        ド 整理〈021〉
 SHORT swk3;
              /* 16bitワ ー ク
                            レ ジスタ3
                                    2013.05.06 tanaka21 = -
                                                        ド 整理(021)
 SHORT swk4;
                        ク レ ジスタ4
                                    2013.05.06 tanaka21 = -
             /* 16bitワ ー
                                    2013.05.06 tanaka21 = -
 SHORT swk5;
              /* 16bitワ ー ク
                           レ ジスタ5
              /* 16bitワ ー ク
                                   2013.05.06 tanaka21 = -
                                                        ド 整理(021)
 SHORT swk6;
                            レ ジスタ6
 SHORT swk7;
              /* 16bitワ ー ク
                           レ ジスタ7
                                    2013. 05. 06 tanaka21 = -
             /* 16bitワ ー ク レ ジスタ8 2013.05.06 tanaka21 コ ー ド 整理<021>
 SHORT swk8;
                /* テー・ブールーポーイーン ター用ワークレジスタ 2013.05.06 tanaka21 コー
 CSHORT* pCtbl;
                                                                         ド 整理(021)
                                                                                    */
 LONG 1wk0;
             /* 32bitワ ー ク
                           レ ジスタ0 2013.05.06 tanaka21 コ ー
                                                        ド 整理(021)
 LONG lwk1;
                           レ ジスタ1 2013.05.06 tanaka21 コ ー
              /* 32bitワ ー ク
                                                                    */
 LONG 1wk2;
             /* 32bitワ ー ク レ ジスタ2 2013.05.06 tanaka21 コ ー ド 整理<021>
                                                                    */
 LONG lwk4;
              /* 32bitワ ー ク レ ジスタ4 2013.05.06 tanaka21 コ ー ド 整理<021>
```

```
/* 32bitワ ー ク レ ジスタ6 2013.05.06 tanaka21 コ ー ド 整理<021>
 LONG lwk6;
                    /* 32bitワ ー ク レ ジスタ 8 2013.05.06 tanaka21 コ ー ド 整理<021>
   LONG 1wk8;
                                                                                              *//* コ メ ン
 SHORT swk10; //\langle 2 \rangle
 SHORT swk11; //\langle 2 \rangle
 SHORT PwmCnt;
 IniWk. IN WK1++; /* for debug counter tanaka21 */
 /* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!O軸目っ て書くのが格好悪い★
 /* level(AD=0, INT1=0/4 HOST=0) */
 INTLVWR &= 0 \times 00F0;
//<2>#ifdef PREG DEF
 OUTPT = 0x1;
               /* Watch dog reset */
 WDT1L = 0x0;
/*
     A/D convert data loading
                                                              */
     IntAdV. IuInData = IntAdP. Kcu * ( IUS + IntAdV. IuOffset ) / 2<sup>8</sup>
     IntAdV. IvInData = IntAdP. Kcv * ( IVS + IntAdV. IvOffset ) / 2<sup>8</sup>
 ADConvDataLoad(&AxisHdl[0]);
  {/* Axis0 start */
 /* Execute Current Loop Main Operation */
   AxisRscI = &AxisHdl[0];
   swk10 = AxisRscI->PhaseV. PhaseH + AxisRscI->PhaseV. PhaseIp;
   AxisRscI->PhaseV. PhaseIpF = cmove((AxisRscI->PhaseV. PhaseIpF!=1), ONE, AxisRscI->PhaseV. PhaseIpF);
   AxisRscI->PhaseV. PhaseH = cmove((AxisRscI->PhaseV. PhaseIpF != 1), AxisRscI->PhaseV. PhaseH, swk10);
// PHASE UPDATE処 理 〈V112〉
```

```
theta calculation
   swk0 = AxisRscI->PhaseV. PhaseH;
                      /* TMP3 <-- PhaseV.PhaseH + 2<sup>5</sup> */
   swk0 = swk0 + 32;
   swk1 = PI23;
   swk2 = swk1 + swk0; /* TMP4 < -- PhaseV. PhaseH + 2PI/3 */
   swk3 = swk0 - swk1; /* TMP5 < -- PhaseV. PhaseH - 2PI/3 */
   table read and get iu, iv by Id, Iq reference
   swk1 = swk0 >> 6;
                       /* TMP1 <-- TMP3 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.SinT. swk1); /* SinTbl.SinT <-- stable「TMP1 ] *//* tanaka21,要 コ メ ン ト解除 */
   swk0 = swk0 + PI2;
                      /* TMP3 <-- TMP3 + PI/2 */
   swk1 = swk0 >> 6;
                       /* TMP1 <-- TMP3 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.CosT、swk1); /* SinTbl.CosT <-- stable「TMP1 ] *//* tanaka21.要 コ メ ン ト解除 */
   swk1 = swk3 >> 6;
                         /* TMP1 <-- TMP5 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.SinT3, swk1); /* SinTbl.SinT3 <-- stable「TMP1 ] *//* tanaka21.要 コ メ ン ト解除 */
   swk3 = swk3 + PI2;
                      /* TMP5 <-- TMP5 + PI/2 */
   swk1 = swk3 >> 6;
                          /* TMP1 <-- TMP5 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.CosT3, swk1); /* SinTbl.CosT3 <-- stable「TMP1 ] *//* tanaka21,要 コ メ ン ト解除 */
   swk1 = swk2 >> 6;
                          /* TMP1 <-- TMP4 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.SinT2, swk1); /* SinTbl.SinT2 <-- stable「TMP1 ] *//* tanaka21,要 コ メ ン ト解除 */
   swk2 = swk2 + PI2;
                       /* TMP4 <-- TMP4 + PI/2 */
   swk1 = swk2 >> 6;
                          /* TMP1 <-- TMP4 >> 6 */
   IxTblSin16(AxisRscI->SinTbl.CosT2, swk1); /* SinTbl.CosT2 <-- stable「TMP1]*//* tanaka21,要 コ メ ン ト解除 */
     dq-trans(UVW to DQ)
     ID = IntAdP. Kc * ( (SinTbl. CosT-SinTbl. CosT2)*IntAdV. IuInData/2^14 + (SinTbl. CosT3-SinTbl. CosT2)*IntAdV. IvInData/2^14 )
/2^{9}
     IQ = IntAdP. Kc * ( (SinTbl. SinT2-SinTbl. SinT)*IntAdV. IuInData/2^14 + (SinTbl. SinT2-SinTbl. SinT3)*IntAdV. IvInData/2^14 )
/2^{9}
```

```
/* TMP1 < --\cos(th) - \cos(th-2pi/3) */
   swk1 = AxisRscI->SinTbl. CosT - AxisRscI->SinTbl. CosT2;
   /* ACC <-- TMP1 * iu */
   swk2 = mulshr(swk1, AxisRscI->IntAdV. IuInData, 14);
   /* TMP1 < --\cos(th-2pi/3) - \cos(th+2pi/3) */
   swk1 = AxisRscI->SinTbl. CosT3 - AxisRscI->SinTbl. CosT2;
   /* ACC <-- TMP1 * iv */
   swk1 = mulshr(swk1, AxisRscI->IntAdV.IvInData, 14 );
   /* TMP2 <-- TMP2 + TMP1 */
   swk2 = swk1 + swk2;
   /* ACC <-- IntAdP. Kc * TMP2 */
   AxisRscI->IntAdV. IdInData = mulshr(AxisRscI->IntAdP. Kc. swk2, 9);
   swk1 = AxisRscI->SinTbl.SinT2 - AxisRscI->SinTbl.SinT;
                                                                              /* TMP1 <-- sin(th+2pi/3) - sin(th)
   swk2 = mulshr(swk1, AxisRscI->IntAdV. IuInData, 14); /* ACC <-- TMP1 * iu
   swk1 = AxisRscI->SinTb1.SinT2 - AxisRscI->SinTb1.SinT3;
                                                                               /* TMP1 <-- sin(th+2pi/3)-sin(th-2pi/3)
   swk1 = mulshr(swk1, AxisRscI->IntAdV. IvInData, 14); /* ACC <-- TMP1 * iv
   swk2 = swk1 + swk2; /* TMP2 <-- TMP2 + TMP1
   AxisRscI->IntAdV. IqInData = mulshr(AxisRscI->IntAdP. Kc, swk2, 9);
                                                                     /* ACC <-- IntAdP. Kc * TMP2
                                                                                                                    */
     Current Observer <V038>
   if (AxisRscI->IntAdP. Ctr1Sw & OBSSEL)
...
// ダ ン ピ ン グ ゲインの設定 <V076>
         AxisRscI->DobsV. DmpGain = 2;
// g軸 電 流 の 飽 和 チェック 〈V076〉
```

```
if (AxisRscI->IntAdV. IqInData >= 0)
    { /* 0以 上 のとき*/
     /* TMP3 = IntAdV. IgInData */
      swk2 = AxisRscI->IntAdV. IqInData;
                /* 負 の とき
    else
      swk2 = ~AxisRscI->IntAdV. IqInData; /* TMP3 = ~IntAdV. IqInData;
     *///110530tanaka21作 業 メモ、 - 1 掛 け る のとどっ ちが速い?
      swk2 = swk2 + 1; /* TMP3 = TMP3 + 1
    if ( swk2 \le 14250 )
      swk3 = ZERO; /* TMP4 = 0 ( OverFlowCheck = OK )
    else
      swk3 = ONE; /* TMP4 = 1 ( OverFlowCheck = NG )
  d軸 オ ブ ザーバ部
    swk0 = mulshr(AxisRscI->DobsP. TsPerL, AxisRscI->VcmpV. VdOut, 15); /* TMPO <-- ACC >> 15 ( TMPO = Ts/L * Vd out >>
    15) */
    swk2 = AxisRscI->IntAdV. IdInData; /* TMP3 <-- IntAdV. IdInData
                                                                    <V076>
    swk2 = 1imit(swk2, 15000);
    swk1 = swk2 - AxisRscI->DobsV. IdObsOut; /*
                                                         <V076>
    swk1 = mulshr(AxisRscI->DobsP. Gobs, swk1, 16); /* ACC <-- TMP2*DobsP. Gobs ( TMP2 = g * ( Id - Id obs ) ) */
    swk0 = swk1 + swk0; /* TMP0 < -- TMP0 + TMP2  ( TMP0 = (g*(Id-Id obs) >> 16) + (Ts/L*Vd out >> 15)) */
                                                                                             (TMP2 = Id obs)
    swk1 = mulshr(AxisRscI->DobsP. RLTs, AxisRscI->DobsV. IdObsOut, 12); /* TMP2 <-- DobsV. IgObsOut
    AxisRscI->DobsV. IdObsOut = add limitf(swk1, swk0); /* DobsV. IdObsOut <-- limit(DobsV. IdObsOut, 2^15-1)
  d軸 フィルタ部
```

```
// error obs
     swk0 = AxisRscI->IntAdV. IdInData - AxisRscI->DobsV. IdObsOut; /*
// low pass filter
     swk0 = sub limitf(swk0, AxisRscI->DobsV.LpfIld.s[1]);
     lwk2 = mul(AxisRscI->DobsP.Fil0bsGain, swk0) << 2; /*
                                                                                        */
     AxisRscI->DobsV.LpfIld.1 = add_limitf(lwk2, AxisRscI->DobsV.LpfIld.1);
// high pass filter
     swk0 = sub_limitf(AxisRscI->DobsV.LpfIld.s[1], AxisRscI->DobsV.HpfIld.s[1]);
     lwk2 = mul(AxisRscI->DobsP.Fil0bsGain, swk0) << 2; /*</pre>
                                                                                        */
     AxisRscI->DobsV. HpfIld. 1 = add limitf(lwk2, AxisRscI->DobsV. HpfIld. 1); /*
     AxisRscI->DobsV. IdObsFreq = AxisRscI->DobsV. LpfIld. s[1] - AxisRscI->DobsV. HpfIld. s[1]; /*
                                                                                                                    */
   IntAdV.\ IdInData = IntAdV.\ IdInData - DobsV.\ IdObsFreq
     AxisRscI->DobsV. IdObsFreq = AxisRscI->DobsV. IdObsFreq * 2; /* ACC <-- DobsV. IdObsFreq * DobsV. DmpGain
     AxisRscI->DobsV. IdObsFreq = cmove((swk3 != 0), ZERO, AxisRscI->DobsV. IdObsFreq);
     AxisRscI->IntAdV. IdInData = AxisRscI->IntAdV. IdInData - AxisRscI->DobsV. IdObsFreq; /*
                                                                                                                         */
   a軸 オ ブ ザ ーバ 部
     swk0 = mulshr(AxisRscI->DobsP. TsPerL, AxisRscI->VcmpV. VqOut, 15); /* ACC <-- TMPO*Ts/L (TMPO = Ts/L * Vq_out)
     swk2 = AxisRscI->IntAdV. IqInData;
                                                                        /* TMP3 <-- IntAdV. IqInData
                                                                                                          <V076>
     swk2 = limit(swk2, 15000);
                                                      /* TMP3 <-- Limit(15000) <V076>
     swk1 = swk2 - AxisRscI->DobsV. Ig0bsOut;
                                                                                  <V076>
                                                                                                */
                                                                  /*
     swk1 = mulshr(AxisRscI-DobsP.Gobs, swk1, 16); /* TMP2 <-- ACC >> 16 ( TMP2 = g * ( Iq - Iq_obs ) >> 16 ) */
     swk0 = swk1 + swk0;
                                              /* TMP0 < -- TMP0 + TMP2  ( TMP0 = (g*(Ia-Ia obs) >> 16) + (Ts/L*Va out>> 15))
     */
     swk1 = mulshr(AxisRscI->DobsP. RLTs, AxisRscI->DobsV. IqObsOut, 12); /* TMP2 <-- ACC >> 12 ( TMP2 = (1-R*Ts/L)*Iq obs
```

```
>> 12 ) */
      AxisRscI->DobsV. IqObsOut = add limitf(swk1, swk0);
                                                                                  /* DobsV. IgObsOut <-- limit ( DobsV. IgObsOut,
   a軸 フィルタ部
   error obs
      swk0 = AxisRscI->IntAdV.IqInData - AxisRscI->DobsV.IqObsOut; /*
   low pass filter
      swk0 = sub_limitf(swk0, AxisRscI->DobsV.LpfIlq.s[1]); /*
      1wk2 = mul(AxisRscI->DobsP.FilObsGain, swk0) << 2; /*
     AxisRscI->DobsV. LpfIlq. 1 = add_limitf(lwk2, AxisRscI->DobsV. LpfIlq. 1); /*
// high pass filter
     swk0 = sub_limitf(AxisRscI->DobsV.LpfIlq.s[1], AxisRscI->DobsV.HpfIlq.s[1]); /*
lwk2 = mul(AxisRscI->DobsP.FilObsGain, swk0) << 2; /*</pre>
      AxisRscI->DobsV. HpfIlq. 1 = add_limitf(lwk2, AxisRscI->DobsV. HpfIlq. 1); /*
      AxisRscI->DobsV. IqObsFreq = AxisRscI->DobsV. LpfIlq. s[1] - AxisRscI->DobsV. HpfIlq. s[1]; /*
   IntAdV. IqInData = IntAdV. IqInData - DobsV. IqObsFreq
      AxisRscI->DobsV. IqObsFreq = AxisRscI->DobsV. IqObsFreq * 2; /* ACC <-- DobsV. IqObsFreq * DobsV. DmpGain
      AxisRscI->DobsV. IqObsFreq = cmove((swk3 != 0), ZERO, AxisRscI->DobsV. IqObsFreq);
      AxisRscI->IntAdV. IqInData = AxisRscI->IntAdV. IqInData - AxisRscI->DobsV. IqObsFreq; /*
                                                                                                                         */
                                                                                       ----*///110526tanaka21,BBチ エ
   ク 処 理 、 処 理 順 をいろいろ変更。
                                                    *///if-else if-elseの 形 で 書 き 換 え 。 正 し く 動 作 するか要 硝
------*/
    Base Block Check
   if( AxisRscI->AdStop. ADRst != 0 )
```

```
AxisRscI->AdStop. ADRst = 0;
   swk6 = AxisRscI->IntAdV.CrFreqW >> 1;
   AxisRscI \rightarrow PwmV. PwmCntT2 = swk6;
   AxisRscI->PwmV. PwmCntT1 = swk6;
   AxisRscI->PwmV. PwmCntT0 = swk6;
  /* 2012.12.20 Y.0ka 誤 り 修正 */
  else if ( (AxisRscI->StsFlg.CtrlStsRW & BB) != 0 )
   swk6 = AxisRscI->IntAdV.CrFreqW >> 1;
   AxisRscI->PwmV. PwmCntT2 = swk6;
   AxisRscI \rightarrow PwmV. PwmCntT1 = swk6;
   AxisRscI->PwmV. PwmCntT0 = swk6;
  else
弱 め 界磁用 I d 指令計算処理
                             <V214>
/*
弱 め 界 磁 方式選択
   if( AxisRscI->IntAdP. Ctr1Sw & V FB )
   差分電圧作成
/*
   /*
   V_{\text{qmax}} = \sqrt{(V_{\text{max}}X^2 - V_{\text{d}}^2)}
```

```
lwk2 = AxisRscI->WeakFV. WfV1Max * AxisRscI->WeakFV. WfV1Max; /* IntAdP. Vmax^2
 lwk4 = AxisRscI->WeakFV.WfVdRef * AxisRscI->WeakFV.WfVdRef; /* Vd^2
                                                                                    ; 削 除 〈V309〉
                                                                                                      復 活<V531> */
 1wk2 = sub limitf(1wk2, 1wk4);
 lwk2 = limitz(lwk2, LPX_REG32_MAX); /* if (IntAdP. Vmax^2 - Vd^2) < 0, then (IntAdP. Vmax^2 - Vd^2) = 0 */
 swk0 = MpSQRT( 1wk2 ); /* \sqrt{\text{IntAdP. Vmax}^2 - \text{Vd}^2}
 if ( swk0 > 0x7FFF )
   swk0 = 0x7FFF; /*
 AxisRscI->WeakFV. WfVqMax = swk0; /* Vqmax = \( \int \text{(IntAdP.Vmax^2 - Vd^2)} \)
TMPO = V_{qmax} - V_{q}
 swk1 = AxisRscI->WeakFV. WfVqRef;
 if (swk1 < 0)
   swk1 = (SHORT) ZEROR - swk1; /* TMP1 = |Vq|
 swk0 = sub limitf(AxisRscI->WeakFV.WfVqMax, swk1);
比例項計算
 1wk1 = (LONG) swk0; /* TMP1, 0 = 符 号 拡張(TMP0)
                                                                      */
 swk2 = (SHORT) mulshr (lwk1, AxisRscI->WeakFV. WfKpV.1, 32);
 if (swk2 > (SHORT) 0 \times 0080)
   swk2 = LPX REG16 MAX; /* 正 の 最 大値
                                                                    */
 else if ( swk2 < (SHORT) 0xFF80 )
   swk2 = LPX_REG16_MIN; /* 負 の 最 大値
 else
   lwk2 = mulshr16( lwk1, AxisRscI->WeakFV.WfKpV.1);
   swk2 = mulshr(1wk2, (LONG)256, 16);
```

```
積 分 項計算
        1 \text{wk4} = 1 \text{wk1} * \text{AxisRscI} \rightarrow \text{WeakFV}. WfKiV. 1; /* \Delta \text{Vg} * \text{Kiv}
                                                                                               */
        lwk6 = mulshr( lwk1, AxisRscI->WeakFV. WfKiV. 1, 32 ); /* \Delta Vq * Kiv
        if ((SHORT) 1 \text{wk} 6 > 0 \text{x} 08)
           1wk4 = LPX REG32 MAX; /* 正 の 最 大値
                                                                                  */
        else if ( (USHORT) 1wk6 > 0xFFF8 )
           1wk4 = LPX REG32 MIN; /* 負 の 最 大値
        else
           1wk4 = 1wk4 >> 4; /*
           1wk4 = 1wk4 & 0x0ffffffff; /*
           1 \text{wk} 6 = 1 \text{wk} 6 << 28; /*
           1 \text{wk4} = 1 \text{wk6} \mid 1 \text{wk4}; /* \text{TMP5}, 4 = \Delta \text{Vg} * \text{Kiv} (* 2^16)
        AxisRscI->WeakFV.WfIntgl.1 = add_limitf(lwk4, AxisRscI->WeakFV.WfIntgl.1); /*
        1wk6 = (ULONG) AxisRscI->WeakFV. WfIntegLim << 16; /* TMP9, 8 = WeakFV. WfIntegLim * 2 16
        AxisRscI->WeakFV. WfIntgl. 1 = limit(AxisRscI->WeakFV. WfIntgl. 1, 1wk6); /* WFINTEGH = \( \Delta \) Vq * Kiv (* 2^16 / 2^16) */
       比 例 項 + 積分 項
        swk4 = add limitf(AxisRscI->WeakFV.WfIntgl.s[1], swk2);
        swk4 = limit(swk4, AxisRscI->WeakFV.WfIdRefLim); /* IdrefLimでリミット

      Idref > 0 な ら ば、Idref = 0,積分 = 0
      */

      Idref(d軸 電 流指 令 ) が 正 に な る こ と は 無 い。正になった 場合は0にする。

/*
/*
                                                                                                                                     */
        AxisRscI->WeakFV. IdOut = swk4;
        swk10 = AxisRscI->WeakFV. IdOut;
        AxisRscI->WeakFV. IdOut = cmove((swk10 > 0), ZERO, AxisRscI->WeakFV. IdOut);
        AxisRscI->WeakFV. WfIntgl. 1 = cmove((swk10 > 0), (LONG) ZEROR, AxisRscI->WeakFV. WfIntgl. 1);
```

```
/*
     ACRd(d軸 電 流 制 御)
     TMP1 = limit( WeakFV. IdOut - IntAdV. IdInData . 2<sup>15</sup> - 1)
     swk1 = sub limitf(AxisRscI->WeakFV.IdOut, AxisRscI->IntAdV.IdInData); /* TMP1 <-- limit( TMP1 ,
     TMP2 = limit(IntAdP. KdP * TMP1 / 2^9 . 2^15 - 1)
     swk2 = mulshr limitf(AxisRscI->IntAdP, KdP, swk1, 9); /* ACC <-- IntAdP, KdP * TMP1
     IdIntgl(32) = (IntAdP. KdI * TMP1) << 3 + IdIntgl(32)
     IDIH = limit( IDIH , IntAdP. VdLim )
     lwk4 = ((ULONG) AxisRscI->IntAdP. VdLim) << 16; /*
                                                                               */
     1 \text{wk} 6 = \text{mul}(A \times i \times R \times I - ) \text{IntAdP. KdI. swk} 1) << 3; /*
     AxisRscI->AcrV. IdIntgl. 1 = add_limitf(lwk6, AxisRscI->AcrV. IdIntgl. 1); /* AcrV. IdIntgl <-- limit( AcrV. IdIntgl , 2 31 -
     if (LPX_ABS (AxisRscI->AcrV. IdIntgl. 1) > LPX_ABS (1wk4) )
       AxisRscI->StsFlg.CtrlStsRW = AxisRscI->StsFlg.CtrlStsRW | DLIM; /*
       swk0 = AxisRscI->IntAdP.Ctr1Sw;
       AxisRscI->AcrV. IdIntgl. 1 = cmove(((AxisRscI->IntAdP. CtrlSw & ICLR) != 0), (LONG)ZEROR, AxisRscI->AcrV. IdIntgl. 1);
     VcmpV. VdOut = limit( TMP2 + IDIH +TMP3, 2<sup>15</sup> - 1)
     swk1 = add limitf(AxisRscI->AcrV.IdIntgl.s[1], swk2); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )
     filter: AcrV. VdFil = ( ( TMP1 - VDFH ) * IntAdP. Tfil ) << 2 ) + AcrV. VdFil
     swk1 = sub_limitf(swk1, AxisRscI->AcrV. VdFil.s[1]); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )
     lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
```

```
AxisRscI->AcrV. VdFil. 1 = add limitf(AxisRscI->AcrV. VdFil. 1, lwk0); /*
                                                                                             */
ACRq(q軸 電 流 制 御)
    Low Pass Filter
    IntAdP. TLpf2 : Time-constant
    IntAdV. IgOut2Lpf: Output (32 bit) .. IQOF: High 16 bit
    WeakFV. IgOut : Input
    IQOF(32) = ( ( WeakFV. IqOut - IQOF(16) ) * IntAdP. TLpf2 ) << 2 ) + IntAdV. IqOut2Lpf(32)
     if ( (AxisRscI->IntAdP. CtrlSw & LPFCDSABL) != 0 )
      AxisRscI->IntAdV. IqOut2Lpf. s[1] = AxisRscI->WeakFV. IqOut; /* disable LPF
     else
      swk0 = sub limitf(AxisRscI->WeakFV. IqOut, AxisRscI->IntAdV. IqOut2Lpf. s[1]); /* TMP0 <-- limit( TMP0, 2^15 - 1)
      1 \text{wk2} = \text{mul}(A \text{xisRscI} \rightarrow \text{IntAdP. TLpf2}, \text{swk0}) << 2;
      AxisRscI->IntAdV. IqOut2Lpf. 1 = add limitf(AxisRscI->IntAdV. IqOut2Lpf. 1, 1wk2);
    AxisRscI->IntAdV. IqMonFil = AxisRscI->IntAdV. IqOut2Lpf. s[1]; /* IntAdV. IqMonFil:フィルタ後のq軸電流(モニタ用) < V2242
    AxisRscI->IntAdV. IqOfRef = add limitf(AxisRscI->IntAdV. IqOut2Lpf. s[1], AxisRscI->IntAdV. IqDist); /* IntAdV. IqOfRef <--
    limit (IntAdV. IgOfRef , 2 15 - 1 ) < V224>
    Torque Limit:
                                                    制 御 で d 軸 電 流 指 令 が作られるので、q軸電流指令は以下の式で
                                      ミット 設 定 値 の いずれか小さい方でリミットする。
```

```
Ta*U ミット値 = \sqrt{(Imax^2-Id*^2)}
/*
     Id*に よるTorque Lim it値
    swk0 = AxisRscI->IntAdP.CtrlSw;
     swk1 = V FB \mid V FB2;
    swk0 = swk0 & swk1; /* TMP0の bit11, bit13 以 外 を マスクする
     if ( swk0 != V FB )
      lwk4 = mul(AxisRscI->WeakFV.IdOut, AxisRscI->WeakFV.IdOut); /* Idref^2
                                                                          ;削 除<V309 > 復活<V531> */
     else
      lwk4 = mul(AxisRscI->WeakFV.WfIdRefLim, AxisRscI->WeakFV.WfIdRefLim); /* IdrefLim^2
                                                                                    ; <V309>
     1wk2 = 1wk2 - 1wk4; /* Imax^2 - Id^2
    swk0 = MpSQRT(1wk2);
    swk1 = swk0; /* TMP0 = \sqrt{(Imax^2 - Id^2)}
     Torque Limit
     if (AxisRscI->IntAdV. IqOfRef >= 0)
      swk1 = limit(swk1, AxisRscI->IntAdV.TLimP); /* 正 側 ト ル ク リミット
      AxisRscI->IntAdV. IqRef = limit(AxisRscI->IntAdV. IqOfRef, swk1); /* 〈V224〉外 乱 ト ル ク 加 算 後 のq軸電流指令
      swk10 = AxisRscI->StsFlg.CtrlStsRW | TLIM; /* TLIM flag set
      AxisRscI->StsFlg. CtrlStsRW = cmove((AxisRscI->IntAdV. IqRef == swk1), swk10, AxisRscI->StsFlg. CtrlStsRW);
     else
      swk1 = limit( swk1, AxisRscI->IntAdV.TLimM ); /* 負 側 ト ル ク リミット
      AxisRscI->IntAdV. IqRef = limit(AxisRscI->IntAdV. IqOfRef, swk1); /* <V224> 外 乱 ト ル ク 加 算 後 のa軸電流指令
      swk10 = AxisRscI->IntAdV. IgRef + swk1;
      swk11 = AxisRscI->StsFlg.CtrlStsRW | TLIM; /* TLIM flag set
      AxisRscI->StsFlg. CtrlStsRW = cmove((swk10 == 0), swk11, AxisRscI->StsFlg. CtrlStsRW); /* TLIM flag set
```

```
*/
TMP1 = limit( IntAdV. IgRef - IntAdV. IgInData , 2<sup>15</sup> - 1)
swk1 = sub_limitf(AxisRscI->IntAdV. IqRef, AxisRscI->IntAdV. IqInData); /* TMP1 <-- limit( TMP1 . 2^15 - 1 )
                                                                                                                         */
TMP2 = limit(IntAdP. KaP * TMP1 / 2^9 , 2^15 - 1)
swk2 = mulshr limitf(AxisRscI->IntAdP. KaP, swk1, 9); /* TMP2 <-- limit( TMP2, 2<sup>15</sup> - 1)
AcrV. IgIntgl(32) = (IntAdP. KgI * TMP1) << 3 + AcrV. IgIntgl(32)
IQIH = limit( IQIH , IntAdP. VgLim )
                                                                      */
if( ( (AxisRscI->IntAdP.CtrlSw & INT_ST) == 0) || ( (AxisRscI->StsFlg.IntglFlg & 1) == 0 ) )
  lwk6 = mul(AxisRscI->IntAdP.KqI, swk1); /* ACC <-- IntAdP.KqI * TMP1
  lwk4 = (ULONG) AxisRscI->IntAdP. VaLim; /*
  1wk4 = 1wk4 << 16; /*
                                                     */
  1 \text{wk} 6 = 1 \text{wk} 6 << 3; /*
  AxisRscI->AcrV. IqIntgl. 1 = add_limitf(lwk6, AxisRscI->AcrV. IqIntgl. 1); /* AcrV. IqIntgl <-- limit( AcrV. IqIntgl , 2^32
  if (LPX ABS (AxisRscI->AcrV. IqIntgl. 1) > LPX ABS (1wk4) )
    AxisRscI->StsFlg. CtrlStsRW = AxisRscI->StsFlg. CtrlStsRW | QLIM; /* IMM3 <-- STAT | QLIM (imm 16)
                                                                                                                           */
    swk10 = AxisRscI->IntAdP. CtrlSw & ICLR;
    AxisRscI->AcrV. IqIntgl. 1 = cmove((swk10 != 0), (LONG)ZEROR, AxisRscI->AcrV. IqIntgl. 1);
VcmpV. VgOut = limit( TMP2 + IQIH +TMP3 , 2<sup>15</sup> - 1)
swk1 = add limitf(AxisRscI->AcrV. IqIntgl. s[1], swk2); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )
filter: AcrV. VaFil = ( ( TMP1 - V0FH ) * IntAdP. Tfil ) << 2 ) + AcrV. VaFil
```

```
swk1 = sub limitf(swk1, AxisRscI->AcrV. VaFil.s[1]); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )
     1 \text{wk0} = \text{mul}(A \text{xisRscI} \rightarrow \text{IntAdP. Tfil. swk1}) << 2; /*
     AxisRscI->AcrV. VaFil. 1 = add limitf(AxisRscI->AcrV. VaFil. 1, lwk0);
Voltage Compensation(電 圧 補償)
/*
     if ( (AxisRscI->IntAdP. Ctr1Sw & ISEL) != 0 )
       swk1 = AxisRscI->WeakFV. IdOut; /* TMP1 <-- reference ID
                                                                                */
       swk2 = AxisRscI->IntAdV. IqRef; /*
     else
       swk1 = AxisRscI->IntAdV. IdInData; /* TMP1 <-- feedback ID
       swk2 = AxisRscI->IntAdV.IqInData; /* TMP2 <-- feedback IQ
     TMP4(VcmpV.VdComp) = IntAdP.MotResist*TMP1/2^15 - VcmpV.LaC * TMP2 / 2^15
     swk4 = mulshr(AxisRscI->VcmpV. LqC, swk2, 15); /* VcmpV. VdComp <-- ACC >> 15
     swk0 = mulshr(AxisRscI->IntAdP. MotResist, swk1, 15);
     swk4 = swk0 - swk4;
     TMP5 (VcmpV. VaComp) = VcmpV. LdC * TMP1 / 2^15 + VcmpV. MagC + IntAdP. MotResist*TMP2/2^15
     swk3 = mulshr(AxisRscI->VcmpV.LdC, swk1, 15); /* TMP3 <-- ACC >> 15
     swk0 = mulshr(AxisRscI->IntAdP. MotResist, swk2, 15);
     swk3 = swk3 + AxisRscI->VcmpV. MagC;
     swk5 = swk3 + swk0; /* VcmpV. VqComp <-- VcmpV. MagC + TMP3 + TMP0
     if (IntAdP. CtrlSw & DIDTSET) VcmpV. VdComp = TMP4 + KDD * (IntAdV. IdDataP - IntAdV. IdInData),
IntAdV. IdDataP=IntAdV. IdInData
               VcmpV. VqComp = TMP5 + KQD * (IntAdV. IqDataP - IntAdV. IqRef), IntAdV. IqDataP=IntAdV. IqRef
                                                                                                                     */
```

```
if ( (AxisRscI->IntAdP. CtrlSw & DIDTSEL) == 0 )
        AxisRscI->VcmpV. VdComp = swk4; /*
       AxisRscI->VcmpV. VqComp = swk5; /*
                                                                           */
     filter: I*FL = ( ( ( TMP1 - I*FH ) * IntAdP. Tfil ) << 2 ) + I*FL
      else
        swk1 = AxisRscI->WeakFV. IdOut; /*
       swk1 = sub_limitf(swk1, AxisRscI->IntAdV. IdLfil. s[1]); /*
                                                                                                */
       lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
        AxisRscI->IntAdV. IdLfil. 1 = add limitf(AxisRscI->IntAdV. IdLfil. 1, lwk0); /*
                                                                         */
        swk1 = AxisRscI->IntAdV. IqRef; /*
       swk1 = sub_limitf(swk1, AxisRscI->IntAdV. IqLfil. s[1]); /*
       lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
       AxisRscI->IntAdV. IqLfil. 1 = add limitf(AxisRscI->IntAdV. IqLfil. 1, lwk0); /*
                                                                                                                     */
        swk2 = AxisRscI->IntAdV. IdLfil.s[1] - AxisRscI->IntAdV. IdDataP; /*
        AxisRscI->IntAdV. IdDataP = AxisRscI->IntAdV. IdLfil.s[1]; /*
        swk2 = mulshr limitf(AxisRscI->IntAdP.L dIdt, swk2, 9); /* limit( VDL , 2 15 - 1 )
       AxisRscI->VcmpV. VdComp = add limitf(swk2, swk4); /* VcmpV. VdComp <-- limit( VcmpV. VdOut, 2 15 - 1)
       swk2 = AxisRscI->IntAdV. IqLfil. s[1] - AxisRscI->IntAdV. IqDataP; /*
        AxisRscI->IntAdV. IqDataP = AxisRscI->IntAdV. IqLfil. s[1];
       swk2 = mulshr limitf(AxisRscI->IntAdP.L dIdt, swk2, 9); /* limit( VQL , 2^15 - 1 )
       AxisRscI->VempV, VqComp = add limitf(swk2, swk5); /* VempV, VqComp <-- limit( VempV, VqOut, 2 15 - 1)
     TMP1 = limit ( VDFH + VcmpV. VdComp , 2<sup>15</sup> - 1 )
/*
     TMP2 = limit( VQFH + VcmpV. VqComp , 2<sup>15</sup> - 1)
     swk1 = add_limitf(AxisRscI->AcrV. VdFil.s[1], AxisRscI->VcmpV. VdComp); /* VcmpV. VdOut <-- limit( VcmpV. VdOut , 2^15 - 1)
     */
```

```
swk2 = add limitf(AxisRscI->AcrV, VqFil.s[1], AxisRscI->VcmpV, VqComp); /* VcmpV, VqOut <-- limit( VcmpV, VqOut , 2^15 - 1)
    TMP1 = limit( VcmpV. VdRef + TMP1 , 2<sup>15</sup> - 1)
/*
    TMP2 = limit(VcmpV, VaRef + TMP2, 2^15 - 1)
    swk1 = add limitf(AxisRscI->VcmpV, VdRef, swk1); /* VcmpV, VdOut <-- limit( VcmpV, VdOut, 2<sup>15</sup> - 1)
    swk2 = add limitf(AxisRscI->VcmpV, VaRef, swk2); /* VcmpV, VaOut <-- limit( VcmpV, VaOut, 2^15 - 1 )
    VcmpV. VdOut = limit( IntAdP. Kvv * TMP1 / 2<sup>13</sup>, 2<sup>15</sup> - 1)
    VcmpV. VaOut = limit(IntAdP. Kvv * TMP2 / 2<sup>13</sup>, 2<sup>15</sup> - 1)
                                                                */
    AxisRscI->VcmpV. VdOut = mulshr limitf(AxisRscI->IntAdP. Kvv, swkl, 13); /* VcmpV. VdOut <-- limit( TMP1 , 2 15 - 1 )
    AxisRscI->VcmpV. VqOut = mulshr limitf(AxisRscI->IntAdP. Kvv, swk2, 13); /* VcmpV. VqOut <-- limit( TMP2 , 2 15 - 1 )
    AxisRscI->WeakFV. WfVdRef = AxisRscI->VcmpV. VdOut;
                                                               <V531>
    AxisRscI->WeakFV. WfVqRef = AxisRscI->VcmpV. VqOut;
                                        /* a輔
                                                               <V531>
                                                                             */
電圧ベクトル補正値計算
                                ⟨V537⟩ 新 弱 め 界 磁 制 御 以 外 は こ の処理をジャンプする
                                                                                         */
if ( (AxisRscI->IntAdP. Ctr1Sw & V FB2) != 0 )
〈V531〉変 調 率 計 算を移動
    Get modulation
lwk2 = mul(AxisRscI->VcmpV. VdOut, AxisRscI->VcmpV. VdOut);
     lwk2 = mac(AxisRscI->VcmpV. VqOut, AxisRscI->VcmpV. VqOut, lwk2);
                           /* TMP0 = \sqrt{\text{(VcmpV. VdOut}^2 + VcmpV. VgOut}^2)}
                                                                        */
     swk0 = MpSQRT(1wk2);
     AxisRscI \rightarrow IntAdV. V1 = swk0; /* IntAdV. V1 = TMP0
〈V531〉IntAdV. V1 〉 8192*127%(10403. 8) -> 飽 和 状態
AxisRscI - VcmpV. Vmax2 = 10403; /* VcmpV. Vmax2 = 8192 * 1.27
```

```
AxisRscI->VcmpV, V12 = AxisRscI->IntAdV, V1; /* VcmpV, V12 = \int (VcmpV, VdOut^2 + VcmpV, VgOut^2)
                                                                                                                    */
       swk10 = AxisRscI->VcmpV. Vmax2 >> 1; /* VcmpV. Vmax2 = 8192 * 1.27 / 2
       swk11 = AxisRscI \rightarrow IntAdV. V1 >> 1; /* VcmpV. V12 = \sqrt{(VcmpV. VdOut^2 + VcmpV. VgOut^2)} / 2
       AxisRscI->VcmpV. Vmax2 = cmove((AxisRscI->IntAdV. V1 < 0), swk10, AxisRscI->VcmpV. Vmax2);
       AxisRscI->VcmpV. V12 = cmove((AxisRscI->IntAdV. V1 < 0), swk11, AxisRscI->VcmpV. V12);
       if (AxisRscI->VcmpV. Vmax2 < AxisRscI->VcmpV. V12)
         AxisRscI->IntAdV. V1 = 10403; /* IntAdV. V1 = IntAdP. Vmax( 8192 * 1.27 )
         AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg | 1; /* 積 分 停 止 フ ラグセット
電 圧 ベ ク ト ル 補 正値計算
                                         <V531> VcmpV, VdOut', VcmpV, VgOut' = IntAdP, Vmax / IntAdV, V1 * VcmpV, VdOut, VcmpV, VgOut
<V537> 削 除
テ ー ブ ルアドレ ス取得
         lwk2 = mul(AxisRscI->VcmpV. V12, AxisRscI->VcmpV. V12); /* TMP3, 2 = VcmpV. V12^2
         1 \text{wk2} = 1 \text{wk2} - 0 \times 00400000; /* TMP3, 2 = IntAdV. V1^2 - 2^22
         1 \text{wk2} = 1 \text{wk2} >> 4; /* TMP3, 2 = (VcmpV, V12^2 - 2^22) / 2^4
         swk0 = (USHORT) (1wk2 >> 16); /* TMP0 = (VcmpV, V12^2 - 2^22) / 2^4 / 2^16 = addr
         1 \text{wk2} = 1 \text{wk2} & 0 \times 00000 \text{ffff}; /* \text{TMP2} = \{ (\text{VcmpV}, \text{V12}^2 - 2^2 \text{2}) / 2^4 \} & 0 \times 00000 \text{ffff} \}
        圧制限ベクトル直線補間用データ取得
         1 \text{wk4} = 65536; /* TMP5, TMP4 = 65536
         1 \text{wk6} = 1 \text{wk4} - 1 \text{wk2}; /* TMP7, 6 = 10000h - Table Index (Lo) -> (addr*2^16-low) */
         IxTblVlmt16(swk8, swk0); /* TMP8: テーブルデータ 読み出し(読み出し アドレスad dr) *//* tanaka21,コン
         1 \text{wk6} = (\text{ULONG}) \text{swk8} * 1 \text{wk6}; /* \text{TMP6} = \text{tb1rv}(\text{addr}) * (2^16-10\text{w})
         swk0 = swk0 + 1; /* TMP0 = addr+1
         IxTblVlmt16(swk8, swk0); /* TMP8: テーブルデータ読み出し(読み出しアドレスad dr+1) *//*
         tanaka21, コ ン パ イ ラ 対応待ち
         1 \text{wk4} = (\text{ULONG}) \text{swk8} * 1 \text{wk2}; /* TMP4 = tb1rv(addr+1)*1ow
         1 \text{wk} 0 = 1 \text{wk} 6 + 1 \text{wk} 4; /* TMP0 = tblrv(addr)*(2^16-low) + tblrv(addr+1)*low */
        圧電圧ベクトル補正値計算
```

```
swk8 = AxisRscI->VcmpV. Vmax2; /* TMP8 = VcmpV. Vmax2
    1 \text{wk2} = \text{mulshr} (\text{ULONG}) \text{swk8}, 1 \text{wk0}, 28); /* TMP2 = MAC / 2^28
    AxisRscI->VcmpV. VdOut = mulshr(swk2, AxisRscI->VcmpV. VdOut, 14);
                                                                            /* VcmpV. VdOut = IntAdP. Vmax / VcmpV. V12 *
    VcmpV. VdOut * 2 (13+13+16) / 2 (28+14)
    AxisRscI->VcmpV. VqOut = mulshr(swk2, AxisRscI->VcmpV. VqOut, 14);
                                                                            /* VcmpV. VqOut = IntAdP. Vmax / VcmpV. V12 *
    VcmpV. VgOut * 2 (13+13+16) / 2 (28+14)
  else
    AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg & OxFFFE; /* 積 分 停 止 フ ラグクリア
UVW transform : dq(2phase) to UVW(3phase) Transform
VcmpV.VuOut = limit( SinTbl.CosT * VcmpV.VdOut / 2^14 - SinTbl.SinT * VcmpV.VqOut / 2^14 , 2^15 - 1 )
swk4 = AxisRscI->IntAdP.Vmax; /*
swk1 = mulshr(AxisRscI->SinTbl.CosT, AxisRscI->VcmpV.VdOut, 14); /* TMP1 <-- ACC >> 14
swk2 = mulshr(AxisRscI->SinTbl.SinT, AxisRscI->VcmpV.VqOut, 14); /* TMP2 <-- ACC >> 14
AxisRscI->VcmpV. VuOut = sub limitf(swk1, swk2); /* VcmpV. VuOut <-- limit( VcmpV. VuOut , 2 15 - 1)
AxisRscI->VcmpV. VuOut = IxLTMIT( AxisRscI->VcmpV. VuOut, swk4 ); /*
VcmpV. VvOut = limit( SinTbl. CosT3 * VcmpV. VdOut / 2^14 - SinTbl. SinT3 * VcmpV. VqOut / 2^14 , 2^15 - 1 )
                                                                                                                         */
swk1 = mulshr(AxisRscI->SinTbl.CosT3, AxisRscI->VcmpV.VdOut, 14); /* TMP1 <-- ACC >> 14
swk2 = mulshr(AxisRscI->SinTbl.SinT3, AxisRscI->VcmpV.VaOut, 14); /* TMP2 <-- ACC >> 14
AxisRscI->VcmpV. VvOut = sub_limitf(swk1, swk2); /* VcmpV. VvOut <-- limit( VcmpV. VvOut , 2 15 - 1)
AxisRscI->VcmpV. VvOut = IxLIMIT( AxisRscI->VcmpV. VvOut, swk4 ); /*
VcmpV. VwOut = limit( - VcmpV. VuOut - VcmpV. VvOut , 2<sup>15</sup> - 1)
swk1 = (SHORT) ZEROR - AxisRscI->VcmpV. VuOut; /* VcmpV. VwOut <-- - VcmpV. VuOut - VcmpV. VvOut
AxisRscI->VcmpV. VwOut = sub limitf(swk1, AxisRscI->VcmpV. VvOut); /* VcmpV. VwOut <-- limit( VcmpV. VwOut , 2 15 - 1)
```

```
*/
   AxisRscI->VcmpV. VwOut = IxLIMIT( AxisRscI->VcmpV. VwOut, swk4 ); /*
#if 0 /* for debug 2013.07.17 tanaka21*/
新 弱 め 界 磁 制 御 判断処理
                         〈V537〉新 弱 め 界 磁 の 場 合
                                                             飽和判断処理を ジャンプする */
if ( (AxisRscI->IntAdP. Ctr1Sw & V FB2) == 0 )
Get modulation
                 <V531> 変
                         率 計算は2相3相変換前に
lwk2 = mul (AxisRscI->VcmpV. VdOut, AxisRscI->VcmpV. VdOut);
    lwk2 = mac(AxisRscI->VcmpV. VqOut, AxisRscI->VcmpV. VqOut, lwk2);
    swk0 = MpSQRT(1wk2);
    if ( (USHORT) swk0 > 0x7FFF )
     swk0 = 0x7FFF; /* \sqrt{} の 計 算が3 2 7 6 7 を 超えた ら 、32767にす る 。
                                                       ; <V350> */
    AxisRscI \rightarrow IntAdV. V1 = swk0;
   飽 和 判断
                 <V531> <V537> 復 活
                                         */
    AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg & OxFFFE;
    swk10 = AxisRscI->StsFlg. IntglFlg | 1;
    AxisRscI->StsFlg. IntglFlg = cmove((AxisRscI->IntAdV. V1 >= 9421), swk10, AxisRscI->StsFlg. IntglFlg);
Over modulation type select
if (AxisRscI->IntAdP. Vmax >= 0x2000)
    if ( (AxisRscI->IntAdP. CtrlSw & OVMSEL2) == 0 )
     if( (AxisRscI\rightarrowIntAdV. V1 >= 0x2000 )&&( (AxisRscI\rightarrowIntAdP. CtrlSw & OVMSEL1) != 0 ) )
```

```
Over modulation1
IxSetCtblAdr( pCtbl, &(OVMODTBLG[0][0]) ); /* gain type
           MpOVMMODK( &AxisRscI->IntAdP, &AxisRscI->IntAdV, pCtbl );
           AxisRscI->VcmpV. VuOut = mulshr limitf(AxisRscI->VcmpV. VuOut, AxisRscI->IntAdP. Kmod, 13);
           AxisRscI->VcmpV. VvOut = mulshr limitf(AxisRscI->VcmpV. VvOut, AxisRscI->IntAdP. Kmod, 13);
          AxisRscI->VcmpV. VwOut = mulshr limitf(AxisRscI->VcmpV. VwOut, AxisRscI->IntAdP. Kmod, 13);
     TMP1 = |V_{cmp}V.V_{u}Out|.
                                                                                 */
                             TMP2 = |VcmpV. VvOut|
                                                     TMP3 = VcmpV. VwOut
/*
     TMP4 = sign(VcmpV. VuOut), TMP5 = sign(VcmpV. VvOut), TMP6 = sign(VcmpV. VwOut)
/*
                                                                                   */
           swk0 = 1;
          swk4 = IxLIMIT( AxisRscI->VcmpV. VuOut, swk0 );
           swk1 = swk4 * AxisRscI->VcmpV. VuOut;
           swk5 = IxLIMIT( AxisRscI->VcmpV. VvOut, swk0 );
          swk2 = swk5 * AxisRscI->VcmpV. VvOut;
           swk6 = IxLIMIT (AxisRscI->VcmpV. VwOut, swk0);
          swk3 = swk6 * AxisRscI->VcmpV.VwOut;
           if(swk1) = swk2
            if(swk1 \ge swk3)
              swk1 = swk1 - 0x2000; /* TMP1 <-- | VcmpV. VuOut | -2000h
              IxLmtzImm16( swk1, 0x7fff ); /* zero limit
              swk0 = swk4 * swk1;
            else
              swk3 = swk3 - 0x2000; /* TMP0 <-- | VcmpV. VwOut | -2000h
              IxLmtzImm16( swk3, 0x7fff ); /* zero limit
                                                                          */
              swk0 = swk6 * swk3;
           else
```

```
swk2 = swk2 - 0x2000; /* TMP0 <-- | VcmpV. VvOut | -2000h
                                                                         */
             IxLmtzImm16( swk2, 0x7fff ); /* zero limit
                                                                  */
             swk0 = swk5 * swk2;
           else
             swk3 = swk3 - 0x2000; /* TMP0 <-- | VcmpV. VwOut | -2000h
             IxLmtzImm16( swk3, 0x7fff ); /* zero limit
             swk0 = swk6 * swk3;
         AxisRscI->VcmpV. VuOut = sub limitf(AxisRscI->VcmpV. VuOut, swk0);
         AxisRscI->VcmpV. VvOut = sub limitf(AxisRscI->VcmpV. VvOut, swk0);
                                                                    /*
         AxisRscI->VcmpV. VwOut = sub_limitf(AxisRscI->VcmpV. VwOut, swk0);
         AxisRscI->IntAdV. Vcent = swk0;
Over modulation2
else
        IxSetCtblAdr( pCtbl, &(OVMODTBLO[0][0]) ); /* ofset type
        MpOVMMODK( &AxisRscI->IntAdP, &AxisRscI->IntAdV, pCtbl );
/*
    MAX = TMP1, MIN = TMP2
                                                     */
/*
    OFS = (TMP1+TMP2)/2
                                                   */
        if( AxisRscI->VcmpV. VuOut >= AxisRscI->VcmpV. VvOut )
         swk1 = AxisRscI->VcmpV. VuOut;
         swk2 = AxisRscI->VcmpV. VvOut;
        else
         swk1 = AxisRscI->VcmpV. VvOut;
         swk2 = AxisRscI->VcmpV. VuOut;
```

```
if ( swk1 < AxisRscI->VcmpV. VwOut )
  swk1 = AxisRscI->VcmpV. VwOut;
else
  if (AxisRscI->VcmpV. VwOut < swk2)
    swk2 = AxisRscI->VcmpV. VwOut;
swk0 = add limitf(swk2, swk1); /*
                                                                */
swk0 = mulshr(swk0, ONE, 1);
AxisRscI->VcmpV. VuOut = sub limitf(AxisRscI->VcmpV. VuOut, swk0);
                                                                        /*
AxisRscI->VcmpV. VvOut = sub_limitf(AxisRscI->VcmpV. VvOut, swk0);
                                                                        /*
AxisRscI->VcmpV. VwOut = sub_limitf(AxisRscI->VcmpV. VwOut, swk0);
                                                                        /*
AxisRscI->IntAdV. Vcent = swk0;
swk0 = 1;
swk0 = IxLIMIT(AxisRscI->VcmpV.VuOut, swk0); /* TMP1= -1/0/+1
                                                                                    */
swk1 = swk1 | 1; /* TMP1 = -1/+1 -----sign (VcmpV. VuOut)
                                                                            */
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
AxisRscI->VcmpV. VuOut = add_limitf( swk2, AxisRscI->VcmpV. VuOut );
swk1 = IxLIMIT( AxisRscI->VcmpV. VvOut, swk0 );
swk1 = swk1 | 1; /* sign(VcmpV. VvOut)
                                                                    */
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
AxisRscI->VcmpV.VvOut = add_limitf( swk2, AxisRscI->VcmpV.VvOut );
                                                                                                           */
swk1 = IxLIMIT( AxisRscI->VcmpV. VwOut, swk0 );
swk1 = swk1 \mid 1; /* sign(VcmpV. VwOut)
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
AxisRscI->VcmpV. VwOut = add_limitf( swk2, AxisRscI->VcmpV. VwOut );
                                                                                                           */
```

```
#endif //#if 0 /* for debug 2013.07.17 tanaka21*/
    On-Delav
IU, IV reference calc
    swk1 = mulshr(AxisRscI->WeakFV.IdOut, AxisRscI->SinTbl.CosT, 14); /* TMP1 <-- ACC >> 14
    swk2 = mulshr(AxisRscI->IntAdV. IqRef, AxisRscI->SinTbl. SinT, 14); /* TMP2 <-- ACC >> 14
    AxisRscI->IntAdV. IuOut = swk1 - swk2; /* IntAdV. IuOut <-- TMP1 - TMP2
    swk3 = mulshr(AxisRscI->WeakFV.IdOut, AxisRscI->SinTbl.CosT3, 14); /* TMP3 <-- ACC >> 14
    swk4 = mulshr(AxisRscI->IntAdV. IqRef, AxisRscI->SinTbl. SinT3, 14); /* TMP4 <-- ACC >> 14
    AxisRscI->IntAdV. IvOut = swk3 - swk4; /* IntAdV. IvOut <-- TMP3 - TMP4
if ( |IntAdV. IuInData | < IntAdP. OnDelayLvl ) TMP1 = IntAdV. IuOut /* Reference */
                      TMP1 = IntAdV. IuInData
     if ( |IntAdV. IvInData | < IntAdP. OnDelayLvl ) TMP2 = IntAdV. IvOut /* Reference */
                     TMP2 = IntAdV. IvInData
     if ( | IWD | < IntAdP.OnDelayLvl ) TMP2 = IWO /* Reference */
                      TMP2 = TWD
swk5 = AxisRscI->IntAdP. OnDelayLvl;
    if(LPX_ABS(AxisRscI->IntAdV. IuInData) > LPX_ABS(swk5)) //110530tanaka21作 業 メモ s w k 2 を 以 降 使 わ な い ため代入 は行
      swk1 = AxisRscI->IntAdV. IuInData; /* TMP1 <-- IntAdV. IuInData
    else
      swk1 = AxisRscI->IntAdV. IuOut; /* TMP1 <-- IntAdV. IuOut
    if(LPX ABS(AxisRscI->IntAdV.IvInData) > LPX ABS(swk5)) //110530tanaka21作業メモ
    swk2を 以 降 使 わ な い た め 代入は行なわない
      swk2 = AxisRscI->IntAdV. IvInData; /* TMP2 <-- IntAdV. IvInData
                                                                         */
```

```
else
 swk2 = AxisRscI->IntAdV. IvOut; /* TMP2 <-- IntAdV. IvOut
                                                                               */
swk3 = -AxisRscI->IntAdV. IuInData - AxisRscI->IntAdV. IvInData; /* TMP3(IWD) <-- - TMP1 - TMP2
if(LPX ABS(swk3) <= LPX ABS(swk5)) //110530tanaka21作 業 メモswk4を 以 降 使 わ な い ため代入 は行なわない
 swk3 = -AxisRscI->IntAdV. IuOut - AxisRscI->IntAdV. IvOut; /* TMP3
swk7 = 0x2000; /* TMP7 < -- 2000h
swk5 = 1; /* TMP5 < -- 1
if (IntAdP. OnDelaySlope != 0) trapezoid type else rectangle type
if( AxisRscI->IntAdP. OnDelaySlope == 0 )
TMP1(ONDVU) = sign(IU)*IntAdP.OnDelayComp
                                                                        */
 swk6 = IxLIMIT(swk1, swk5); /* TMP6 = -1/0/+1
  swk1 = AxisRscI->IntAdP.OnDelayComp * swk6;
TMP2 (ONDVU) = sign(IV)*IntAdP. OnDelayComp
                                                                        */
 swk6 = IxLIMIT(swk2, swk5);
 swk2 = AxisRscI->IntAdP.OnDelayComp * swk6;
TMP3 (ONDVU) = sign(IW)*IntAdP.OnDelayComp
                                                                        */
  swk6 = IxLIMIT(swk3, swk5);
 swk3 = AxisRscI->IntAdP. OnDelayComp * swk6;
trapezoid type
                                                    */
else
```

```
swk0 = mulshr limitf(AxisRscI->IntAdP.OnDelaySlope, swk1, 8); /* TMPO <-- IU*IntAdP.OnDelaySlope>>8
      swk0 = IxLIMIT(swk0, 8192); /* TMP0 = limit(TMP0, 8192)
      swk1 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
                                                                                                         */
      swk0 = mulshr limitf(AxisRscI->IntAdP.OnDelaySlope, swk2, 8); /* TMPO = limit(TMPO, 2^15-1)
                                                                                               */
      swk0 = IxLIMIT( swk0, 8192 ); /* TMP0 = limit(TMP0, 8192)
      swk2 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
      swk0 = mulshr_limitf(AxisRscI->IntAdP.OnDelaySlope, swk3, 8); /* TMPO = limit(TMPO, 2^15-1)
                                                                                               */
      swk0 = IxLIMIT(swk0, 8192); /* TMP0 = limit(TMP0, 8192)
      swk3 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
                                                                                                         */
Voltage conversion to Carrier count range
-2000h..2000h ---> 0h..4000h ---> 0h..CRFRQ
AxisRscI->VcmpV. VuOut = IxLIMIT( AxisRscI->VcmpV. VuOut, swk7 ); /* limit +-2000h
     AxisRscI->VcmpV. VvOut = IxLIMIT( AxisRscI->VcmpV. VvOut, swk7 );
     AxisRscI->VcmpV. VwOut = IxLIMIT( AxisRscI->VcmpV. VwOut, swk7);
/* for debug */
     swk4 = swk7 - AxisRscI->VcmpV. VuOut;
     swk4 = mulshr(swk4, AxisRscI->IntAdV.CrFreqW, 14);
     swk5 = swk7 - AxisRscI->VcmpV. VvOut;
     swk5 = mulshr(swk5, AxisRscI->IntAdV.CrFreqW, 14);
     swk6 = swk7 - AxisRscI->VcmpV. VwOut;
     swk6 = mulshr(swk6, AxisRscI->IntAdV. CrFreqW, 14);
    Deat-time compensation (timer) : if (Vx == 0 \mid \mid Vx == IntAdV. CrFreqW) No compensation
    if ( ( swk4 != ZEROR ) && (swk4 != AxisRscI->IntAdV.CrFreqW ) )
```

```
swk4 = swk4 - swk1; /* VcmpV. VuOut <-- VcmpV. VuOut+ONDVU
        IxLmtzReg16( swk4, swk4, AxisRscI->IntAdV.CrFreqW); /* VcmpV.VuOut <-- limitz( VcmpV.VuOut , IntAdV.CrFreqW)
      if ( (swk5 != ZEROR ) && (swk5 != AxisRscI->IntAdV.CrFreqW ) )
        swk5 = swk5 - swk2; /* VcmpV. VvOut <-- VcmpV. VvOut+ONDVV</pre>
        IxLmtzReg16( swk5, swk5, AxisRscI->IntAdV.CrFreqW); /* VcmpV.VvOut <-- limitz( VcmpV.VvOut , IntAdV.CrFreqW)
      if ( ( swk6 != ZEROR ) && (swk6 != AxisRscI->IntAdV.CrFreqW ) )
        swk6 = swk6 - swk3; /* VcmpV. VwOut <-- VcmpV. VwOut+ONDVW</pre>
        IxLmtzReg16( swk6, swk6, AxisRscI->IntAdV.CrFreqW ); /* VcmpV.VwOut <-- limitz( VcmpV.VwOut , IntAdV.CrFreqW )</pre>
      AxisRscI \rightarrow PwmV. PwmCntT2 = swk6;
      AxisRscI \rightarrow PwmV. PwmCntT1 = swk5;
      AxisRscI->PwmV. PwmCntT0 = swk4;
      Output Voltage & status
                                                                  */
//<2>#ifdef PREG DEF
     CTSTW = AxisRscI->StsFlg.CtrlStsRW; /* Status Set
    AxisRscI->CtrlStsOut = AxisRscI->StsFlg.CtrlStsRW; /* Status Set
                                                                                     *//* <0ka01> */
#ifdef MULTI AXIS
                                                理有効
  {/* Axis1 start */
 /* Execute Current Loop Main Operation */
    AxisRscI = &AxisHdl[1];
// 位 相 補 間処理
                          <V112>
```

```
swk10 = AxisRscI->PhaseV. PhaseH + AxisRscI->PhaseV. PhaseIp;
      AxisRscI->PhaseV. PhaseIpF = cmove((AxisRscI->PhaseV. PhaseIpF!= 1), ONE, AxisRscI->PhaseV. PhaseIpF);
      AxisRscI->PhaseV. PhaseV. Phas
    PHASE UPDATE処 理 〈V112〉
          theta calculation
      swk0 = AxisRscI->PhaseV. PhaseH;
      swk0 = swk0 + \frac{32}{32}; /* TMP3 <-- PhaseV. PhaseH + 2^5 */
      swk1 = PI23;
      swk2 = swk1 + swk0; /* TMP4 < -- PhaseV. PhaseH + 2PI/3 */
      swk3 = swk0 - swk1; /* TMP5 <-- PhaseV. PhaseH - 2PI/3 */
          table read and get iu, iv by Id, Ig reference
                                          /* TMP1 <-- TMP3 >> 6 */
      swk1 = swk0 >> 6;
      IxTblSin16(AxisRscI->SinTbl.SinT、swk1); /* SinTbl.SinT <-- stable「TMP1 ] *//* tanaka21.要 コ メ ン ト解除 */
      swk0 = swk0 + PI2;
                                           /* TMP3 <-- TMP3 + PI/2 */
                                           /* TMP1 <-- TMP3 >> 6 */
      swk1 = swk0 >> 6;
      IxTblSin16(AxisRscI->SinTbl.CosT, swk1); /* SinTbl.CosT <-- stable[TMP1]*//* tanaka21,要 コ メ ン ト解除 */
      swk1 = swk3 >> 6;
                                                   /* TMP1 <-- TMP5 >> 6 */
      IxTblSin16(AxisRscI->SinTbl.SinT3, swk1); /* SinTbl.SinT3 <-- stable「TMP1 ] *//* tanaka21,要 コ メ ン ト解除 */
      swk3 = swk3 + PI2;
                                            /* TMP5 <-- TMP5 + PI/2 */
      swk1 = swk3 >> 6;
                                                  /* TMP1 <-- TMP5 >> 6 */
      IxTblSin16(AxisRscI->SinTbl.CosT3, swk1); /* SinTbl.CosT3 <-- stable「TMP1]*//* tanaka21,要 コ メ ン ト解除 */
      swk1 = swk2 >> 6;
                                                  /* TMP1 <-- TMP4 >> 6 */
      IxTblSin16(AxisRscI->SinTbl.SinT2, swk1); /* SinTbl.SinT2 <-- stable「TMP1]*//* tanaka21,要 コ メ ン ト解除 */
      swk2 = swk2 + PI2;
                                             /* TMP4 <-- TMP4 + PI/2 */
                                                /* TMP1 <-- TMP4 >> 6 */
      swk1 = swk2 >> 6;
      IxTblSin16(AxisRscI->SinTbl.CosT2, swk1); /* SinTbl.CosT2 <-- stable「TMP1 ] *//* tanaka21,要 コ メ ン ト解除 */
                                       _____
```

```
dq-trans(UVW to DQ)
/*
     ID = IntAdP. Kc * ( (SinTbl. CosT-SinTbl. CosT2)*IntAdV. IuInData/2^14 + (SinTbl. CosT3-SinTbl. CosT2)*IntAdV. IvInData/2^14 )
/2^{9}
     */
     IQ = IntAdP. Kc * ( (SinTbl. SinT2-SinTbl. SinT)*IntAdV. IuInData/2 14 + (SinTbl. SinT2-SinTbl. SinT3)*IntAdV. IvInData/2 14 )
/2^{9}
/*--
   /* TMP1 < --\cos(th) - \cos(th-2pi/3) */
   swk1 = AxisRscI->SinTbl.CosT - AxisRscI->SinTbl.CosT2;
   /* ACC <-- TMP1 * iu */
   swk2 = mulshr(swk1, AxisRscI->IntAdV. IuInData, 14);
   /* TMP1 < --\cos(th-2pi/3) - \cos(th+2pi/3) */
   swk1 = AxisRscI->SinTb1.CosT3 - AxisRscI->SinTb1.CosT2;
   /* ACC <-- TMP1 * iv */
   swk1 = mulshr(swk1, AxisRscI->IntAdV.IvInData, 14 );
   /* TMP2 <-- TMP2 + TMP1 */
   swk2 = swk1 + swk2;
   /* ACC <-- IntAdP. Kc * TMP2 */
   AxisRscI->IntAdV. IdInData = mulshr(AxisRscI->IntAdP. Kc, swk2, 9);
                                                                           /* TMP1 <-- sin(th+2pi/3) - sin(th)
   swk1 = AxisRscI->SinTbl.SinT2 - AxisRscI->SinTbl.SinT;
   swk2 = mulshr(swk1, AxisRscI->IntAdV. IuInData, 14); /* ACC <-- TMP1 * iu
                                                                           /* TMP1 \leftarrow sin(th+2pi/3)-sin(th-2pi/3)
   swk1 = AxisRscI->SinTb1.SinT2 - AxisRscI->SinTb1.SinT3;
   swk1 = mulshr(swk1, AxisRscI->IntAdV. IvInData, 14); /* ACC <-- TMP1 * iv
   swk2 = swk1 + swk2; /* TMP2 <-- TMP2 + TMP1
   AxisRscI->IntAdV. IqInData = mulshr(AxisRscI->IntAdP. Kc, swk2, 9); /* ACC <-- IntAdP. Kc * TMP2
                                                                                                               */
     Current Observer <V038>
       流 オ ブ ザ ー バ スイッチ
if( AxisRscI->IntAdP.Ctr1Sw & OBSSEL )
```

```
if (AxisRscI->IntAdV. IqInData >= 0)
  { /* 0以 上 のとき */
   /* TMP3 = IntAdV. IgInData */
   swk2 = AxisRscI->IntAdV. IqInData;
  else
              /* 負 の とき
   swk2 = ~AxisRscI->IntAdV. IqInData; /* TMP3 = ~IntAdV. IqInData;
   *///110530tanaka21作 業 メモ、 - 1 掛 け る のとどっ
                                                                 ちが凍い?
   swk2 = swk2 + 1; /* TMP3 = TMP3 + 1
  if ( swk2 <= 14250 )
    swk3 = ZERO; /* TMP4 = 0 ( OverFlowCheck = OK )
  else
    swk3 = ONE; /* TMP4 = 1 ( OverFlowCheck = NG )
d軸 オ ブ ザ ーバ 部
  swk0 = mulshr(AxisRscI->DobsP. TsPerL, AxisRscI->VcmpV. VdOut, 15); /* TMPO <-- ACC >> 15 ( TMPO = Ts/L * Vd out >>
  15) */
  swk2 = AxisRscI->IntAdV. IdInData; /* TMP3 <-- IntAdV. IdInData
                                                                    <V076>
  swk2 = 1imit(swk2, 15000);
  swk1 = swk2 - AxisRscI->DobsV. IdObsOut; /*
                                                                    */
                                                         <V076>
  swk1 = mulshr(AxisRscI->DobsP. Gobs, swk1, 16); /* ACC <-- TMP2*DobsP. Gobs ( TMP2 = g * ( Id - Id obs ) ) */
  swk0 = swk1 + swk0; /* TMP0 < -- TMP0 + TMP2  ( TMP0 = (g*(Id-Id obs)>>16) + (Ts/L*Vd out>>15) ) */
  swk1 = mulshr(AxisRscI->DobsP. RLTs, AxisRscI->DobsV. IdObsOut, 12); /* TMP2 <-- DobsV. IqObsOut
  */
```

```
AxisRscI->DobsV. IdObsOut = add limitf(swk1, swk0); /* DobsV. IdObsOut <-- limit( DobsV. IdObsOut, 2^15-1)
                                                                                                                      */
    フィルタ部
error obs
  swk0 = AxisRscI->IntAdV. IdInData - AxisRscI->DobsV. IdObsOut; /*
low pass filter
  swk0 = sub limitf(swk0, AxisRscI->DobsV.LpfIld.s[1]);
  lwk2 = mul(AxisRscI->DobsP.Fil0bsGain, swk0) << 2; /*
                                                                                    */
  AxisRscI->DobsV.LpfIld.1 = add limitf(lwk2, AxisRscI->DobsV.LpfIld.1);
high pass filter
  swk0 = sub_limitf(AxisRscI->DobsV.LpfIld.s[1], AxisRscI->DobsV.HpfIld.s[1]);
                                                                                    */
  lwk2 = mul(AxisRscI->DobsP.FilObsGain, swk0) << 2; /*
  AxisRscI->DobsV. HpfIld. 1 = add_limitf(lwk2, AxisRscI->DobsV. HpfIld. 1); /*
  AxisRscI->DobsV. IdObsFreq = AxisRscI->DobsV. LpfIld. s[1] - AxisRscI->DobsV. HpfIld. s[1]; /*
                                                                                                                */
IntAdV. IdInData = IntAdV. IdInData - DobsV. IdObsFreq
  AxisRscI->DobsV. IdObsFreq = AxisRscI->DobsV. IdObsFreq * 2; /* ACC <-- DobsV. IdObsFreq * DobsV. DmpGain
  AxisRscI->DobsV. IdObsFreq = cmove((swk3 != 0), ZERO, AxisRscI->DobsV. IdObsFreq);
  AxisRscI->IntAdV. IdInData = AxisRscI->IntAdV. IdInData - AxisRscI->DobsV. IdObsFreq; /*
                                                                                                                    */
a軸 オ ブ ザ ーバ 部
  swk0 = mulshr(AxisRscI->DobsP. TsPerL, AxisRscI->VcmpV, VqOut, 15); /* ACC <-- TMPO*Ts/L (TMPO = Ts/L * Vq out)
                                                                    /* TMP3 <-- IntAdV. IgInData
                                                                                                       < V076>
  swk2 = AxisRscI->IntAdV. IqInData;
  swk2 = 1imit(swk2, 15000);
                                                  /* TMP3 <-- Limit(15000) <V076>
```

```
<V076>
  swk1 = swk2 - AxisRscI->DobsV. Ig0bsOut;
  swk1 = mulshr(AxisRscI \rightarrow DobsP. Gobs, swk1, 16); /* TMP2 <-- ACC >> 16 ( TMP2 = g * ( Iq - Iq obs ) >> 16 ) */
  swk0 = swk1 + swk0;
                                          /* TMP0 < -- TMP0 + TMP2 ( TMP0 = (g*(Ia-Ia obs))>16 ) + (Ts/L*Va out>>15) )
  swk1 = mulshr(AxisRscI->DobsP. RLTs, AxisRscI->DobsV. IqObsOut, 12); /* TMP2 <-- ACC >> 12 (TMP2 = (1-R*Ts/L)*Iq obs
  AxisRscI->DobsV. IqObsOut = add limitf(swk1, swk0);
                                                                                /* DobsV. IgObsOut <-- limit( DobsV. IgObsOut,
  swk0 = AxisRscI->IntAdV. IqInData - AxisRscI->DobsV. IqObsOut; /*
low pass filter
  swk0 = sub limitf(swk0. AxisRscI->DobsV.LpfIla.s[1]); /*
  lwk2 = mul(AxisRscI->DobsP.FilObsGain, swk0) << 2; /*
  AxisRscI->DobsV. LpfIlq. 1 = add_limitf(lwk2, AxisRscI->DobsV. LpfIlq. 1); /*
high pass filter
  swk0 = sub_limitf(AxisRscI->DobsV.LpfIlq.s[1], AxisRscI->DobsV.HpfIlq.s[1]); /*
  lwk2 = mul(AxisRscI->DobsP.Fil0bsGain, swk0) << 2; /*</pre>
  AxisRscI->DobsV. HpfIlq. 1 = add_limitf(lwk2, AxisRscI->DobsV. HpfIlq. 1); /*
  AxisRscI->DobsV. IqObsFreq = AxisRscI->DobsV. LpfIlq. s[1] - AxisRscI->DobsV. HpfIlq. s[1]; /*
IntAdV. IqInData = IntAdV. IqInData - DobsV. IqObsFreq
  AxisRscI->DobsV. IqObsFreq = AxisRscI->DobsV. IqObsFreq * 2; /* ACC <-- DobsV. IqObsFreq * DobsV. DmpGain
  AxisRscI->DobsV. IqObsFreq = cmove((swk3 != 0), ZERO, AxisRscI->DobsV. IqObsFreq);
  AxisRscI->IntAdV. IqInData = AxisRscI->IntAdV. IqInData - AxisRscI->DobsV. IqObsFreq; /*
                                                                                                                       */
                                                                                               ---*///110526tanaka21.BBチ ェ
```

```
ク 処 理 、 処 理 順 をいろいろ変更。
                                         *///if-else if-elseの 形 で 書 き 換 え 。 正 し く 動 作 するか要 硝
   Base Block Check
  if( AxisRscI->AdStop. ADRst != 0 )
   AxisRscI->AdStop. ADRst = 0;
   swk6 = AxisRscI->IntAdV. CrFreqW >> 1;
   AxisRscI->PwmV. PwmCntT2 = swk6;
   AxisRscI->PwmV. PwmCntT1 = swk6;
   AxisRscI->PwmV. PwmCntT0 = swk6;
  /* 2012.12.20 Y.Oka 誤 り 修正 */
  else if ( (AxisRscI->StsFlg.CtrlStsRW & BB) != 0 )
   swk6 = AxisRscI->IntAdV.CrFreqW >> 1;
   AxisRscI->PwmV. PwmCntT2 = swk6;
   AxisRscI->PwmV. PwmCntT1 = swk6;
   AxisRscI->PwmV. PwmCntT0 = swk6;
  else
弱 め 界磁用 Id指令計算処理
                               <V214>
   if( AxisRscI->IntAdP. Ctr1Sw & V_FB )
/*
    差分電圧作成
    */
```

```
V_{\text{qmax}} = \sqrt{(V_{\text{max}}X^2 - V_{\text{d}}^2)}
 1wk2 = AxisRscI->WeakFV.WfV1Max * AxisRscI->WeakFV.WfV1Max; /* IntAdP.Vmax^2
                                                                                                         復 活<V531> */
 lwk4 = AxisRscI->WeakFV.WfVdRef * AxisRscI->WeakFV.WfVdRef; /* Vd^2
 1wk2 = sub limitf(1wk2, 1wk4);
 lwk2 = limitz(lwk2, LPX_REG32_MAX); /* if (IntAdP. Vmax^2 - Vd^2) < 0, then (IntAdP. Vmax^2 - Vd^2) = 0 */
                          /*\sqrt{\text{IntAdP. Vmax}^2 - Vd^2}
 swk0 = MpSQRT(1wk2);
 if ( swk0 > 0x7FFF )
   swk0 = 0x7FFF; /*
 AxisRscI->WeakFV. WfVqMax = swk0; /* Vqmax = \sqrt{\frac{1}{1000}} (IntAdP. Vmax^2 - Vd^2)
TMPO = Vamax - Va
 swk1 = AxisRscI->WeakFV. WfVqRef;
 if (swk1 < 0)
   swk1 = (SHORT) ZEROR - swk1; /* TMP1 = |Vq|
 swk0 = sub_limitf(AxisRscI->WeakFV.WfVqMax, swk1);
比例項計算
                                                           */
 1wk1 = (LONG) swk0; /* TMP1, 0 = 符 号 拡張(TMP0)
                                                                        */
 swk2 = (SHORT) mulshr( lwk1 , AxisRscI->WeakFV. WfKpV. 1, 32 );
 if (swk2 > (SHORT)0x0080)
    swk2 = LPX REG16 MAX; /* 正 の 最 大値
 else if ( swk2 < (SHORT) 0xFF80 )
   swk2 = LPX_REG16_MIN; /* 負 の 最 大値
 else
```

```
lwk2 = mulshr16( lwk1, AxisRscI->WeakFV.WfKpV.1);
          swk2 = mulshr(lwk2, (LONG) 256, 16);
/*
        1 \text{wk4} = 1 \text{wk1} * \text{AxisRscI} \rightarrow \text{WeakFV. WfKiV. 1}; /* \Delta \text{Vg} * \text{Kiv}
                                                                                         */
        lwk6 = mulshr( lwk1, AxisRscI->WeakFV. WfKiV. 1, 32 ); /* \Delta Vq * Kiv
        if ((SHORT) 1 \text{wk} 6 > 0 \text{x} 08)
          1wk4 = LPX REG32 MAX; /* 正 の 最 大値
        else if ( (USHORT) 1wk6 > 0xFFF8 )
          1wk4 = LPX_REG32_MIN; /* 負 の 最 大値
        else
          1wk4 = 1wk4 >> 4; /*
          1wk4 = 1wk4 & 0x0ffffffff; /*
          1 \text{wk} 6 = 1 \text{wk} 6 << 28; /*
          1 \text{wk4} = 1 \text{wk6} \mid 1 \text{wk4}; /* \text{TMP5}, 4 = \Delta \text{Vq} * \text{Kiv} (* 2^16)
        AxisRscI->WeakFV.WfIntgl.1 = add limitf(lwk4, AxisRscI->WeakFV.WfIntgl.1); /*
        lwk6 = (ULONG) AxisRscI->WeakFV. WfIntegLim << 16; /* TMP9, 8 = WeakFV. WfIntegLim * 2^16
        AxisRscI->WeakFV. WfIntgl. 1 = limit(AxisRscI->WeakFV. WfIntgl. 1, lwk6); /* WFINTEGH = \Delta Vq * Kiv (* 2^16 / 2^16) */
      比例項 + 積分項
        swk4 = add_limitf(AxisRscI->WeakFV. WfIntgl. s[1], swk2);
        swk4 = limit(swk4, AxisRscI->WeakFV.WfIdRefLim); /* IdrefLimでリミット
      Idref > 0 な ら ば、Idref = 0,積分 = 0
/*
        Idref(d軸 電 流指 令 ) が 正 に な る こ と は 無 い 。正になった 場合は0にする。
                                                                                                                            */
/*
        AxisRscI->WeakFV. IdOut = swk4;
```

```
swk10 = AxisRscI->WeakFV. IdOut;
       AxisRscI->WeakFV. IdOut = cmove((swk10 > 0), ZERO, AxisRscI->WeakFV. IdOut);
       AxisRscI-WeakFV. WfIntgl. 1 = cmove((swk10 > 0), (LONG)ZEROR, AxisRscI-WeakFV. WfIntgl. 1);
ACRd(d軸 電 流 制 御)
     TMP1 = limit(WeakFV.IdOut - IntAdV.IdInData . 2<sup>15</sup> - 1)
     swk1 = sub limitf(AxisRscI->WeakFV.IdOut, AxisRscI->IntAdV.IdInData); /* TMP1 <-- limit( TMP1
                                                                                                                    */
     TMP2 = limit(IntAdP. KdP * TMP1 / 2^9, 2^15 - 1)
     swk2 = mulshr_limitf(AxisRscI->IntAdP. KdP, swk1, 9); /* ACC <-- IntAdP. KdP * TMP1
     IdIntgl(32) = (IntAdP. KdI * TMP1) << 3 + IdIntgl(32)
     IDIH = limit( IDIH , IntAdP. VdLim )
                                                                             */
     lwk4 = ((ULONG) AxisRscI->IntAdP. VdLim) << 16; /*</pre>
     lwk6 = mul(AxisRscI->IntAdP. KdI, swk1) << 3; /*</pre>
     AxisRscI->AcrV. IdIntgl. 1 = add limitf(lwk6, AxisRscI->AcrV. IdIntgl. 1); /* AcrV. IdIntgl <-- limit( AcrV. IdIntgl , 2 31 -
     if (LPX ABS (AxisRscI->AcrV. IdIntgl. 1) > LPX ABS (1wk4) )
       AxisRscI->StsFlg.CtrlStsRW = AxisRscI->StsFlg.CtrlStsRW | DLIM; /*
       swk0 = AxisRscI->IntAdP.CtrlSw;
       AxisRscI->AcrV. IdIntgl. 1 = cmove(((AxisRscI->IntAdP. CtrlSw & ICLR) != 0), (LONG) ZEROR, AxisRscI->AcrV. IdIntgl. 1);
     VcmpV. VdOut = limit( TMP2 + IDIH +TMP3, 2<sup>15</sup> - 1)
     swk1 = add_limitf(AxisRscI->AcrV.IdIntgl.s[1], swk2); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )</pre>
```

```
filter: AcrV. VdFil = (((TMP1 - VDFH) * IntAdP. Tfil) << 2) + AcrV. VdFil
ACRq(q軸 電 流 制 御)
Low Pass Filter
IntAdP. TLpf2 : Time-constant
IntAdV. IgOut2Lpf: Output (32 bit) .. IQOF: High 16 bit
WeakFV. IqOut : Input
IQOF(32) = ( ( WeakFV. IqOut - IQOF(16) ) * IntAdP. TLpf2 ) << 2 ) + IntAdV. IqOut2Lpf(32)
if ( (AxisRscI->IntAdP. CtrlSw & LPFCDSABL) != 0 )
 AxisRscI->IntAdV. IqOut2Lpf. s[1] = AxisRscI->WeakFV. IqOut; /* disable LPF
else
 swk0 = sub_limitf(AxisRscI->WeakFV.IqOut, AxisRscI->IntAdV.IqOut2Lpf.s[1]); /* TMPO <-- limit( TMPO, 2^15 - 1 )
 lwk2 = mul(AxisRscI->IntAdP. TLpf2, swk0) << 2;</pre>
 AxisRscI->IntAdV. IqOut2Lpf. 1 = add_limitf(AxisRscI->IntAdV. IqOut2Lpf. 1, 1wk2);
AxisRscI->IntAdV. IqMonFil = AxisRscI->IntAdV. IqOut2Lpf. s[1]; /* IntAdV. IqMonFil:フィルタ後のq軸電流(モニタ用) < V2242
AxisRscI->IntAdV. IqOfRef = add_limitf(AxisRscI->IntAdV. IqOut2Lpf. s[1], AxisRscI->IntAdV. IqDist); /* IntAdV. IqOfRef <--
limit (IntAdV. IgOfRef , 2<sup>15</sup> - 1) <V224>
```

```
Torque Limit:
                    <V214>
                                                      御
                                                                          令 が作られるので、a軸電流指令は以下の式で
/*
      電 圧
                                                         で d 軸 電 流 指
                                 リ ミ ッ ト 設 定 値 の いずれか小さい方でリミットする。
/*
                 ット値 = \sqrt{(Imax^2-Id*^2)}
    Id*に よるTorque Lim it値
    */
    swk0 = AxisRscI->IntAdP. Ctr1Sw;
    swk1 = V FB \mid V FB2;
    swk0 = swk0 & swk1; /* TMP0の bit11, bit13 以 外 を マスクする
    if ( swk0 != V FB )
      lwk4 = mul(AxisRscI->WeakFV.IdOut, AxisRscI->WeakFV.IdOut); /* Idref^2
                                                                                 ;削 除<V309 > 復活<V531> */
    else
      lwk4 = mul(AxisRscI->WeakFV.WfIdRefLim, AxisRscI->WeakFV.WfIdRefLim); /* IdrefLim^2
                                                                                  ; <V309>
    1wk2 = 1wk2 - 1wk4; /* Imax^2 - Id^2
    swk0 = MpSQRT(1wk2);
    swk1 = swk0; /* TMP0 = \sqrt{(Imax^2 - Id^2)}
    Torque Limit
    if( AxisRscI->IntAdV. IqOfRef >= 0 )
      swk1 = limit(swk1, AxisRscI->IntAdV.TLimP); /* 正 側 ト ル ク リミット
      AxisRscI->IntAdV. IqRef = limit( AxisRscI->IntAdV. IqOfRef, swk1 ); /* 〈V224〉外
      swk10 = AxisRscI->StsFlg.CtrlStsRW | TLIM; /* TLIM flag set
      AxisRscI->StsFlg. CtrlStsRW = cmove((AxisRscI->IntAdV. IqRef == swk1), swk10, AxisRscI->StsFlg. CtrlStsRW);
    else
      swk1 = limit(swk1, AxisRscI->IntAdV.TLimM); /* 負 側 ト ル ク リミット
```

```
AxisRscI->IntAdV. IaRef = limit( AxisRscI->IntAdV. IaOfRef, swk1 ); /* <V224> 外 乱 ト ル ク 加 算 後 のa軸電流指令
  swk10 = AxisRscI->IntAdV. IaRef + swk1;
  swk11 = AxisRscI->StsFlg.CtrlStsRW | TLIM; /* TLIM flag set
  AxisRscI->StsFlg.CtrlStsRW = cmove((swk10 == 0), swk11, AxisRscI->StsFlg.CtrlStsRW); /* TLIM flag set
TMP1 = limit(IntAdV.IqRef - IntAdV.IqInData, 2^15 - 1)
swk1 = sub limitf(AxisRscI->IntAdV. IgRef, AxisRscI->IntAdV. IgInData); /* TMP1 <-- limit( TMP1, 215 - 1)
                                                                                                                    */
TMP2 = limit( IntAdP. KqP * TMP1 / 2^9 , 2^15 - 1 )
swk2 = mulshr_limitf(AxisRscI->IntAdP. KqP, swk1, 9); /* TMP2 <-- limit( TMP2 , 2^15 - 1 )
AcrV. IqIntgl(32) = (IntAdP. KqI * TMP1) << 3 + AcrV. IqIntgl(32)
IQIH = limit( IQIH , IntAdP.VqLim )
if( ((AxisRscI->IntAdP.CtrlSw & INT ST) == 0) || ((AxisRscI->StsFlg.IntglFlg & 1) == 0))
  lwk6 = mul(AxisRscI->IntAdP.KqI, swk1); /* ACC <-- IntAdP.KqI * TMP1
  lwk4 = (ULONG) AxisRscI->IntAdP. VqLim; /*
  1wk4 = 1wk4 << 16; /*
  1 \text{wk} 6 = 1 \text{wk} 6 << 3; /*
  AxisRscI->AcrV. IqIntgl. 1 = add_limitf(lwk6, AxisRscI->AcrV. IqIntgl. 1); /* AcrV. IqIntgl <-- limit( AcrV. IqIntgl , 2^32
  if (LPX_ABS (AxisRscI->AcrV. IqIntgl. 1) > LPX_ABS (1wk4) )
    AxisRscI->StsFlg. CtrlStsRW = AxisRscI->StsFlg. CtrlStsRW | QLIM; /* IMM3 <-- STAT | QLIM (imm 16)
                                                                                                                      */
    swk10 = AxisRscI->IntAdP. CtrlSw & ICLR;
    AxisRscI->AcrV. IqIntgl. 1 = cmove((swk10 != 0), (LONG) ZEROR, AxisRscI->AcrV. IqIntgl. 1);
VcmpV. VgOut = limit( TMP2 + IQIH +TMP3 , 2<sup>15</sup> - 1)
```

```
swk1 = add_limitf(AxisRscI->AcrV.IqIntgl.s[1], swk2); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )</pre>
     filter: AcrV.VaFil = ( ( ( TMP1 - VQFH ) * IntAdP.Tfil ) << 2 ) + AcrV.VaFil
    swk1 = sub limitf(swk1, AxisRscI->AcrV. VqFil.s[1]); /* TMP1 <-- limit( TMP1 , 2^15 - 1 )
    lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
    AxisRscI->AcrV. VqFil. 1 = add limitf(AxisRscI->AcrV. VqFil. 1, 1wk0);
/*
    Voltage Compensation(電 圧 補償)
if ( (AxisRscI->IntAdP. Ctr1Sw & ISEL) != 0 )
      swk1 = AxisRscI->WeakFV. IdOut; /* TMP1 <-- reference ID</pre>
      swk2 = AxisRscI->IntAdV. IqRef; /*
     else
      swk1 = AxisRscI->IntAdV. IdInData; /* TMP1 <-- feedback ID
      swk2 = AxisRscI->IntAdV. IqInData; /* TMP2 <-- feedback IQ
     TMP4(VcmpV.VdComp) = IntAdP.MotResist*TMP1/2^15 - VcmpV.LqC * TMP2 / 2^15
    swk4 = mulshr(AxisRscI->VcmpV. LqC, swk2, 15); /* VcmpV. VdComp <-- ACC >> 15
    swk0 = mulshr(AxisRscI->IntAdP. MotResist, swk1, 15);
     swk4 = swk0 - swk4;
    TMP5 (VcmpV. VaComp) = VcmpV. LdC * TMP1 / 2^15 + VcmpV. MagC + IntAdP. MotResist*TMP2/2^15
     swk3 = mulshr(AxisRscI->VcmpV.LdC, swk1, 15); /* TMP3 <-- ACC >> 15
    swk0 = mulshr(AxisRscI->IntAdP. MotResist, swk2, 15);
     swk3 = swk3 + AxisRscI->VcmpV. MagC;
    swk5 = swk3 + swk0; /* VcmpV. VqComp <-- VcmpV. MagC + TMP3 + TMP0
```

```
if (IntAdP. CtrlSw & DIDTSET) VcmpV. VdComp = TMP4 + KDD * (IntAdV. IdDataP - IntAdV. IdInData).
IntAdV. IdDataP=IntAdV. IdInData
               VcmpV.VqComp = TMP5 + KQD * (IntAdV.IqDataP - IntAdV.IqRef), IntAdV.IqDataP=IntAdV.IqRef
                                                                                                                           */
      if ( (AxisRscI->IntAdP. CtrlSw & DIDTSEL) == 0 )
        AxisRscI->VcmpV. VdComp = swk4; /*
        AxisRscI->VcmpV. VqComp = swk5; /*
     filter: I*FL = (((TMP1 - I*FH) * IntAdP.Tfil) << 2) + I*FL
      else
        swk1 = AxisRscI->WeakFV. IdOut; /*
        swk1 = sub limitf(swk1, AxisRscI->IntAdV. IdLfil.s[1]); /*
        lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
        AxisRscI->IntAdV. IdLfil. 1 = add limitf(AxisRscI->IntAdV. IdLfil. 1, lwk0); /*
                                                                                                                  */
        swk1 = AxisRscI->IntAdV. IqRef; /*
        swk1 = sub limitf(swk1, AxisRscI->IntAdV. IqLfil. s[1]); /*
        lwk0 = mul(AxisRscI->IntAdP. Tfil, swk1) << 2; /*</pre>
        AxisRscI->IntAdV. IqLfil. 1 = add limitf(AxisRscI->IntAdV. IqLfil. 1, lwk0); /*
                                                                                                                  */
        swk2 = AxisRscI->IntAdV. IdLfil.s[1] - AxisRscI->IntAdV. IdDataP; /*
        AxisRscI->IntAdV. IdDataP = AxisRscI->IntAdV. IdLfil. s[1]; /*
        swk2 = mulshr_limitf(AxisRscI->IntAdP.L_dIdt, swk2, 9); /* limit( VDL , 2^15 - 1 )
        AxisRscI->VcmpV. VdComp = add limitf(swk2, swk4); /* VcmpV. VdComp <-- limit( VcmpV. VdOut , 2 15 - 1 )
        swk2 = AxisRscI->IntAdV. IqLfil. s[1] - AxisRscI->IntAdV. IqDataP; /*
        AxisRscI->IntAdV. IqDataP = AxisRscI->IntAdV. IqLfil. s[1];
        swk2 = mulshr_limitf(AxisRscI->IntAdP.L_dIdt, swk2, 9); /* limit( VQL , 2^15 - 1 )
        AxisRscI->VcmpV. VqComp = add limitf(swk2, swk5); /* VcmpV. VqComp <-- limit( VcmpV. VqOut, 2 15 - 1)
     TMP1 = limit( VDFH + VcmpV. VdComp , 2<sup>15</sup> - 1)
                                                                                 */
```

```
TMP2 = limit ( VQFH + VcmpV. VqComp , 2<sup>15</sup> - 1 )
    swk1 = add limitf(AxisRscI->AcrV, VdFil.s[1], AxisRscI->VcmpV, VdComp); /* VcmpV, VdOut <-- limit( VcmpV, VdOut , 2^15 - 1)
    swk2 = add limitf(AxisRscI->AcrV. VqFil. s[1], AxisRscI->VcmpV. VqComp); /* VcmpV. VqOut <-- limit( VcmpV. VqOut , 2^15 - 1)
    TMP1 = limit(VcmpV. VdRef + TMP1, 2^15 - 1)
/*
    TMP2 = 1imit(VcmpV.VqRef + TMP2, 2^15 - 1)
/*
    swk1 = add limitf(AxisRscI->VcmpV.VdRef, swk1); /* VcmpV.VdOut <-- limit( VcmpV.VdOut, 2^15 - 1)
    swk2 = add_limitf(AxisRscI->VcmpV. VqRef, swk2); /* VcmpV. VqOut <-- limit( VcmpV. VqOut . 2^15 - 1
    VcmpV. VdOut = limit(IntAdP. Kvv * TMP1 / 2<sup>13</sup>, 2<sup>15</sup> - 1)
    VcmpV. VqOut = limit(IntAdP. Kvv * TMP2 / 2<sup>13</sup>, 2<sup>15</sup> - 1)
    AxisRscI->VcmpV. VdOut = mulshr_limitf(AxisRscI->IntAdP. Kvv, swk1, 13); /* VcmpV. VdOut <-- limit( TMP1 . 2^15 - 1 )
                                                          /* VcmpV. VaOut <-- limit( TMP2 , 2 15 - 1 )
    AxisRscI->VcmpV. VqOut = mulshr limitf(AxisRscI->IntAdP. Kvv, swk2, 13);
    AxisRscI->WeakFV. WfVdRef = AxisRscI->VcmpV. VdOut;
                                         /* d軸
                                                                <V531>
                                                                               */
    AxisRscI->WeakFV. WfVqRef = AxisRscI->VcmpV. VqOut;
                                         /* a軸
                                                                <V531>
                                                                               */
電 圧 ベ ク ト ル 補 正値計算
                                 〈V537〉新 弱 め 界 磁 制
                                                                                          */
if ( (AxisRscI->IntAdP. CtrlSw & V FB2) != 0 )
Get modulation
                        〈V531〉麥
lwk2 = mul (AxisRscI->VcmpV. VdOut, AxisRscI->VcmpV. VdOut);
     lwk2 = mac(AxisRscI->VcmpV.VqOut, AxisRscI->VcmpV.VqOut, lwk2);
                        /* TMP0 = \sqrt{\text{(VcmpV. VdOut}^2 + VcmpV. VqOut}^2}
     swk0 = MpSQRT(1wk2);
                                                                         */
     AxisRscI->IntAdV. V1 = swk0; /* IntAdV. V1 = TMP0
〈V531〉 IntAdV, V1 > 8192*127%(10403, 8) -> 飽
```

```
AxisRscI - VcmpV. Vmax2 = 10403; /* VcmpV. Vmax2 = 8192 * 1.27
       AxisRscI\rightarrowVcmpV. V12 = AxisRscI\rightarrowIntAdV. V1; /* VcmpV. V12 = \sqrt{\text{(VcmpV. VdOut}^2 + VcmpV. VaOut}^2}
                                                                                                                            */
       swk10 = AxisRscI->VcmpV. Vmax2 >> 1; /* VcmpV. Vmax2 = 8192 * 1.27 / 2
       swk11 = AxisRscI - VIntAdV. V1 >> 1; /* VcmpV. V12 = \sqrt{(VcmpV. VdOut^2 + VcmpV. VgOut^2)} / 2
                                                                                                                  */
       AxisRscI->VcmpV. Vmax2 = cmove((AxisRscI->IntAdV. V1 < 0), swk10, AxisRscI->VcmpV. Vmax2);
       AxisRscI->VcmpV. V12 = cmove ((AxisRscI->IntAdV. V1 < 0), swk11, AxisRscI->VcmpV. V12);
       if (AxisRscI->VcmpV. Vmax2 < AxisRscI->VcmpV. V12)
         AxisRscI->IntAdV. V1 = 10403; /* IntAdV. V1 = IntAdP. Vmax( 8192 * 1.27 )
         AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg | 1; /* 積 分 停
電圧ベクトル補正値計算
                                                 <V531> VcmpV, VdOut', VcmpV, VgOut' = IntAdP, Vmax / IntAdV, V1 * VcmpV, VdOut, VcmpV, VgOut
//;*
<V537> 削 除
                      テ ー ブ ルアドレ ス取 得
         1wk2 = mul(AxisRscI->VcmpV.V12, AxisRscI->VcmpV.V12); /* TMP3, 2 = VcmpV.V12^2
         1 \text{wk2} = 1 \text{wk2} - 0 \text{x} 00400000; /* TMP3, 2 = IntAdV. V1^2 - 2^22
         1 \text{wk2} = 1 \text{wk2} >> 4; /* TMP3, 2 = (VcmpV. V12^2 - 2^22) / 2^4
         swk0 = (USHORT) (1wk2 >> 16); /* TMP0 = (VcmpV. V12^2 - 2^22) / 2^4 / 2^16 = addr
         1 \text{wk2} = 1 \text{wk2} & 0 \times 00000 \text{ffff}; /* \text{TMP2} = \{ (\text{VcmpV}, \text{V12}^2 - 2^2 \text{2}) / 2^4 \} & 0 \times 00000 \text{ffff} \}
         圧制限ベクトル直線補間用データ取得
         1 \text{wk4} = 65536; /* TMP5, TMP4 = 65536
         lwk6 = lwk4 - lwk2; /* TMP7,6 = 10000h - Table Index (Lo) -> (addr*2^16-low) */
         IxTblVlmt16(swk8, swk0); /* TMP8: テーブルデータ 読み出し(読み出し アドレスad dr) *//* tanaka21,コン
         1 \text{wk6} = (\text{ULONG}) \text{swk8} * 1 \text{wk6}; /* \text{TMP6} = \text{tb1rv}(\text{addr}) * (2^16-1 \text{ow})
         swk0 = swk0 + 1; /* TMP0 = addr+1
         IxTblVlmt16(swk8, swk0); /* TMP8: テーブルデータ読み出し(読み出しアドレスad dr+1) *//*
         tanaka21, コ ン パ イ ラ 対応待ち
         1 \text{wk4} = (\text{ULONG}) \text{swk8} * 1 \text{wk2}; /* \text{TMP4} = \text{tb1rv} (\text{addr+1}) *1 \text{ow}
         1 \text{wk} 0 = 1 \text{wk} 6 + 1 \text{wk} 4; /* TMP0 = tblrv(addr)*(2^16-10w) + tblrv(addr+1)*low */
/*
      電 圧 電 圧 ベ ク トル補正値 計算
                                                                                 */
```

```
swk8 = AxisRscI->VcmpV. Vmax2; /* TMP8 = VcmpV. Vmax2
                                                                            */
        1 \text{wk2} = \text{mulshr}((\text{ULONG}) \text{swk8}, 1 \text{wk0}, 28);
                                              /* TMP2 = MAC / 2^28
        AxisRscI->VcmpV. VdOut = mulshr(swk2, AxisRscI->VcmpV. VdOut, 14);
                                                                        /* VcmpV. VdOut = IntAdP. Vmax / VcmpV. V12 *
        VcmpV. VdOut * 2 (13+13+16) / 2 (28+14)
        AxisRscI->VcmpV. VqOut = mulshr(swk2, AxisRscI->VcmpV. VqOut, 14);
                                                                        /* VcmpV. VgOut = IntAdP. Vmax / VcmpV. V12 *
        VcmpV. VqOut * 2^{(13+13+16)} / 2^{(28+14)}
      else
        AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg & OxFFFE; /* 積 分 停 止 フ ラグクリア
    UVW transform : dq(2phase) to UVW(3phase) Transform
swk4 = AxisRscI->IntAdP.Vmax; /*
    swk1 = mulshr(AxisRscI->SinTbl.CosT, AxisRscI->VcmpV.VdOut, 14); /* TMP1 <-- ACC >> 14
    swk2 = mulshr(AxisRscI->SinTbl.SinT, AxisRscI->VcmpV.VqOut, 14); /* TMP2 <-- ACC >> 14
    AxisRscI->VcmpV. VuOut = sub limitf(swk1, swk2); /* VcmpV. VuOut <-- limit( VcmpV. VuOut , 2 15 - 1)
    AxisRscI->VcmpV. VuOut = IxLIMIT( AxisRscI->VcmpV. VuOut, swk4 ); /*
    VcmpV. VvOut = limit(SinTbl. CosT3 * VcmpV. VdOut / 2^14 - SinTbl. SinT3 * VcmpV. VqOut / 2^14 . 2^15 - 1)
    swk1 = mulshr(AxisRscI->SinTbl.CosT3, AxisRscI->VcmpV.VdOut, 14); /* TMP1 <-- ACC >> 14
     swk2 = mulshr(AxisRscI->SinTbl.SinT3, AxisRscI->VcmpV.VaOut, 14); /* TMP2 <-- ACC >> 14
    AxisRscI->VcmpV. VvOut = sub_limitf(swk1, swk2); /* VcmpV. VvOut <-- limit( VcmpV. VvOut , 2^15 - 1 )
    AxisRscI->VcmpV. VvOut = IxLIMIT(AxisRscI->VcmpV. VvOut, swk4); /*
    VcmpV. VwOut = limit( - VcmpV. VuOut - VcmpV. VvOut , 2<sup>15</sup> - 1)
    swk1 = (SHORT) ZEROR - AxisRscI->VcmpV. VuOut; /* VcmpV. VwOut <-- - VcmpV. VuOut - VcmpV. VvOut
```

```
/* VcmpV. VwOut <-- limit( VcmpV. VwOut , 2<sup>15</sup> - 1 )
    AxisRscI->VcmpV. VwOut = sub limitf(swk1, AxisRscI->VcmpV. VvOut);
    AxisRscI->VcmpV. VwOut = IxLIMIT(AxisRscI->VcmpV. VwOut, swk4); /*
#if 0 /* for debug 2013.07.17 tanaka21*/
飽和判断処理を ジャンプする */
                              <V537> 新
if( (AxisRscI->IntAdP. Ctr1Sw & V FB2) == 0 )
                              率 計算は2相
    Get modulation
1wk2 = mul (AxisRscI->VcmpV. VdOut, AxisRscI->VcmpV. VdOut);
     lwk2 = mac(AxisRscI->VcmpV.VqOut, AxisRscI->VcmpV.VqOut, lwk2);
     swk0 = MpSQRT(1wk2);
     if ( (USHORT) swk0 > 0x7FFF )
      swk0 = 0x7FFF; /* \( \sigma\) の 計 算が3 2 7 6 7 を 超えた ら 、32767にす る
                                                                   ; <V350> */
     AxisRscI \rightarrow IntAdV. V1 = swk0;
    飽 和 判断
                    <V531> <V537> 復 活
                                                  */
     AxisRscI->StsFlg. IntglFlg = AxisRscI->StsFlg. IntglFlg & OxFFFE;
     swk10 = AxisRscI->StsFlg. IntglFlg | 1;
     AxisRscI->StsFlg. IntglFlg = cmove ((AxisRscI->IntAdV. V1 >= 9421), swk10, AxisRscI->StsFlg. IntglFlg);
Over modulation type select
if (AxisRscI->IntAdP. Vmax >= 0x2000)
     if ( (AxisRscI->IntAdP. Ctr1Sw & OVMSEL2) == 0 )
      if ( AxisRscI \rightarrow IntAdV.V1 \ge 0x2000 ) && ( AxisRscI \rightarrow IntAdP.CtrlSw & OVMSEL1) != 0 )
```

```
Over modulation1
IxSetCtblAdr( pCtbl, &(OVMODTBLG[0][0]) ); /* gain type
          MpOVMMODK( &AxisRscI->IntAdP, &AxisRscI->IntAdV, pCtbl );
          AxisRscI->VcmpV. VuOut = mulshr limitf(AxisRscI->VcmpV. VuOut, AxisRscI->IntAdP. Kmod, 13);
          AxisRscI->VcmpV. VvOut = mulshr limitf(AxisRscI->VcmpV. VvOut, AxisRscI->IntAdP. Kmod, 13);
          AxisRscI->VcmpV. VwOut = mulshr limitf(AxisRscI->VcmpV. VwOut, AxisRscI->IntAdP. Kmod, 13);
/*
     TMP1 = | VcmpV. VuOut |,
                            TMP2 = |VcmpV.VvOut|
                                                   TMP3 = | VcmpV. VwOut |
                                                                              */
/*
     TMP4 = sign(VcmpV, VuOut), TMP5 = sign(VcmpV, VvOut), TMP6 = sign(VcmpV, VwOut)
/*-
          swk0 = 1;
          swk4 = IxLIMIT( AxisRscI->VcmpV. VuOut, swk0 );
          swk1 = swk4 * AxisRscI->VcmpV. VuOut;
          swk5 = IxLIMIT(AxisRscI-)VcmpV.VvOut. swk0);
          swk2 = swk5 * AxisRscI->VcmpV. VvOut;
          swk6 = IxLIMIT (AxisRscI->VcmpV. VwOut, swk0);
          swk3 = swk6 * AxisRscI->VcmpV. VwOut;
          if(swk1 \ge swk2)
            if ( swk1 \ge swk3 )
             swk1 = swk1 - 0x2000; /* TMP1 <-- | VcmpV. VuOut | -2000h
             IxLmtzImm16( swk1, 0x7fff ); /* zero limit
              swk0 = swk4 * swk1;
            else
              swk3 = swk3 - 0x2000; /* TMP0 < -- | VcmpV, VwOut | -2000h
              IxLmtzImm16( swk3, 0x7fff ); /* zero limit
              swk0 = swk6 * swk3;
          else
```

```
if(swk2) = swk3)
              swk2 = swk2 - 0x2000; /* TMP0 <-- | VcmpV. VvOut | -2000h
              IxLmtzImm16( swk2, 0x7fff ); /* zero limit
              swk0 = swk5 * swk2;
            else
              swk3 = swk3 - 0x2000; /* TMP0 <-- | VcmpV. VwOut | -2000h
              IxLmtzImm16( swk3, 0x7fff ); /* zero limit
              swk0 = swk6 * swk3;
          AxisRscI->VcmpV. VuOut = sub limitf(AxisRscI->VcmpV. VuOut, swk0);
          AxisRscI->VcmpV. VvOut = sub limitf(AxisRscI->VcmpV. VvOut, swk0);
          AxisRscI->VcmpV. VwOut = sub limitf(AxisRscI->VcmpV. VwOut, swk0);
                                                                          /*
          AxisRscI->IntAdV. Vcent = swk0;
     Over modulation2
else
        IxSetCtblAdr( pCtbl, &(OVMODTBLO[0][0]) ); /* ofset type
        MpOVMMODK( &AxisRscI->IntAdP, &AxisRscI->IntAdV, pCtbl);
     MAX = TMP1, MIN = TMP2
     OFS = (TMP1+TMP2)/2
        if ( AxisRscI->VcmpV. VuOut >= AxisRscI->VcmpV. VvOut )
          swk1 = AxisRscI->VcmpV. VuOut;
          swk2 = AxisRscI->VcmpV. VvOut;
         else
```

```
swk1 = AxisRscI->VcmpV. VvOut;
  swk2 = AxisRscI->VcmpV. VuOut;
if ( swk1 < AxisRscI->VcmpV. VwOut )
  swk1 = AxisRscI->VcmpV. VwOut;
else
  if (AxisRscI->VcmpV. VwOut < swk2)
    swk2 = AxisRscI->VcmpV. VwOut;
swk0 = add limitf(swk2, swk1); /*
swk0 = mulshr(swk0, ONE, 1);
AxisRscI->VcmpV. VuOut = sub_limitf(AxisRscI->VcmpV. VuOut, swk0);
                                                                         /*
AxisRscI->VcmpV. VvOut = sub limitf(AxisRscI->VcmpV. VvOut, swk0);
                                                                                                          */
                                                                         /*
AxisRscI->VcmpV. VwOut = sub_limitf(AxisRscI->VcmpV. VwOut, swk0);
                                                                         /*
AxisRscI->IntAdV. Vcent = swk0;
swk0 = 1;
swk0 = IxLIMIT(AxisRscI -> VcmpV. VuOut, swk0); /* TMP1 = -1/0/+1
                                                                                     */
swk1 = swk1 | 1; /* TMP1 = -1/+1 -----sign (VcmpV. VuOut)
                                                                             */
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
AxisRscI->VcmpV. VuOut = add limitf( swk2, AxisRscI->VcmpV. VuOut );
                                                                                                            */
swk1 = IxLIMIT( AxisRscI->VcmpV. VvOut, swk0 );
swk1 = swk1 \mid 1; /* sign(VcmpV, VvOut)
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
AxisRscI->VcmpV. VvOut = add limitf( swk2, AxisRscI->VcmpV. VvOut );
                                                                                                            */
swk1 = IxLIMIT( AxisRscI->VcmpV. VwOut, swk0 );
swk1 = swk1 \mid 1; /* sign(VcmpV. VwOut)
                                                                    */
swk2 = swk1 * AxisRscI->IntAdP. Kmod;
```

```
AxisRscI->VcmpV. VwOut = add limitf( swk2, AxisRscI->VcmpV. VwOut );
#endif //#if 0 /* for debug 2013.07.17 tanaka21*/
On-Delay
  IU, IV reference calc
    swk1 = mulshr(AxisRscI->WeakFV.IdOut, AxisRscI->SinTbl.CosT, 14); /* TMP1 <-- ACC >> 14
    swk2 = mulshr(AxisRscI->IntAdV, IaRef, AxisRscI->SinTbl, SinT, 14); /* TMP2 <-- ACC >> 14
    AxisRscI->IntAdV. IuOut = swk1 - swk2; /* IntAdV. IuOut <-- TMP1 - TMP2
    swk3 = mulshr(AxisRscI->WeakFV.IdOut, AxisRscI->SinTbl.CosT3, 14); /* TMP3 <-- ACC >> 14
    swk4 = mulshr(AxisRscI->IntAdV. IqRef, AxisRscI->SinTbl. SinT3, 14); /* TMP4 <-- ACC >> 14
    AxisRscI->IntAdV. IvOut = swk3 - swk4; /* IntAdV. IvOut <-- TMP3 - TMP4
if ( |IntAdV. IuInData | < IntAdP. OnDelayLvl ) TMP1 = IntAdV. IuOut /* Reference */
                     TMP1 = IntAdV. IuInData
     if ( |IntAdV.IvInData | < IntAdP.OnDelavLvl ) TMP2 = IntAdV.IvOut /* Reference */
                     TMP2 = IntAdV. IvInData
     if ( | IWD | < IntAdP. OnDelayLvl ) TMP2 = IWO /* Reference */
                      TMP2 = IWD
swk5 = AxisRscI->IntAdP.OnDelayLv1;
    if(LPX ABS(AxisRscI->IntAdV. IuInData) > LPX ABS(swk5)) //110530tanaka21作 業 メモ s w k 2 を 以 降 使 わ な い ため代入 は行
      swk1 = AxisRscI->IntAdV. IuInData; /* TMP1 <-- IntAdV. IuInData
    else
      swk1 = AxisRscI->IntAdV. IuOut; /* TMP1 <-- IntAdV. IuOut
    if(LPX_ABS(AxisRscI->IntAdV.IvInData) > LPX_ABS(swk5)) //110530tanaka21作 業 メモ
    swk2を 以 降 使 わ な い た め 代入は行なわない
```

```
swk2 = AxisRscI->IntAdV. IvInData; /* TMP2 <-- IntAdV. IvInData
else
  swk2 = AxisRscI->IntAdV. IvOut; /* TMP2 <-- IntAdV. IvOut
swk3 = -AxisRscI->IntAdV. IuInData - AxisRscI->IntAdV. IvInData; /* TMP3(IWD) <-- - TMP1 - TMP2
if(LPX_ABS(swk3) <= LPX_ABS(swk5)) //110530tanaka21作 業 メモ s w k 4 を 以 降 使 わ な い ため代入 は行なわない
 swk3 = -AxisRscI->IntAdV. IuOut - AxisRscI->IntAdV. IvOut; /* TMP3
swk7 = 0x2000; /* TMP7 < -- 2000h
swk5 = 1; /* TMP5 < -- 1
                                             */
if(IntAdP.OnDelaySlope != 0) trapezoid type else rectangle type
if( AxisRscI->IntAdP. OnDelaySlope == 0 )
TMP1(ONDVU) = sign(IU)*IntAdP.OnDelayComp
 swk6 = IxLIMIT(swk1, swk5); /* TMP6 = -1/0/+1
                                                            */
  swk1 = AxisRscI->IntAdP.OnDelayComp * swk6;
TMP2(ONDVU) = sign(IV)*IntAdP.OnDelayComp
                                                                        */
  swk6 = IxLIMIT(swk2, swk5);
 swk2 = AxisRscI->IntAdP.OnDelayComp * swk6;
TMP3(ONDVU) = sign(IW)*IntAdP.OnDelayComp
  swk6 = IxLIMIT(swk3, swk5);
 swk3 = AxisRscI->IntAdP. OnDelayComp * swk6;
trapezoid type
                                                    */
```

```
else
      swk0 = mulshr limitf(AxisRscI->IntAdP.OnDelaySlope, swk1, 8); /* TMPO <-- IU*IntAdP.OnDelaySlope>>8
      swk0 = IxLIMIT( swk0, 8192 ); /* TMP0 = limit(TMP0, 8192)
      swk1 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
                                                                                                      */
      swk0 = mulshr_limitf(AxisRscI->IntAdP.OnDelaySlope, swk2, 8); /* TMPO = limit(TMPO, 2^15-1)
      swk0 = IxLIMIT(swk0, 8192); /* TMP0 = limit(TMP0, 8192) */
      swk2 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
                                                                                                      */
      swk0 = mulshr_limitf(AxisRscI->IntAdP. OnDelaySlope, swk3, 8); /* TMP0 = limit(TMP0, 2^15-1)
                                                                                            */
      swk0 = IxLIMIT(swk0, 8192); /* TMP0 = limit(TMP0, 8192)
      swk3 = mulshr(AxisRscI->IntAdP.OnDelayComp, swk0, 13); /* TMP1(ONDVU) = (IntAdP.OnDelayComp*TMP0)>>13
                                                                                                      */
Voltage conversion to Carrier count range
-2000h...2000h ---> 0h...4000h ---> 0h...CRFRQ
AxisRscI->VcmpV. VuOut = IxLIMIT( AxisRscI->VcmpV. VuOut, swk7 ); /* limit +-2000h
    AxisRscI->VcmpV. VvOut = IxLIMIT( AxisRscI->VcmpV. VvOut, swk7);
    AxisRscI->VcmpV. VwOut = IxLIMIT( AxisRscI->VcmpV. VwOut, swk7 );
/* for debug */
    swk4 = swk7 - AxisRscI->VcmpV. VuOut;
    swk4 = mulshr(swk4, AxisRscI->IntAdV.CrFreqW, 14);
    swk5 = swk7 - AxisRscI->VcmpV. VvOut;
    swk5 = mulshr(swk5, AxisRscI->IntAdV.CrFreqW, 14);
    swk6 = swk7 - AxisRscI->VcmpV. VwOut;
    swk6 = mulshr(swk6, AxisRscI->IntAdV.CrFreqW, 14);
    Deat-time compensation (timer) : if (Vx == 0 \mid \mid Vx == IntAdV. CrFreqW) No compensation
```

```
if( ( swk4 != ZEROR ) && (swk4 != AxisRscI->IntAdV.CrFreqW ) )
       swk4 = swk4 - swk1; /* VcmpV. VuOut <-- VcmpV. VuOut+ONDVU
       IxLmtzReg16( swk4, swk4, AxisRscI->IntAdV.CrFreqW ); /* VcmpV.VuOut <-- limitz( VcmpV.VuOut , IntAdV.CrFreqW )
     if ( (swk5 != ZEROR ) && (swk5 != AxisRscI->IntAdV.CrFreqW ) )
       swk5 = swk5 - swk2; /* VcmpV. VvOut <-- VcmpV. VvOut+ONDVV */
       IxLmtzReg16( swk5, swk5, AxisRscI->IntAdV.CrFreqW); /* VcmpV.VvOut <-- limitz( VcmpV.VvOut , IntAdV.CrFreqW)
     if ( ( swk6 != ZEROR ) && (swk6 != AxisRscI->IntAdV.CrFreqW ) )
       swk6 = swk6 - swk3; /* VcmpV. VwOut <-- VcmpV. VwOut+ONDVW
       IxLmtzReg16( swk6, swk6, AxisRscI->IntAdV.CrFreqW); /* VcmpV.VwOut <-- limitz( VcmpV.VwOut , IntAdV.CrFreqW)
       */
     AxisRscI->PwmV. PwmCntT2 = swk6;
     AxisRscI \rightarrow PwmV. PwmCntT1 = swk5;
     AxisRscI->PwmV. PwmCntT0 = swk4;
     Output Voltage & status
                                                              */
//<2>#ifdef PREG DEF
// CTSTW = AxisRscI->StsFlg.CtrlStsRW; /* Status Set
   AxisRscI->CtrlStsOut = AxisRscI->StsFlg. CtrlStsRW; /* Status Set
#endif //#ifdef MULTI AXIS
                               /* 多 軸 処 理有 効
                                                                                   */
 /* Output PWM Data */
 SetPWM( &AxisHdl[0] );
```

```
/* ★ H/W ア ク セ ス が 共 通 の も の を ま と め た い !!O軸目って書くのが格好悪い★
 /* level(AD=3, INT1=0/4 HOST=0) */
 INTLVWR = 0x0004;
//<2>#ifdef PREG DEF
 OUTPT = 0x0;
 IniWk. IN WK1H++; /* for debug counter tanaka21 */
 return;
#if 0 /* JL086で 実 行 す る た め コメントアウト */
Encoder (SPGO) Interrupt Procedure ; 通 常 ( 初 期 イ ン ク レ パルス出力 完了時 ):11clk 〈V720〉 */
   [注 意]優 先 順 位 が 最 高 位 の 割 込 処 理 な の で、できるだけ 短い処理にすること。
                                                                             */
void MpIntEnc( void )
  if( EncIfV. IncPlsReq == 1 )
   PCVSO = EncIfV. DivPls. s[0]; /* パ ル ス 変 換 位置セット
  else if( EncIfV. PAOSegCmd != PAOPLSOUT )
   PCVSO = (SHORT) IHostWk. IncInitPls; /* パ ル ス 変 換 位置セット
  IEncWk. RxFlg0 = FCCST; /* SDM status bit8 : IEncWk. RxFlg0 (Serial-Enc0 receive flag) */
```

```
縮のため、使用しないデータの読込みはしない。
                                                                                */
  IEncWk. RxPos. s[0] = SRPGORD5; /* 今 回 値 読 込み: Position Low
  IEncWk. RxPos. s 1 = SRPGORD6; /* 今 回 値 読 込み: Position High
                      /* INT1 Acknowledge
  IEncWk. EncWk0 = INT1SET;
                                     */
               /* return
  return;
分周パルス更新処理
void MpUPDATE DIVPOS (void)
  IHostWk. Divuswk = INT1SET;
                      /* INT1 Acknowledge
  IHostWk. LastRcvPosX = EncIfV. RcvPosX0.1; /* 前 回 位 置 データ更新
    シ リ ア ル エ ン コ ー ダ受信 チェック ; IEncWk. RxFlg0の 値 は@INT_E N C 割 込 に て更新
    Divuswk = IEncWk. RxFlg0; /* SDMSTS bit8 : SPG0 Recieve Completed Check */
  if ( (IEncWk. RxFlg0 \& 0x100 ) == 0 )
    if (EncIfV. SPGFail >= IHostWk. EncMstErrCnt)
     EncIfV. RcvPosX2. 1 = EncIfV. RcvPosX1. 1; /* 前 々 回 位 置データ
     EncIfV. RcvPosX1. 1 = EncIfV. RcvPosX0. 1; /* 前 回 位 置 データ
     EncIfV. RcvPosX0. 1 = EncIfV. RcvPosX0. 1 + EncIfV. RcvPosX1. 1; /* 補 間 演算
     EncIfV. RcvPosX0. 1 = EncIfV. RcvPosX0. 1 - EncIfV. RcvPosX2. 1; /* EncIfV. RcvPosX0 += (EncIfV. RcvPosX1 - EncIfV. RcvPosX2)
     */
```

```
IHostWk. EncMstErrCnt++; /* IHostWk. EncMstErrCnt++
   else
     IHostWk. RxPos0 = IEncWk. RxPos. 1; /* 今 回 値 更新: IEncWk. R x P osの値は@ I N T E N C 割込にて更 新 */
     IHostWk.RcvPosX = MencP.MposSign * ((MencV.RxPosL[0].s1>>MencP.MposSftX)
     32bitト 位 詰 め デ ー タ の た め 、 論 理 シ フ ト にて計算(符号 ビットの影響なし)
     IHostWk. RcvPosX = ( IHostWk. RxPosO >> EncIfV. MotPosSftX ) << EncIfV. MotPosSftR; /* IHostWk. RcvPosX = (ULONG) DivWkO <<
     EncIfV. MotPosSftR */
     IHostWk. RcvPosX = IHostWk. RcvPosX * EncIfV. MotPosSign
     if( EncIfV. MotPosSign != 1 )
       IHostWk. RcvPosX = ~IHostWk. RcvPosX;
       IHostWk, RcvPosX = IHostWk, RcvPosX + ONER; /* IHostWk, RcvPosX = -IHostWk, RcvPosX
     if( DivPlsV. AccCntClrReq != 0 )
       IHostWk. Divuswk = ~EncIfV. BitData; /* DivWkO=~EncIfV. BitData
       IHostWk. Divuswk = IHostWk. Divuswk | ACCCHKENA; /* DivWkO. ACCCHKENA = TRUE
       EncIfV. BitData = "IHostWk. Divuswk; /* EncIfV. BitData="DivWk0"
       IHostWk. AccChkCnt = 0; /* IHostWk. AccChkCnt = 0
       DivPlsV. AccCntClrReq = 0; /* 加 速 度 チ ェ ッ ク 開 始 カ ウ ントクリア要求 リ
//
       Divuswk = EncIfV. BitData;
     if ( (EncIfV. BitData & ACCCHKENA ) == 0 )
```

```
THostWk. MotAcc = ZEROR;
                          /* IHostWk. MotAcc = 0
  IHostWk. AccChkCnt++;
                           /* IHostWk. AccChkCnt++
                                                                      */
  if ( IHostWk. AccChkCnt >= 4 )
                                                    /* EncIfV. BitData. ACCCHKENA = TRUE
    EncIfV. BitData = EncIfV. BitData | ACCCHKENA;
 EncIfV. RcvPosX0. 1 = IHostWk. RcvPosX; /* EncIfV. RcvPosX0 = IHostWk. RcvPosX
  EncIfV. RevPosX1. 1 = IHostWk. RevPosX; /* EncIfV. RevPosX1 = IHostWk. RevPosX
  EncIfV. RevPosX2. 1 = IHostWk. RevPosX; /* EncIfV. RevPosX2 = IHostWk. RevPosX
else
  IHostWk, DivWk0 = IHostWk, RcvPosX - EncIfV, RcvPosX0, 1; /* DivWk0 = IHostWk, RcvPosX - EncIfV, RcvPosX0
  IHostWk. DivWk1 = EncIfV. RcvPosX0. 1 - EncIfV. RcvPosX1. 1; /* DivWk1 = EncIfV. RcvPosX0 - EncIfV. RcvPosX1
  IHostWk, MotAcc = IHostWk, DivWk0 - IHostWk, DivWk1; /* IHostWk, MotAcc = DivWk0 - DivWk1
  if (EncIfV. AccErrLv. 1 >= IHostWk. MotAcc)
   if ( EncIfV. AccErrLv. 1 + IHostWk. MotAcc ) < 0 )
DivWkO = (IHostWk. RcvPosX - EncIfV. RcvPosX1) >> 1
      IHostWk. DivWk0 = IHostWk. RcvPosX - EncIfV. RcvPosX1. 1; /* DivWk0 = IHostWk. RcvPosX - EncIfV. RcvPosX1
      IHostWk. DivWkO = IHostWk. DivWkO & Oxfffffffe; /* 算 術 右 シ フ ト の 四 捨 五入無効化の対策
      IHostWk, DivWk0 = IlibASR32(IHostWk, DivWk0 , 1); /* DivWk0 = (IHostWk, RevPosX - EncIfV, RevPosX1) >> 1
      IHostWk. DivWk1 = EncIfV. RcvPosX1. 1 - EncIfV. RcvPosX2. 1; /* DivWk1 = EncIfV. RcvPosX1 - EncIfV. RcvPosX2
      IHostWk. MotAcc = IHostWk. DivWk0 - IHostWk. DivWk1; /* IHostWk. MotAcc = DivWk0 - DivWk1
  else
DivWk0 = (IHostWk.RcvPosX - EncIfV.RcvPosX1) >> 1
    IHostWk. DivWk0 = IHostWk. RcvPosX - EncIfV. RcvPosX1. 1; /* DivWk0 = IHostWk. RcvPosX - EncIfV. RcvPosX1
   IHostWk. DivWk0 = IHostWk. DivWk0 & Oxfffffffe; /* 算 術 右 シ フ ト の 四 捨 五入無効化の対策
```

```
IHostWk. DivWk0 = IlibASR32(IHostWk. DivWk0 , 1); /* DivWk0 = (IHostWk. RevPosX - EncIfV. RevPosX1) >> 1
                                                                                                                      */
     IHostWk. DivWk1 = EncIfV. RcvPosX1. 1 - EncIfV. RcvPosX2. 1; /* DivWk1 = EncIfV. RcvPosX1 - EncIfV. RcvPosX2 */
     IHostWk, MotAcc = IHostWk, DivWk0 - IHostWk, DivWk1; /* IHostWk, MotAcc = DivWk0 - DivWk1
  if (EncIfV. AccErrLv. 1 >= IHostWk. MotAcc)
    if (EncIfV. SPGFail < IHostWk. EncMstErrCnt)
     EncIfV. RcvPosX2. 1 = EncIfV. RcvPosX1. 1; /* 前 々 回
     EncIfV. RevPosX1. 1 = EncIfV. RevPosX0. 1; /* 前 回 位
      EncIfV. RcvPosX0. 1 = IHostWk. RcvPosX; /* 加 速 度 異
                              /* IHostWk.EncMstErrCnt++
      IHostWk. EncMstErrCnt++;
  else if ( EncIfV. AccErrLv. 1 + IHostWk. MotAcc ) < 0 )
                                /* IHostWk. EncMstErrCnt=0
    IHostWk. EncMstErrCnt = 0;
   EncIfV. RcvPosX2. 1 = EncIfV. RcvPosX1. 1; /* 前 々
   EncIfV. RcvPosX1. 1 = EncIfV. RcvPosX0. 1; /* 前 回 位
   EncIfV. RcvPosX0. 1 = IHostWk. RcvPosX; /* 今 回
 dMotPos = RMX dPosOfXpos( MencV. MotPosX[0], LastMotPosX );
                                     り捨てられる下位ビ
IHostWk. DMotPos = EncIfV. RcvPosX0. 1 - IHostWk. LastRcvPosX; /* IHostWk. DMotPos = EncIfV. RcvPosX0 - IHostWk. LastRcvPosX */
IHostWk. DMotPos = IlibASR32(IHostWk. DMotPos , EncIfV. MotPosSftR);
```

```
if( EncIfV. IncPlsReg == 1 )
 EncIfV. PlsOSetCmd = DivPlsV. PlsOSetCmdIn; /* パ ル ス 出 力 回 路 初期化要求更新 from H os t C PU */
 if( EncIfV.PlsOSetCmd == POSETCMD00 )
   PCVS0 = 0x0000;
   DivPlsV. PlsOSetCmdIn = POSETNOCMD; /* 初 期 化 要 求クリア
                                                                             */
 else if (EncIfV. PlsOSetCmd == POSETCMDFF)
   PCVSO = 0xFFFF;
   DivPlsV.PlsOSetCmdIn = POSETNOCMD; /* 初 期
                                                                             */
 else
   IHostWk. IncInitPls = DivPlsV. IncInitPlsIn. 1; /*
   EncIfV. DivPls. 1 = DivPlsV. IncInitPlsIn. 1; /*
   EncIfV. DivPos. 1 = DivPlsV. IncInitPlsIn. 1; /* for Linear
   EncIfV. DivPlsRem. 1 = DivPlsV. IncInitRemIn. 1; /* for Linear
else
 if ( IHostWk. PoSet1W != DivPlsV. PoSet1In )
   IHostWk.PoSet1W = DivPlsV.PoSet1In;
   IHostWk. PoSet2W = DivPlsV. PoSet2In;
                          /* パ ル ス
   PCVS1 = IHostWk.PoSet1W;
                                                                        ( H o s tCPUと 同 じ 状態に設 定) */
                                            変
                           /* パ ル ス
                                                換原 点補正2セット
   PCVS2 = IHostWk.PoSet2W;
if ( IHostWk. DivSetW != DivPlsV. DivSetIn )
 IHostWk. DivSetW = DivPlsV. DivSetIn;
 DivSet = IHostWk.DivSetW;
                          /* 分 周 機 能 セット (Host CPUと 同じ状態に 設 定 )
```

```
if( EncIfV. IncPlsReq != 1 )
     if( EncIfV. AmpType != LINEAR )
     分 周 パルス = (MencV. MotPosX[0] >> MencP. EncIfV. DivOutSft);
                       ト に て 切 り 捨 て られ る 下 位 ビットを0にする
       IHostWk. DivWk1 = NONER << EncIfV. DivOutSft; /* DivWk1=(FFFFFFFFK<EncIfV. DivOutSft)</pre>
       EncIfV. DivPls. 1 = IlibASR32(IHostWk. DivWkO , EncIfV. DivOutSft); /*
       EncIfV. DivPls=((EncIfV. RcvPosX0&(FFFFFFFh<(EncIfV. DivOutSft))>>EncIfV. DivOutSft */
     else
       DivPlsV. Argu0. 1 = IHostWk. DMotPos; /* DivPlsV. Argu0 <-- IHostWk. DMotPos
       DivPlsV. Argul. 1 = EncIfV. DivOutGain. 1; /* DivPlsV. Argul <-- EncIfV. DivOutGain
       DivPlsV. Iu0. 1 = EncIfV. DivPlsRem. 1; /* DivPlsV. Iu0 <-- EncIfV. DivPlsRem
       MpMlibPfbkxremNolim();
                                 /* DivPlsV.Ret0 = MLIBPFBKXREMNOLIM()
       EncIfV. DivPos. 1 = EncIfV. DivPos. 1 + DivPlsV. Ret0. 1; /* EncIfV. DivPos = EncIfV. DivPos + DivPlsV. Ret0
       EncIfV. DivPlsRem. 1 = DivPlsV. Iu0. 1; /* EncIfV. DivPlsRem <-- DivPlsV. Iu0
       EncIfV. DivPls. 1 = EncIfV. DivPos. 1; /* EncIfV. DivPls = EncIfV. DivPos
                                                           レ パ ル ス出力要求更新 from HostCPU */
   EncIfV. IncPlsReg = DivPlsV. IncPlsRegIn; /* 初 期 イ ン ク
   EncIfV. PAOSegCmd = DivPlsV. PAOSegCmdIn; /*
                   /* return
   return;
#endif //#if 0 /* JL086で 実 行 す る た め コメントアウト
     DATA clear subroutin
/*
```

```
void MpDataClear( MICRO AXIS HANDLE *AxisRsc )
      HOST int clear<1.02>
                                                              */
                                                          ; <V388> 追
                                                                       加
 AxisRsc->IntAdV. IgOut1L. 1 = ZEROR;
                                                          ; <V388> 追
                                                                       加
 AxisRsc->IntAdV. IgOut1PL. 1 = ZEROR; /*
 AxisRsc->IntAdV. IgOut1PPL. 1 = ZEROR; /*
                                                            ; <V388> 追 加
 AxisRsc->IntAdV. IqIn1PL. 1 = ZEROR;
                                                            〈V388〉 i自
                                                                       加
 AxisRsc->IntAdV. IqIn1PPL. 1 = ZEROR; /*
                                                            <V388> i自
                                                                       加
 AxisRsc->IntAdV. IaOut2L. 1 = ZEROR;
                                                          ; <V388> i自
                                                                       加
                                                          ; <V388> i自
 AxisRsc->IntAdV. IgOut2PL. 1 = ZEROR; /*
                                                                       加
 AxisRsc->IntAdV. IgOut2PPL. 1 = ZEROR; /*
                                                            ; <V388> i自 加
 AxisRsc->IntAdV. IqIn2PL. 1 = ZEROR;
                                                          ;〈V388〉 追
                                                                       加
                                                          ; <V388> 追
 AxisRsc->IntAdV. IqIn2PPL. 1 = ZEROR; /*
                                                                       加
                                                           〈V388〉 i自
                                                                       加
 AxisRsc->IntAdV. IaOut3L. 1 = ZEROR;
 AxisRsc->IntAdV. IgOut3PL. 1 = ZEROR; /*
                                                            〈V388〉追
                                                            ; 〈V388〉追 加
 AxisRsc->IntAdV. IgOut3PPL. 1 = ZEROR; /*
 AxisRsc->IntAdV. IqIn3PL. 1 = ZEROR; /*
                                                          ; 〈V388〉 追
                                                                       加
 AxisRsc->IntAdV. IqIn3PPL. 1 = ZEROR; /*
                                                          ; <V388> 追 加
 AxisRsc->AcrV. IdIntgl. 1 = ZEROR; /* integral(32bit) <-- 0
 AxisRsc->AcrV. IqIntgl. 1 = ZEROR; /* integral (32bit) <-- 0
                                                                            */
 AxisRsc->AcrV. VdFil. 1 = ZEROR; /* vd filter out(32bit) <-- 0
                                                                            */
 AxisRsc->AcrV. VaFil. 1 = ZEROR; /* va filter out(32bit) <-- 0
 AxisRsc->IntAdV. IqOut2Lpf. 1 = ZEROR; /* iq filter out(32bit) <-- 0
 AxisRsc->IntAdV. IaRef = 0x0;
                                 /* ig(after limit) <-- 0
 AxisRsc->VcmpV. VdOut = 0x0;
                                  /* vd <-- 0
 AxisRsc->VcmpV. VqOut = 0x0;
                                  /* vg <-- 0
                                  /* vu <-- 0
 AxisRsc \rightarrow VcmpV. VuOut = 0x0;
 AxisRsc \rightarrow VcmpV. VvOut = 0x0;
                                  /* vv <-- 0
                                  /* vw <-- 0
 AxisRsc \rightarrow VcmpV. VwOut = 0x0;
 AxisRsc \rightarrow VcmpV. LdC = 0x0;
 AxisRsc \rightarrow VcmpV. LqC = 0x0;
 AxisRsc->VcmpV. MagC = 0x0;
 AxisRsc \rightarrow IntAdV. IuOut = 0x0;
```

```
AxisRsc->IntAdV. IvOut = 0x0;
 AxisRsc->IntAdV. IdDataP = AxisRsc->IntAdV. IdInData; /*
 AxisRsc->IntAdV. IqDataP = AxisRsc->IntAdV. IqRef;
 AxisRsc->WeakFV. IdOut = 0;
 AxisRsc \rightarrow VcmpV. VdOut = 0;
                       /*
 AxisRsc->VcmpV. VqOut = 0;
 AxisRsc->IntAdV. IdLfil. 1 = ZEROR; /*
 AxisRsc->IntAdV. IqLfil. 1 = ZEROR; /*
 AxisRsc->WeakFV.WfIntgl.1 = ZEROR; /* <V214>
                                                       活<V531> */
 AxisRsc->WeakFV. WfVdRef = 0; /* \langle V214 \rangle
                                              除〈V309〉
 AxisRsc->WeakFV. WfVqRef = 0; /* \langle V214 \rangle
                                              除<V309>
                                                        活〈V531〉 */
 return;
/*
     SQRT (TMP2(32)) Sub-routin (MAX 1.21us)
/*
/*
Input TMP2 : Low data
        TMP3 : High data
    Output TMPO : SQRT(dat)
    Stack No. 0
    Work TMP0, TMP1, TMP2, TMP3, TMP4, TMP5, TMP8
        MACCL, MACCH, SACCL, SACCH
//USHORT MpSQRT( INTADWK *IntAdwk, ULONG src )
#if 0
USHORT MpSQRT(ULONG src) /* 2013.05.06 tanaka21 コ ー ド 整理<020>
  USHORT Low; /* 引 数 下位16 bit值
                                  2013.05.06 tanaka21 コ ー ド 整理<020>
```

```
2013.05.06 tanaka21 コ ー ド 整理<020>
   USHORT High;
                 /* 引 数 上位16 bit值
   USHORT uswk0;
                   /* 平 方 根 演算用 1 6 b i t ワ ークレジスタ0
                                                                2013.05.06 tanaka21 コ ー ド 整理<020>
                    /* 平 方 根 演算用 1 6 b i t ワ ークレジ スタ1
    USHORT uswk1;
                                                                   2013.05.06 tanaka21 コ ー ド 整理<020>
  メ ン ト アウト
                   ( u swk0と統合) <022>
                                        */
                             根
                                演算用 16 b i t ワ ークレジスタ3
   USHORT uswk3;
                    /* 平
                         方
                                                                2013.05.06 tanaka21 =
                         方根
                   /* 平
                                      16 b i t ワ ークレジスタ4
   USHORT
         uswk4;
                                演算用
                                                                2013.05.06 tanaka21 = -
                                                                                                     */
                   /* 平 方 根
                                                                2013.05.06 tanaka21 ⊐ —
   USHORT uswk5;
                                                                                       ド 整理(020)
                                演算用 16bit ワ ークレジスタ5
                                                                                                     */
                   /* 平 方 根 演算用 1 6 b i t ワ ークレジスタ6
                                                                2013.05.06 tanaka21 コ ー ド 整理<020>
   USHORT uswk6;
                                                                                                     */
                  /* 平 方 根 演算用 3 2 b i t ワ ークレジスタ0
   ULONG ulwk0;
                                                              2013.05.06 tanaka21 コ ー ド 整理〈020〉
                 /* 平 方 根 演算用 3 2 b i t ワ ークレジ スタ2
   ULONG ulwk2;
                                                                 2013.05.06 tanaka21 = -
コ メ ン ト アウト ( u swk0と統合) <022>
                /* 平 方 根 演算用16/32bitワークレジスタ0
   DWREG tmp0;
                                                              2013.05.06 tanaka21 コ ー ド 整理〈020〉
   Low = (USHORT) src;
   High = (USHORT) (src >> 16);
    TMP0(16) = sart(TMP2(32))
                                                     */
    TMP3 (High), TMP2 (Low) ---> TMP0 (result)
    table search from high 8bits
    and closely resemble using low 15 bits
                23
                    19
                        15
                         6
                                 10
    uswk6 = 0; /* 2013.05.06 tanaka21 コ ー ド 整理<0 20>
                                                         */
   if ( High & 0xF000 )
     TMP8 0
                                               */
/*
     | xxxx | yyyy | aaaa | aaaa | aaaa | aaa- | ------
                                                            */
    uswk6 = 0; /* 2013.05.06 tanaka21 コ ー ド整理〈020〉
    tmp0.ul = (src >> 9); /* TMP4 for approxmate (15bit)
    tmp0. us[0] = (tmp0. us[0] & 0x7FFF); /* mask 15bit
                                                                   */
    uswk5 = (High >> 8); /* TMP5 for table search(8bit)
```

```
else if (High & OxOFOO)
     TMP8 2
                                                    */
     /*
     uswk6 = 2;
     tmp0.ul = ( src >> 5 ); /* TMP4 for approximate(15bit)
     tmp0. us[0] = (tmp0. us[0] & 0x7FFF); /* mask 15bit
     uswk5 = (High \gg 4); /* TMP5 for table search(8bit)
   else if (High & Ox00F0)
     TMP8 4
     uswk6 = 4;
     uswk5 = High; /* TMP5 for table search(8bit)
     tmp0. us [0] = (Low >> 1); /* TMP4 for approximate (15bit)
   else if (High & Ox000F)
                                                    */
/*
     | 0000 | 0000 | 0000 | xxxx | yyyy | aaaa | aaaaaaaa | (000)
     uswk6 = 6;
     uswk5 = (USHORT) (( src & 0x0FFFF000 ) >> 12); /* TMP5 for table search(8bit)
     tmp0.ul = ( src << 4 ); /* TMP5 for table search(8bit)</pre>
     tmp0. us[0] = (tmp0. us[0] >> 1); /* TMP4 for approximate(15bit)
     tmp0. us[0] = (tmp0. us[0] & 0x7FFF); /* mask 15bit
   else if ( Low & 0xF000 )
     TMP8 8
                                                    */
```

```
uswk6 = 8;
      uswk5 = (Low >> 8); /* TMP5 for table search (8bit)
      uswk4 = (Low & 0x0FF);
      tmp0. us \begin{bmatrix} 0 \end{bmatrix} = ( uswk4 \langle \langle 7 \rangle); /* TMP4 for approximate (15bit)
                                                                                   */
    else if (Low & 0x0F00)
      TMP8 10
      | 0000 | 0000 | 0000 | 0000 | 0000 | xxxx | yyyyaaaa | (00000000000)
      uswk6 = 10;
      uswk5 = (Low >> 4); /* TMP5 table search (8bit)
      uswk4 = (Low & 0x00F);
      tmp0. us \begin{bmatrix} 0 \end{bmatrix} = \begin{pmatrix} uswk4 & \langle 11 \rangle \end{pmatrix}; /* TMP4 approximate (15bit)
                                                                               */
else
      uswk6 = 12;
      IxTblSqrt16( (uswk0), Low ); /* TMP0 = table data
                                                                             */
     table read and approximate
                                                                    */
      TMP5 (High), TMP4 (Low)
    if ( uswk6 < 12 )
      IxTblSqrt16( (uswk3), uswk5 ); /* TMP3 <-- tbl[tmp]</pre>
      if ( uswk5 == 0x00FF )
        uswk0 = 0xFFFF; /* TMP0 <-- (tb1[tmp+1])
      else
```

```
uswk5 = uswk5 + 1;
       IxTblSqrt16( (uswk0), uswk5 ); /* TMP0 <-- tbl[tmp+1]</pre>
     (tb1[tmp+1] - tb1[tmp])*1ow/32768 + tb1[tmp]
     uswk4 = uswk0 - uswk3;
//<022> uswk1 = (USHORT)IlibASR32(( (LONG)uswk4 * (LONG)tmp0.us[0] ) , 15);
          uswk0 = uswk1 + uswk3; /* TMP0 = read data */
//<022>
     uswk0 = (USHORT)IlibASR32(( (LONG)uswk4 * (LONG)tmp0.us[0] ) , 15);
     uswk0 = uswk0 + uswk3; /* TMP0 = read data
   Scaling
//\langle 022\rangle ulwk2 = (ULONG) (uswk0);
//\langle 022\rangle ulwk0 = (ulwk2 \rangle uswk6);
   ulwk0 = ((ULONG)(uswk0) >> uswk6);
   return((USHORT)ulwk0);
#else
//<3> start
inline USHORT MpSQRT( ULONG src )
 USHORT uswk0;
 ULONG ulwk0;
 ULONG ulwk2;
 uswk0 = sqrt(src); // 結 果 は 小 数 点 以 下は切り捨て
       ulwk2 = mul((SHORT)uswk0, (SHORT)uswk0); // 平 方 根 の 結 果を自乗
                            // 入 力 と 自 乗 の 差 を 取る(切捨て誤差)
       u1wk2 = src - u1wk2;
       u1wk0 = (ULONG)uswk0;
 if( uswk0 < 0xffff ) {</pre>
                              // 最 大 値 を 超 え る 場 合 は切捨ての補正なし
// 切 捨 て 誤 差 が 平 方 根 の 結 果より大きい場 合 補 正
   if ( ulwk0 < ulwk2 ) {
     uswk0 = uswk0 + 1;
```

```
return (uswk0);
//\langle 3 \rangle end
#endif
/*
    Over modulation compasation calculation
/*
TMP4: table address, IntAdV.V1:modulation
    OUTPUT: Kmod: compensation gain/offset
   work:
           TMP0, TMP1, TMP2, TMP3
//void MpOVMMODK(INTADP *IntAdP, INTADV *IntAdV, INTADWK *IntAdwk)
inline void MpOVMMODK(INTADP *IntAdP, INTADV *IntAdV, CSHORT* pCtbl) /* 2013.05.06 tanaka21 コ ー ド整理<020>
 SHORT swk0;
               /* 16bitワ ー ク レ ジスタ0
                                          2013.05.06 tanaka21 コ ー ド 整理〈020〉
               /* 16bitワ ー ク レ ジスタ1
                                          2013.05.06 tanaka21 コ ー ド 整理〈020〉
 SHORT swk1;
               /* 16bitワ ー ク レ ジスタ2
                                          2013.05.06 tanaka21 コ ー ド整理<020>
 SHORT swk2;
               /* 16bitワ ー ク レ ジスタ3
 SHORT swk3;
                                          2013.05.06 tanaka21 コ ー ド 整理〈020〉
           swk4; //\langle 2 \rangle
      SHORT
 if (IntAdV->V1 < 9459)
//<2> IxLoadMomem16(IntAdP->Kmod, pCtbl, 0); /* IntAdP->Kmod = G[0];
   IxLoadMpmem16 ( swk4, pCtb1, 0 ); /* IntAdP->Kmod = G[0];
 else if ( (IntAdP->CtrlSw & OVMMOD) == 0 )
   pCtb1 = pCtb1 + 15;
//<2> IxLoadMpmem16(IntAdP->Kmod, pCtbl, 1);
   IxLoadMpmem16( swk4, pCtbl, 1 );
 else
```

```
if ( IntAdV->V1 < 10431 )
      swk0 = IntAdV -> V1;
      swk0 = swk0 - 9443; /* -9439-5 (margin)
                                                                 */
      swk1 = swk0;
      swk0 = swk0 >> 5; /* high
                                                       */
      swk1 = swk1 & 0x1F; /* 1ow
      if ( swk0 >= 32 )
       pCtb1 = pCtb1 + 15;
//<2>
           IxLoadMpmem16( IntAdP->Kmod, pCtbl, 1 );
        IxLoadMpmem16( swk4, pCtbl, 1 );
      else
        swk2 = swk0;
        swk0 = swk0 >> 1;
        if((swk2 & 1) == 0)
         pCtb1 = pCtb1 + swk0;
         IxLoadMpmem16( swk2, pCtbl, 0 );
         IxLoadMpmem16( swk3, pCtbl, 1 );
        else
         pCtb1 = pCtb1 + swk0;
         IxLoadMpmem16( swk2, pCtbl, 1 );
          pCtb1 = pCtb1 + 1;
         IxLoadMpmem16( swk3, pCtbl, 0 );
        swk0 = swk3 - swk2;
/* 2012.10.05 Y.0ka 変 換 前は% s h rなのでIli b A SR32では ? */
         swk0 = IlibASR16(swk0 * swk1, 5);
           swk0 = (SHORT) I1 ibASR32 ((LONG) swk0 * (LONG) swk1, 5);
//<1>
        swk0 = mulshr(swk0, swk1, 5);
/* 2012.10.05 Y.0ka 変 換 前は% s h rなのでIli b A SR32では ? */
```

```
//\langle 2 \rangle IntAdP->Kmod = swk0 + swk2;
    swk4 = swk0 + swk2;
   else
    pCtb1 = pCtb1 + 15;
//<2> IxLoadMpmem16(IntAdP->Kmod, pCtbl, 1);
    IxLoadMpmem16( swk4, pCtbl, 1 );
 IntAdP \rightarrow Kmod = swk4;
 return;
#if 0
    制御演算ライブラリ
/*
                                         */
              き 位 置FB計算: rv = (kx*u+pfbrem)>>sx ; ??clk
                                                                    <V720> */
//LONG MpMlibPfbkxremNolim(
                       /* DivPlsV. Argu0 : 入 力
/* DivPlsV. Argu1 : ゲ イン
    LONG u,
/*
    LONG k,
                          /* DivPlsV. Iu0 : 余りへのポインタ
/*
    LONG *pfbrem )
/*-
                    /* DivPlsV.Ret0
                                  : 戻り値
/*
                                     : kx
/*
    LONG kx
                       /* DivPlsV.Kx
                                                      */
/*
    LONG sx
                       /* DivPlsV.Sx : sx
                                                      */
                       /* lswk10 : 演 算 結果
    LONG rv
```

```
/* lswk11 : 余
      LONG pfbrem
                                                       業用
      LONG wk1
                                  /* 1swk1
                                                                              */
                                             · · · 作 算 算
                                                     業結結
       LONG wk2
                                  /* 1swk2
                                                          果保果
                                         /*
                              /* 1swk3
/*
                              /* 1swk4
void MpMlibPfbkxremNolim( void )
    DivPlsV. Kx. 1 = DivPlsV. Argul. 1 \langle \langle 8 \rangle /* DivPlsV. Kx = k\langle \langle 8 \rangle
    DivPlsV. Sx. 1 = DivPlsV. Argul. 1 \Rightarrow 24; /* DivPlsV. Sx = k\Rightarrow24
    IPfbwk. 1swk1 = 24;
                                 /* 1swk1 = 24
                                                                       */
    if (IPfbwk. lswk1 >= DivPlsV. Sx. 1)
         IPfbwk. dlwk. dl = DivPlsV. Argu0. 1 * DivPlsV. Kx. 1;
      IPfbwk. dlwk. 1[0] = DivPlsV. Argu0. 1 * DivPlsV. Kx. 1; //provision
       IPfbwk. lswk1 = IPfbwk. lswk1 - DivPlsV. Sx. 1; /* lswk1 = 24 - sx
      IPfbwk. lswk2 = IPfbwk. dlwk. 1[0] >> DivPlsV. Sx. s[0]; /* lswk2 = (x1>>sx)
       IPfbwk. 1swk2 = IPfbwk. 1swk2 >> 8; /* 1swk2 = ((x1)>sx)>>8
      IPfbwk. lswk10 = IPfbwk. dlwk. 1 \lceil 1 \rceil  << IPfbwk. lswk1; /* lswk10 = (xh<<(24-sx))
      IPfbwk. 1swk10 = IPfbwk. 1swk10 + IPfbwk. 1swk2; /* 1swk10 = ((xh < (24-sx)) + ((x1>>sx)>>8))
      IPfbwk. lswk11 = IPfbwk. dlwk. l\begin{bmatrix} 0 \end{bmatrix} << IPfbwk. lswk1; /* lswk11 = (x1<<(24-sx))
       IPfbwk. 1swk11 = IPfbwk. 1swk11 >> 8; /* 1swk11 = ((x1<<(24-sx))>>8)
       IPfbwk. lswk11 = IPfbwk. lswk11 + DivPlsV. Iu0. l;
    else
         IPfbwk. dlwk. dl = DivPlsV. Argu0. 1 * DivPlsV. Kx. 1;
       IPfbwk, dlwk, 1 0 = DivPlsV, Argu0, 1 * DivPlsV, Kx, 1; //provision
       IPfbwk. 1swk3 = IPfbwk. dlwk. 1\begin{bmatrix} 0 \end{bmatrix}; /* 1swk3 = x1
      IPfbwk. 1swk4 = IPfbwk. d1wk. 1[1];
                                              /* 1swk4 = xh
      IPfbwk. 1swk1 = DivP1sV. Sx. 1 - IPfbwk. 1swk1; /* 1swk1 = sx - 24
```

```
捨 て ら れ る 下 位 ビッ ト を0にする(四捨 五入無効化対策)
      IPfbwk. lswk2 = NONER << IPfbwk. lswk1; /* lswk2 = (FFFFFFFFK<((sx-24))
      IPfbwk. lswk2 = IPfbwk. lswk4 & IPfbwk. lswk2; /* lswk2 = (xh & (FFFFFFFFFK<((sx-24)))
                                                                                               */
//#ifdef WIN32
      IPfbwk. lswk10 = (LONG) ((INT64) IPfbwk. lswk2 \rightarrow IPfbwk. lswk1); /* lswk10 = (xh\rightarrow)(sx-24))
//#elif defined(ASIP CC)
        IPfbwk. lswk10 = asr( IPfbwk. lswk2, IPfbwk. lswk1); /* lswk10 = (xh) (sx-24))
//#endif
      IPfbwk. 1swk11 = IPfbwk. 1swk3 >> IPfbwk. 1swk1; /* 1swk11 = (x1>>(sx-24))
                                                                                             */
      IPfbwk. lswk11 = IPfbwk. lswk11 \Rightarrow 7; /* lswk11 = ((x1)>(sx-24))>>7)
      IPfbwk. lswk11 = IPfbwk. lswk11 + ONER; /* lswk11 = (((x1)>(sx-24))>7)+1)
                                                                                         */
      IPfbwk. lswk11 = IPfbwk. lswk11 >> 1; /* lswk11 = ((((x1>>(sx-24))>>7)+1)>>1)
      IPfbwk. lswk11 = IPfbwk. lswk11 + DivPlsV. Iu0. l; /* lswk11 = pfbrem + (((x1>>(sx-24))>>7)+1)>>1) */
      IPfbwk. 1swk1 = 56;
                                /* 1swk1 = 56
      IPfbwk. lswk1 = IPfbwk. lswk1 - DivPlsV. Sx. 1; /* lswk1 = 56 - sx
      IPfbwk. 1swk2 = IPfbwk. 1swk4 \lt\lt IPfbwk. 1swk1; /* 1swk2 = (xh\lt\lt(56-sx))
      IPfbwk. 1swk2 = IPfbwk. 1swk2 >> 8; /* 1swk2 = ((xh << (56-sx))>>8)
      IPfbwk. lswk11 = IPfbwk. lswk11 + IPfbwk. lswk2; /* lswk11= lswk11 + ((xh < (56-sx)) >> 8)
    IPfbwk. 1swk2 = 0x00800000; /* 1swk2 = 0x00800000
#if 0
    if (IPfbwk. lswk11 >= IPfbwk. lswk2)
      IPfbwk. lswk11 = IPfbwk. lswk11 - (IPfbwk. lswk2 << 1); /* lswk11 = pfbrem - 0x008000000 * 2
      IPfbwk. 1swk10 = IPfbwk. 1swk10 + ONER; /* 1swk10 = 1swk10 + 1
#endif
    DivPlsV. Iu0. 1 = IPfbwk. lswk11;
                                    /* lswk11 --> pfbrem
    DivPlsV. Ret0. 1 = IPfbwk. 1swk10;
                                      /* lswk10 --> DivPlsV.Ret0
    return;
#endif
```

```
//\langle 2 \rangle start
/*
            凍 化 用ライブラリ
                                                         */
/*
電 流 検 出 値 読み 取り
inline void ADConvDataLoad( MICRO AXIS HANDLE *AxisRsc )
 SHORT swk;
    A/D convert data loading
    IntAdV. IuInData = IntAdP. Kcu * ( IUS + IntAdV. IuOffset ) / 2<sup>8</sup>
    IntAdV. IvInData = IntAdP. Kcv * ( IVS + IntAdV. IvOffset ) / 2<sup>8</sup>
 swk = mulshr(IuAD, ONE, 2);
 AxisRsc->IntAdV. IuInData = mulshr((swk + AxisRsc->IntAdV. IuOffset), AxisRsc->IntAdP. Kcu, 8);
 swk = mulshr(IvAD, ONE, 2);
 AxisRsc->IntAdV. IvInData = mulshr((swk + AxisRsc->IntAdV. IvOffset), AxisRsc->IntAdP. Kcv, 8);
#ifdef MULTI AXIS
 AxisRsc++;
 swk = mulshr(IuAD 2, ONE, 2);
 AxisRsc->IntAdV. IuInData = mulshr((swk + AxisRsc->IntAdV. IuOffset), AxisRsc->IntAdP. Kcu, 8);
 swk = mulshr(IvAD 2, ONE, 2);
 AxisRsc->IntAdV. IvInData = mulshr((swk + AxisRsc->IntAdV. IvOffset), AxisRsc->IntAdP. Kcv, 8);
#endif
 return;
```

```
/*
   PWM出 力
/*
inline void SetPWM( MICRO AXIS HANDLE *AxisRsc )
                            /* <S015> */
 PwmT2 = AxisRsc->PwmV. PwmCntT2;
 PwmT1 = AxisRsc->PwmV. PwmCntT1;
 PwmT0 = AxisRsc->PwmV. PwmCntT0;
                 /* 多 軸 処 理有効
#ifdef MULTI AXIS
 AxisRsc++;
 PwmT2_2 = AxisRsc->PwmV. PwmCntT2;
 PwmT1 2 = AxisRsc->PwmV. PwmCntT1;
 PwmT0 2 = AxisRsc->PwmV. PwmCntT0;
#endif /* MULTI_AXIS */
             /* 多 軸 処 理有効
//\langle 2 \rangle end
```