

September 2009

NC7SZ157 TinyLogic[®] UHS 2-Input Non-Inverting Multiplexer

Features

- Broad V_{CC} Operating Range: 1.65V to 5.5V
- Ultra High-Speed
- Power Down High-Impedance Inputs/Outputs
- Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation
- Proprietary Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SC70 Package

Description

The NC7SZ157 is a single, high performance, 2-to-1 CMOS non-inverting multiplexer from Fairchild's Ultra-High Speed series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra high speed with high output drive while maintaining low static power dissipation over a broad $V_{\rm CC}$ operating range. The device is specified to operate over the 1.65V to 5.5V Vcc operating range. The inputs and outputs are high impedance when $V_{\rm CC}$ is 0V. Inputs tolerate voltages up to 5.5V independent of $V_{\rm CC}$ operating range.

Ordering Information

Part Number	Top Mark	© Eco Status	Package	Packing Method
NC7SZ157P6X	ZF7	RoHS	6-Lead SC70, EIAJ SC-88, 1.25mm Wide	3000 Units on Tape & Reel
NC7SZ157L6X	В9	RoHS	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SZ157FHX	B9	Green	6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

For Fairchild's definition of Eco Status, please visit: http://www.fairchildsemi.com/company/green/rohs_green.html.

Connection Diagrams

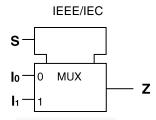


Figure 1. Logic Symbol

Pin Configurations

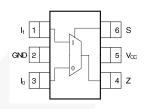


Figure 2. SC70 (Top View)

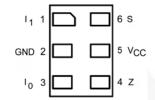


Figure 3. MicroPak™ (Top Through View)

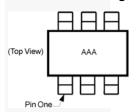


Figure 4. Pin 1 Orientation

Notes:

- 1. AAA represents product code top mark (see Ordering Information).
- 2. Orientation of top mark determines pin one location.
- 3. Reading the top mark left to right, pin one is the lower left pin.

Pin Definitions

Pin # SC70	Pin # MicroPak	Name	Description
1	1	I ₁	Data Input
2	2	GND	Ground
3	3	I ₀	Data Input
4	4	Z	Output
5	5	V _{CC}	Supply Voltage
6	6	S	Control Input

Function Table

	Inputs		Output	
S I ₁ I ₀		l _o	$Z = (I_0) \bullet (S) + (I_1) \bullet (S)$	
L	Х	L	L	
L	Х	Н	Н	
Н	L	X	L	
Н	Н	X	Н	

H = HIGH Logic Level

L = LOW Logic Level

X = Don't' Care

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	ameter	Min.	Max.	Unit
V _{CC}	Supply Voltage		-0.5	7.0	V
V _{IN}	DC Input Voltage		-0.5	7.0	V
V _{OUT}	DC Output Voltage		-0.5	7.0	V
I _{IK}	DC Input Diode Current	$V_{IN} \leq 0.5V$		-50	mA
lok	DC Output Diode Current	$V_{OUT} \le -0.5V$		-50	mA
I _{OUT}	DC Output Current			±50	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current			±50	mA
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under B	ias		+150	°C
TĻ	Junction Lead Temperature (S	oldering, 10 Seconds)		+260	°C
		SC70-6		180	
P_D	Power Dissipation at +85°C	MicroPak-6		130	mW
		MicroPak2-6		120	
ECD	Human Body Model, JEDEC:Ji		4000	\/	
ESD	Charge Device Model, JEDEC:	1	2000	V	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
	Supply Voltage Operating		1.65	5.50		
Vcc	Supply Voltage Data Retention		1.50	5.50	V	
V _{IN}	Input Voltage		0	5.5	V	
V _{OUT}	Output Voltage		0	Vcc	V	
T _A	Operating Temperature		-40	+85	°C	
		V _{CC} at 1.8V ± 0.15V, 2.5V ± 0.2V	0	20		
t_r , t_f	Input Rise and Fall Times	V _{CC} at 3.3V ± 0.3V	0	10	ns/V	
		V _{CC} at 5.0V ± 0.5V	0	5		
		SC70-6		350		
$\theta_{\sf JA}$	Thermal Resistance	MicroPak-6		500	°C/W	
		MicroPak2-6		560		

DC Electrical Characteristics

	-	.,			T,	₄ =+25°	,C	T _A =-40	to +85°C	
Symbol	Parameter	V _{CC} Conditions		Min.	Тур.	Max.	Min.	Max.	Units	
HIGH Level Input		1.65 to 1.95			0.75V _{CC}			0.75V _{CC}		
V_{IH}	Voltage	2.30 to 5.50			0.70V _{CC}			0.70V _{CC}		V
V	LOW Level Input	1.65 to 1.95					0.25V _{CC}		0.25V _{CC}	V
V_{IL}	Voltage	2.30 to 5.50	1				0.30V _{CC}		0.30V _{CC}	V
		1.65			1.55	1.65		1.55		
		2.30	V _{IN} =V _{IL}	1 100	2.20	2.30		2.20		
		3.00	or V _{IH}	I _{OH} = -100μA	2.90	3.00		2.90		
		4.50			4.40	4.50		4.40		
V_{OH}	HIGH Level Output Voltage	1.65		I _{OH} = -4mA	1.29	1.52		1.29		V
	ouput voltage	2.30	V _{IN} =V _{IL} or V _{IH}	I _{OH} = -8mA	1.90	2.15		1.90		
		3.00		I _{OH} = -16mA	2.40	2.80		2.40		
		3.00		I _{OH} = -24mA	2.30	3.68		2.30		
		4.50		I _{OH} = -32mA	3.90	4.20		3.80		
		1.65	V _{IN} =V _{IL} or V _{IH}			0	0.10		0.10	
		2.30		I _{OL} = 100μA		0	0.10		0.10	V
		3.00				0	0.10		0.10	
		4.50				0	0.10		0.10	
V_{OL}	LOW Level Output Voltage	1.65		I _{OL} = 4mA		0.08	0.24		0.24	
		2.30	V _{IN} =V _{IL}	I _{OL} = 8mA		0.10	0.30		0.30	
		3.00	or V _{IH}	I _{OL} = 16mA		0.15	0.40		0.40	V
		3.00		I _{OL} = 24mA		0.22	0.55		0.55	
		4.5		I _{OL} = 32mA		0.22	0.55		0.55	
I _{IN}	Input Leakage Current	0 to 5.50	V _{IN} =5.5	/, GND			±0.1		±1	μΑ
l _{OFF}	Power Off Leakage Current	0	V _{IN} or V	_{ОИТ} =5.5V			1		10	μΑ
Icc	Quiescent Supply Current	1.65 to 5.50	V _{IN} =5.5\	/, GND					10	μΑ

AC Electrical Characteristics

Cumbal	Symbol Parameter		Conditions	T,	₄ =+25°(3	T _A =-40 1	to +85°C	Unito	Figure
Symbol	Parameter	V _{CC}	Conditions	Min.	Тур.	Max.	Min.	Max.	Units	Figure
		1.80 ± 0.15		2.5	6.0	11.5	2.5	12.0		
	Propagation Delay	2.50 ± 0.20	C _L =15pF,	1.2	3.5	6.1	1.2	6.5		
	S to Z	3.30 ± 0.30	$R_L=1M\Omega$,	0.8	2.6	4.1	0.8	4.5		
		5.00 ± 0.50		0.5	1.9	3.2	0.5	3.5	ns	Figure 5 Figure 6
	Propagation Delay I _n to Z	1.80 ± 0.15	C_L =15pF, R_L =1M Ω ,	2.5	5.9	10.0	2.5	10.5		
4		5.00 ± 0.50		1.2	3.5	5.8	1.2	6.1		
t _{PLH} , t _{PHL}		3.30 ± 0.30		0.8	2.6	3.9	0.8	4.2		
		5.00 ± 0.50		0.5 1.9 3.1 0	0.5	3.3				
	Propagation Delay	3.30 ± 0.30	C_L =50pF, R_L =500 Ω ,	1.2	3.2	4.8	1.2	5.2		
	S to Z	5.00 ± 0.50		0.8	2.4	3.8	0.8	4.1		
	Propagation Delay	3.30 ± 0.30	C _L =50pF,	1.2	3.2	4.6	1.2	5.0		
	I _n to Z	5.00 ± 0.50	$R_L=500\Omega$,	0.8	2.4	3.7	0.8	4.0		
C _{IN}	Input Capacitance	0.00			2				pF	
C	Power Dissipation	3.30			14				pF	Figure 7
C_{PD}	Capacitance ⁽⁴⁾	5.00			17					Figure 7

Note:

4. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD}=(C_{PD})(V_{CC})(f_{IN})+(I_{CC}static).

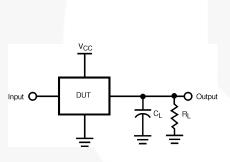


Figure 5. AC Test Circuit

Note:

5. C_L includes load and stray capacitance. Input PRR=1.0MHz, t_w =500ns.

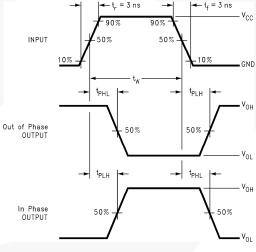
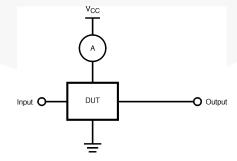


Figure 6. AC Waveforms



Note:

6. Input=AC Waveform; PRR=Variable; Duty Cycle=50%.

Figure 7. I_{CCD} Test Circuit

Physical Dimensions

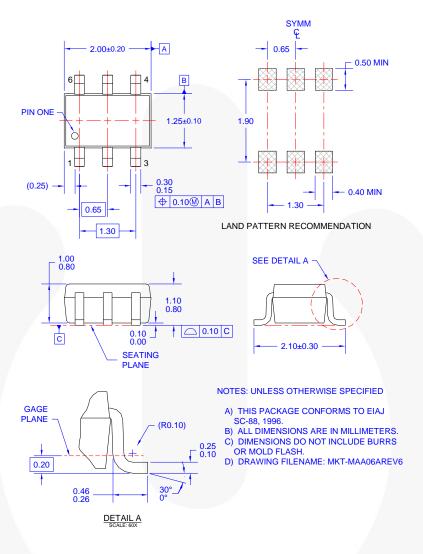


Figure 8. 6-Lead, SC70, EIAJ SC-88, 1.25mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

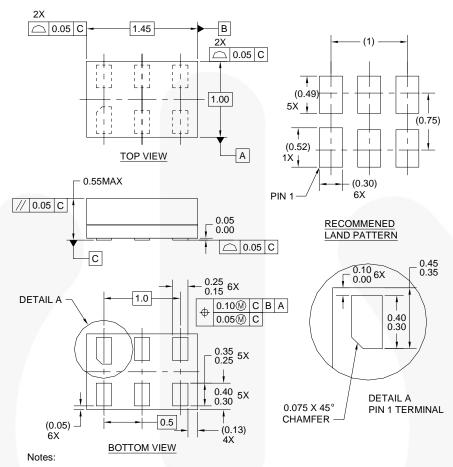
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/analog/pdf/sc70-6_tr.pdf.

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P6X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

Figure 9. 6-Lead, MicroPak™, 1.0mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/logic/pdf/micropak_tr.pdf.

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions

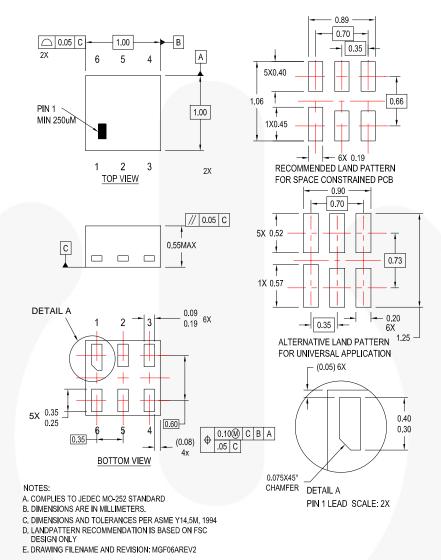


Figure 10. 6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/packaging/MicroPAK2 6L tr.pdf.

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed





TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™
ActoPower™
Auto-SPM™
Build it Now™
CorePLUS™
CorePOWER™
CROSSVOL7™
CTL™
Current Transfer Logic™

Current Transfer Logic™ EcoSPARK® EfficientMax™ EZSWITCH™*

EZI™ DEUXPEED™ F® Fairchild®

Fairchild[®]
Fairchild Semiconductor[®]
FACT Quiet Series[™]
FACT[®]
FAST[®]

FAST® FastvCore™ FETBench™ FlashWriter®* FPS™ F-PFS™ FRFET®

Global Power ResourceSM Green FPSTM Green FPSTM e-SeriesTM

G maxTM
GTOTM
IntelliMAXTM
ISOPLANARTM
MegaBuckTM
MICROCOUPLERTM

MicroFET™
MicroPak™
MillerDrive™
MotionMax™
Motion-SPM™
OPTOLOGIC®
OPTOPLANAR®

PDP SPM™

Power-SPM™ PowerTrench® PowerXS™

Programmable Active Droop™

QFET[®]
QS™
Quiet Series™
RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax[™]
SMART START[™]
SPM[®]
STEALTH[™]

SPM*
STEALTH™
SUPERSOT™
SuperSOT™-8
SuperSOT™-8
SuperSOT™-8
SuperMOS™
SyncFET™
Sync-Lock™

EGENERAL
The Power Franchise

the wer
franchise

TinyBoost™
TinyBuck™
TinyLogic®
TinyLogic®
TinyPOPTO™
TinyPower™
TinyPVM™
TinyPVM™
TinyWire™
TRIFault Detect™
TRUECURRENT™

µSerDes™

SerDes
UHC®
Ultra FRFET™
UniFET™
VCX™
VisualMax™
XS™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Dennicion of Terms	ennuon or rems						
Datasheet Identification	Product Status	Definition					
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.					
Preliminary	First Production	Data sheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.					
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.					

Rev. 143

^{*} Trademarks of System General Corporation, used under license by Fairchild Semiconductor.