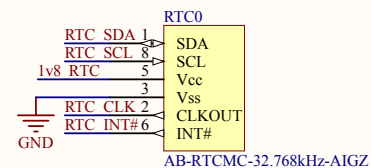
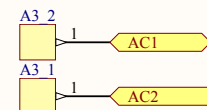
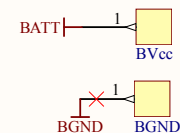
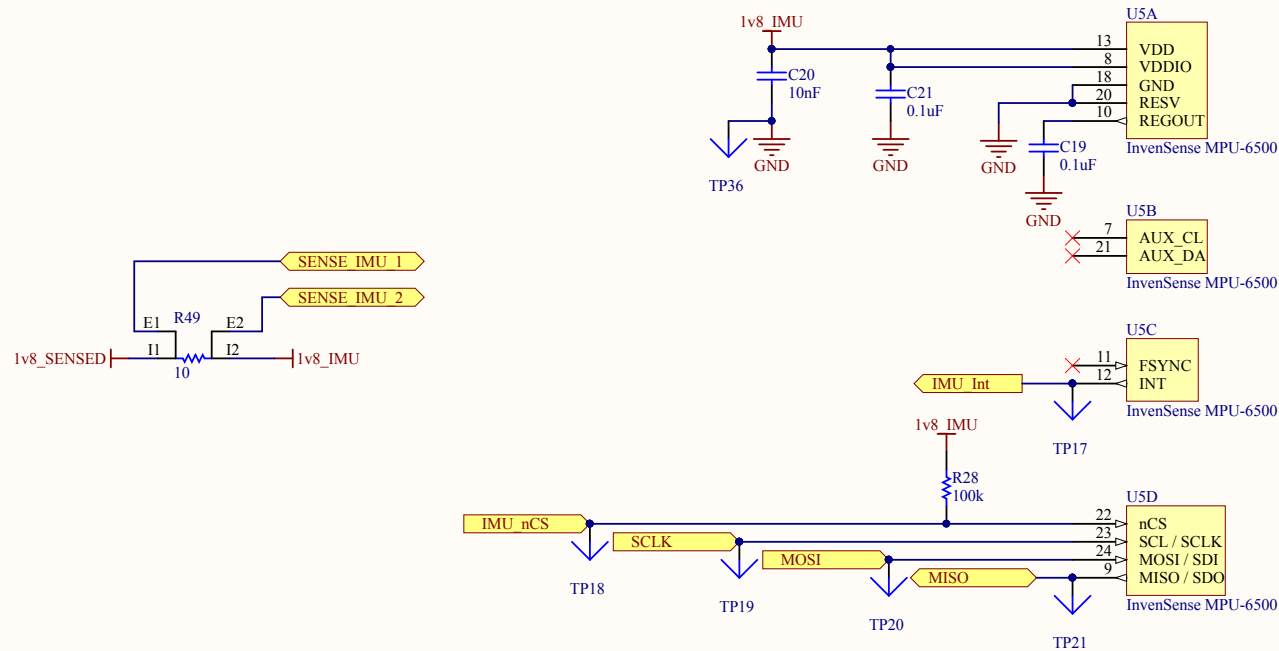


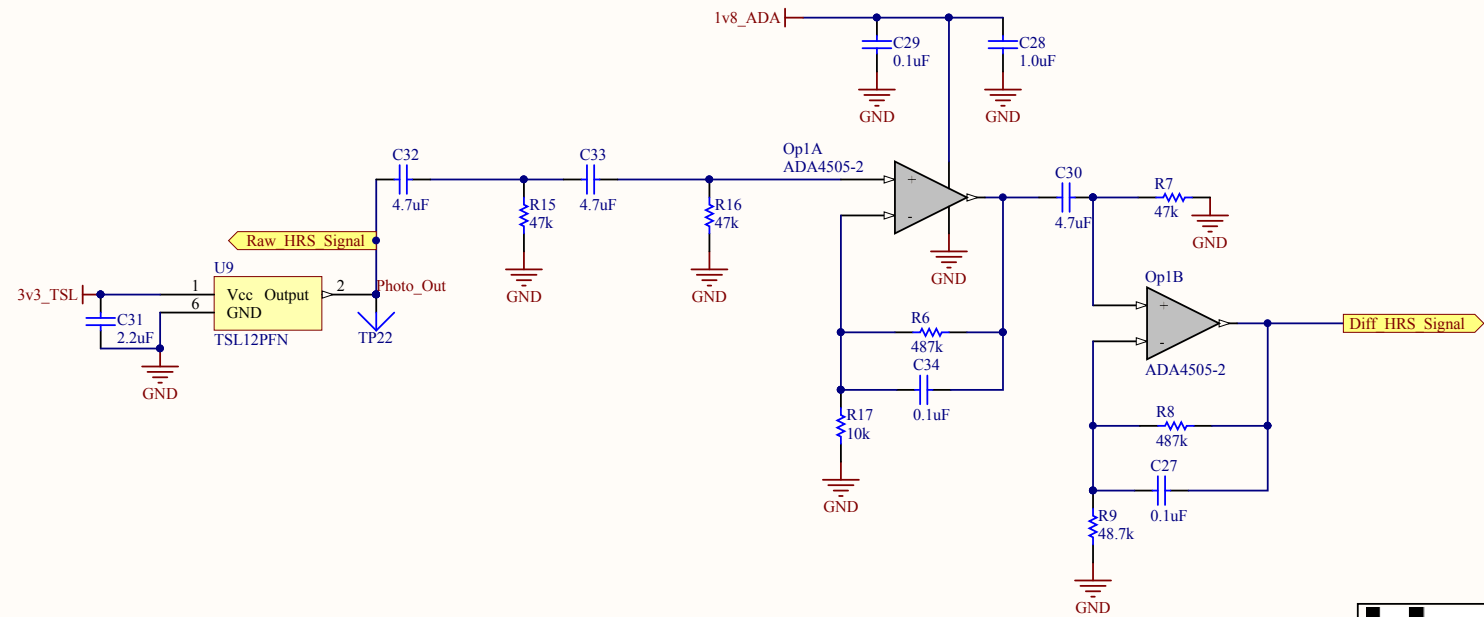
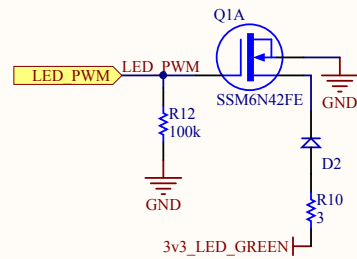
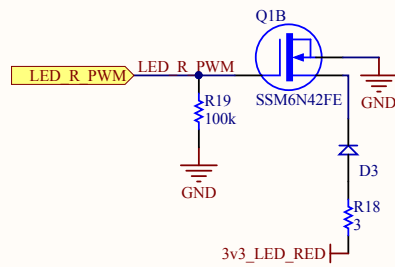
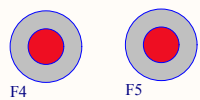
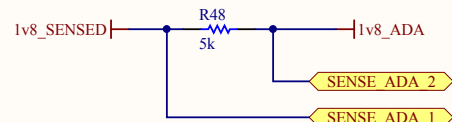
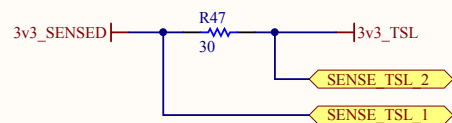
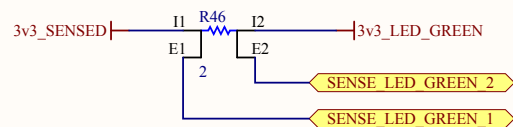
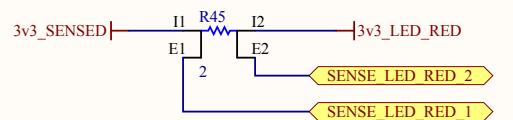
Hello

TITLE Band		REV EVT2
DATE 2/21/2014	DRAWN BY John Kelley	SHEET 1 OF 8

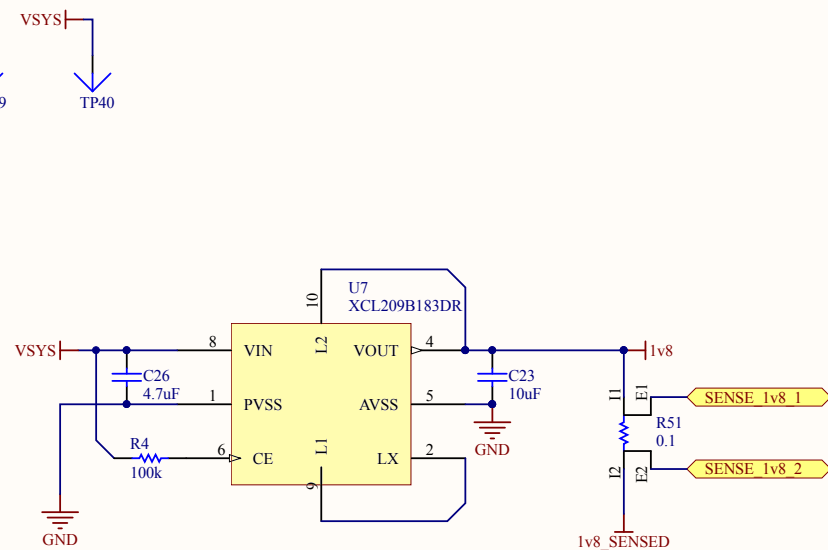




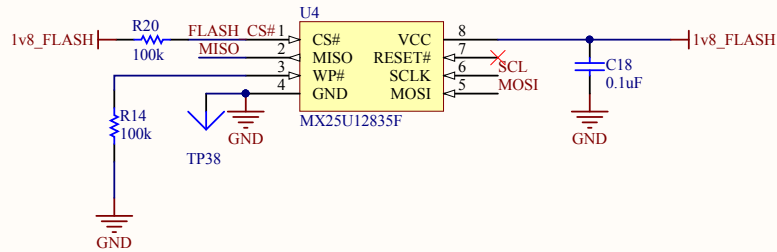
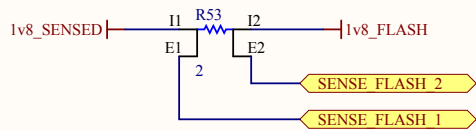
Hello		
TITLE IMU Sub-board		REV EVT2
DATE 2/20/2014	DRAWN BY John Kelley	SHEET 3 OF 8



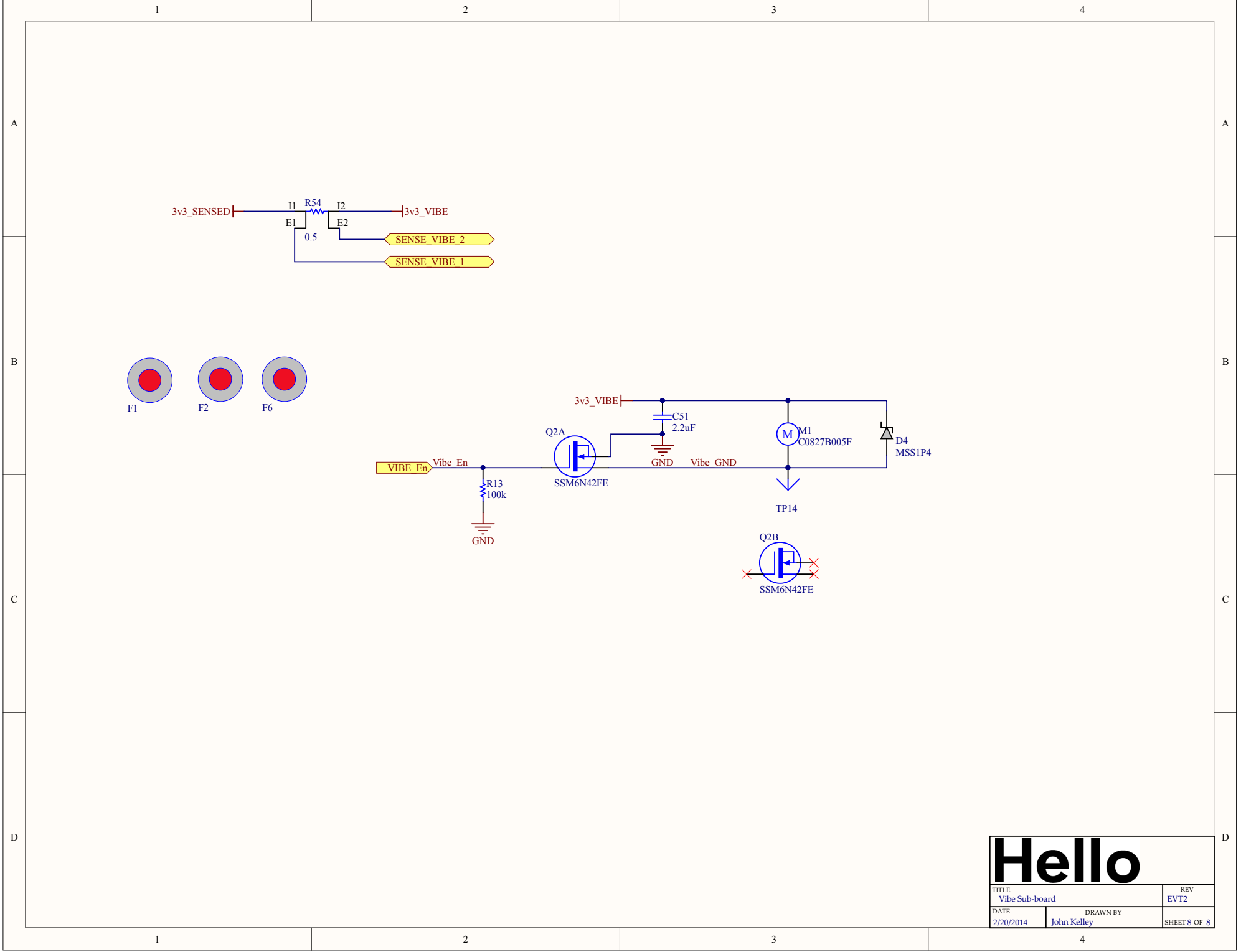
<h1>Hello</h1>		REV
		EVT2
DATE	DRAWN BY	SHEET 5 OF 8
2/20/2014	John Kelley	



TITLE Power Sub-board		REV EVT2
DATE 2/20/2014	DRAWN BY John Kelley	SHEET 6 OF 8

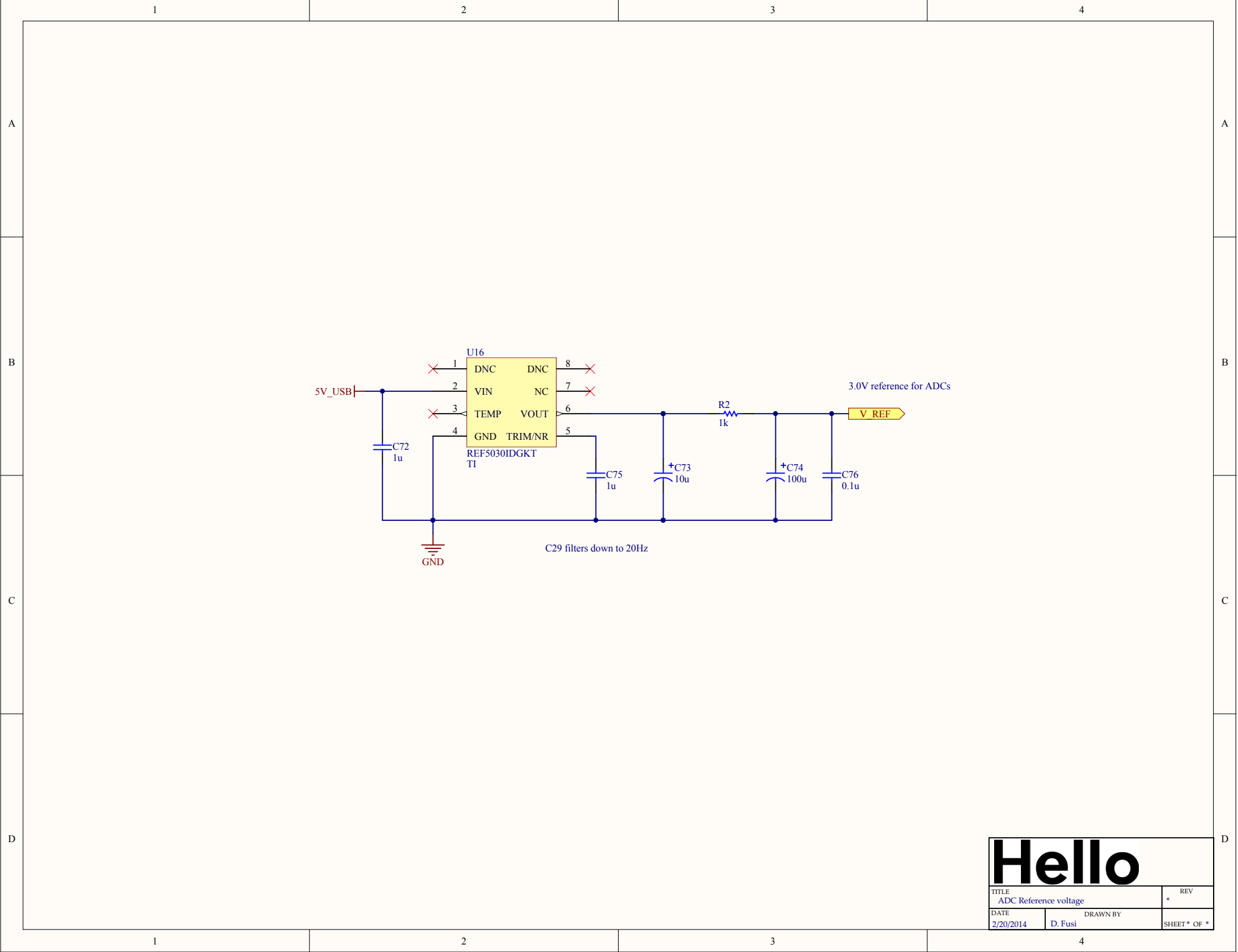


Hello		
TITLE Storage Sub-board		REV EVT2
DATE 2/20/2014	DRAWN BY John Kelley	SHEET 7 OF 8



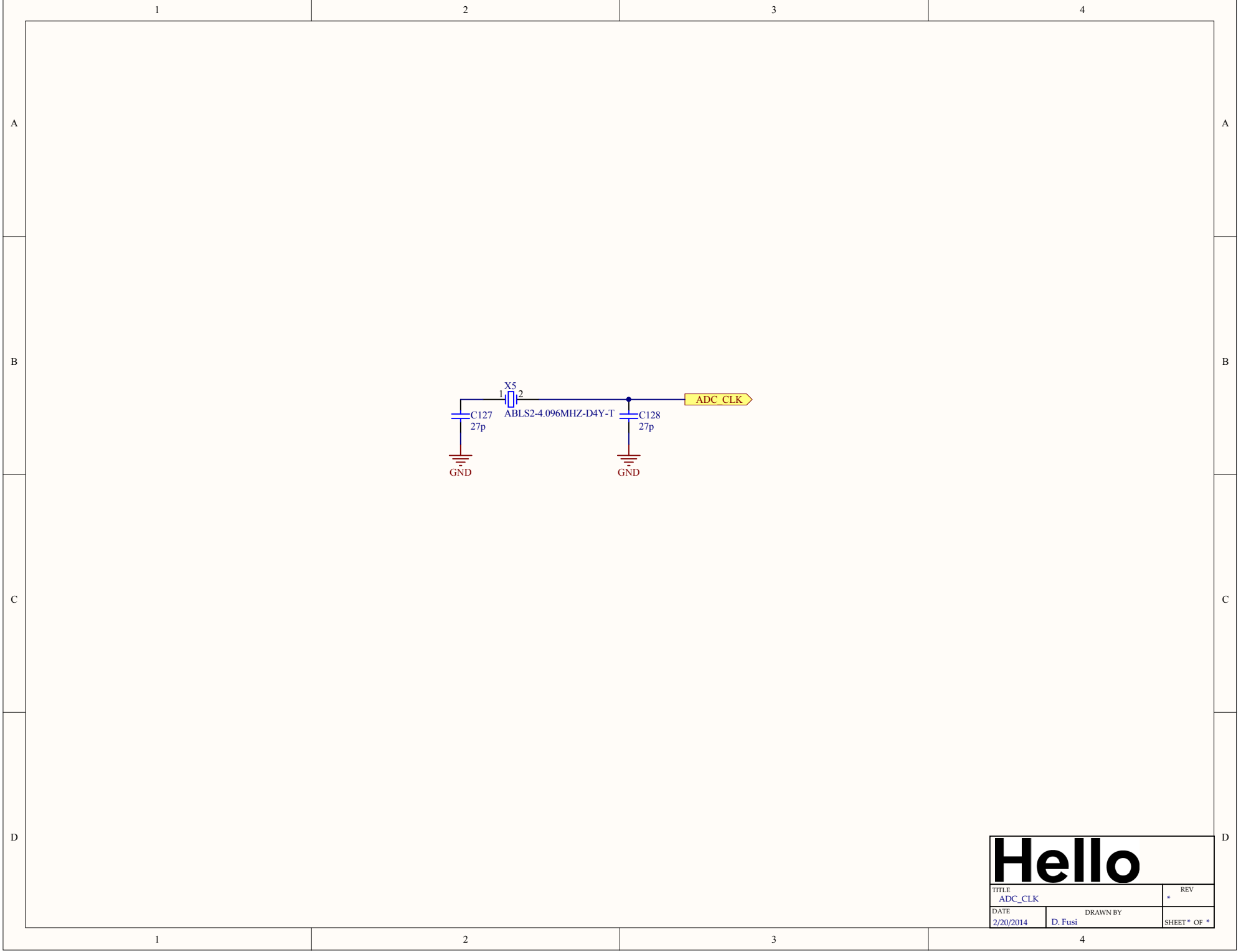
Hello

TITLE Vibe Sub-board		REV EVT2
DATE 2/20/2014	DRAWN BY John Kelley	SHEET 8 OF 8



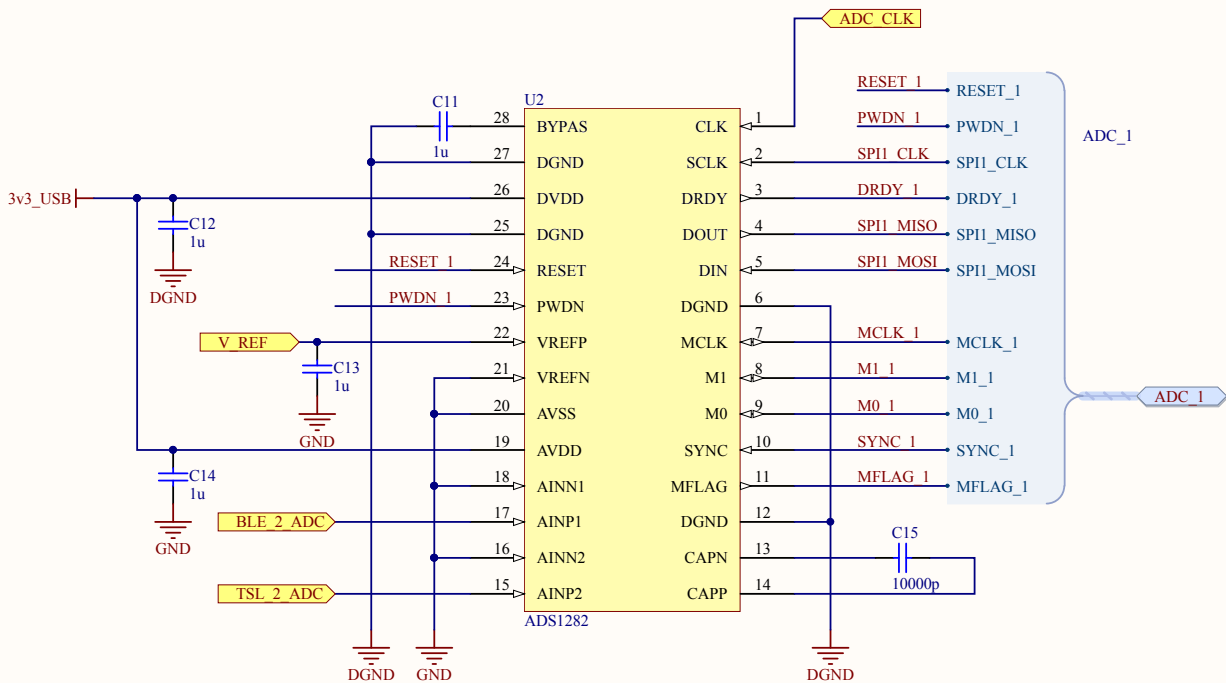
Hello

TITLE		REV
ADC Reference voltage		*
DATE	DRAWN BY	SHEET * OF *
2/20/2014	D. Fusi	



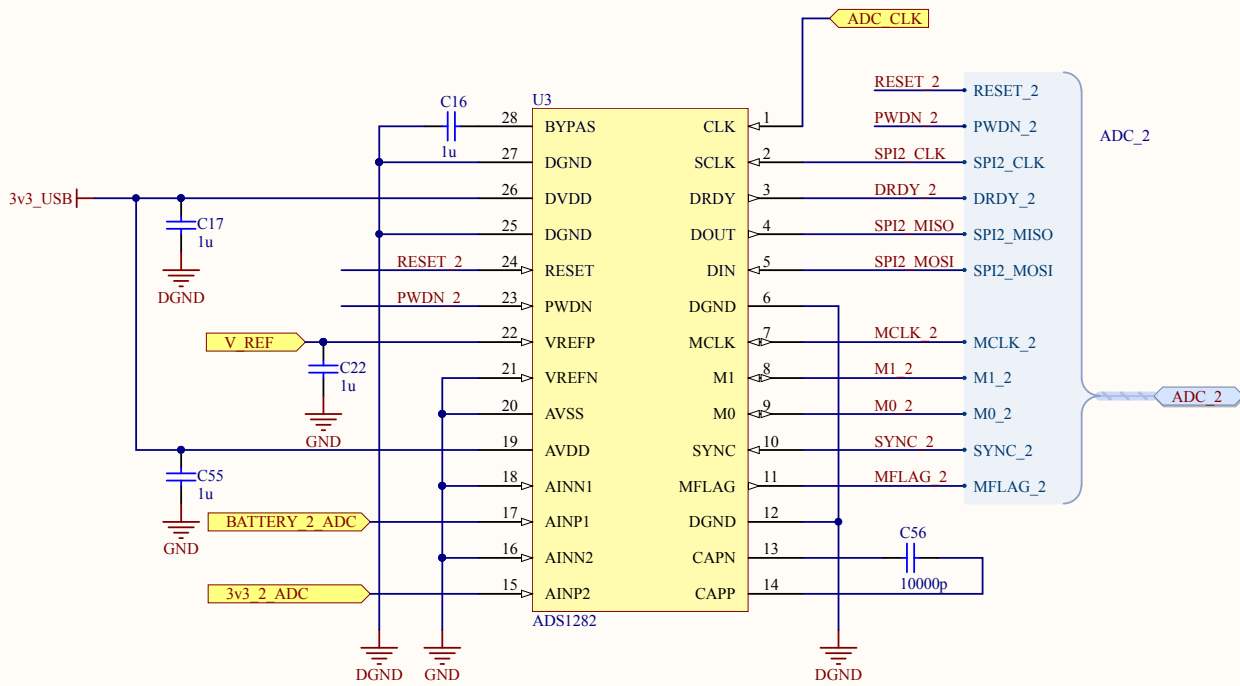
Hello

TITLE ADC_CLK		REV *
DATE 2/20/2014	DRAWN BY D. Fusi	SHEET * OF *



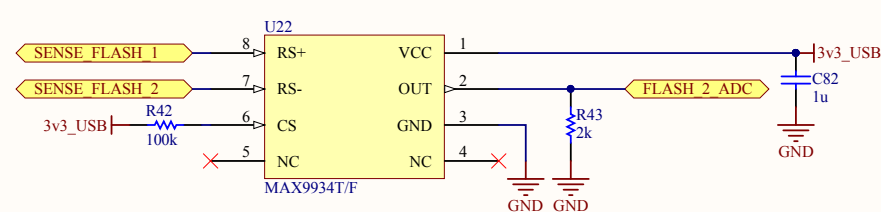
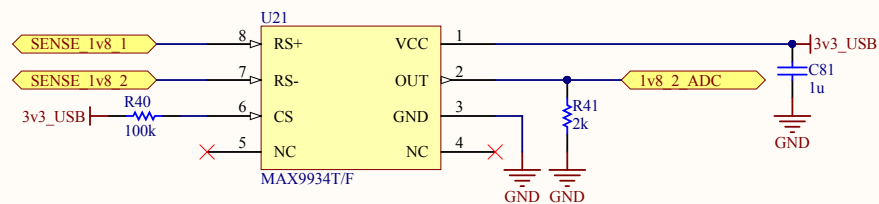
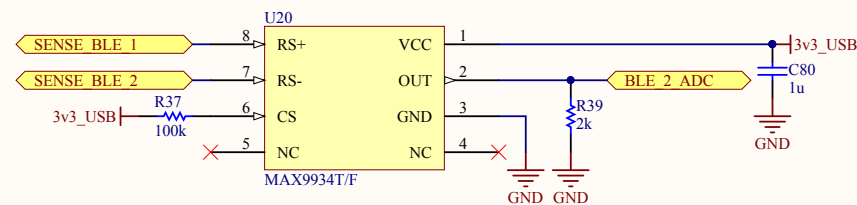
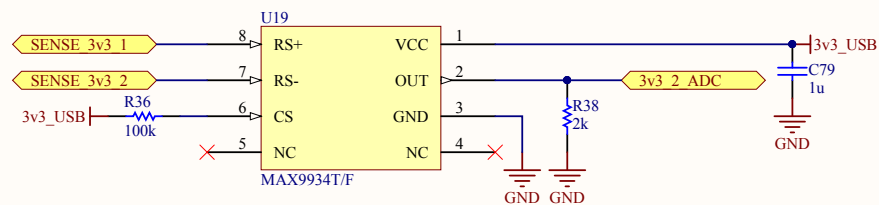
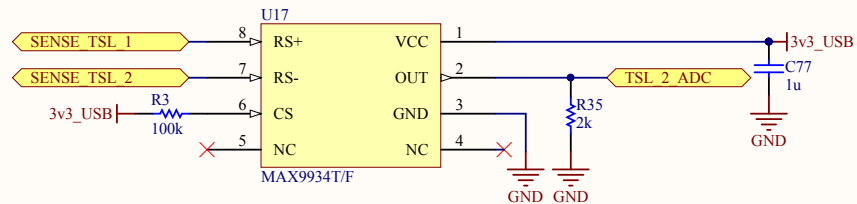
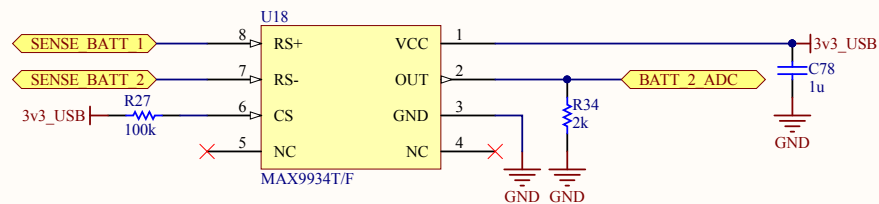
Hello

TITLE ADC1		REV *
DATE 2/20/2014	DRAWN BY D. Fusi	SHEET * OF *

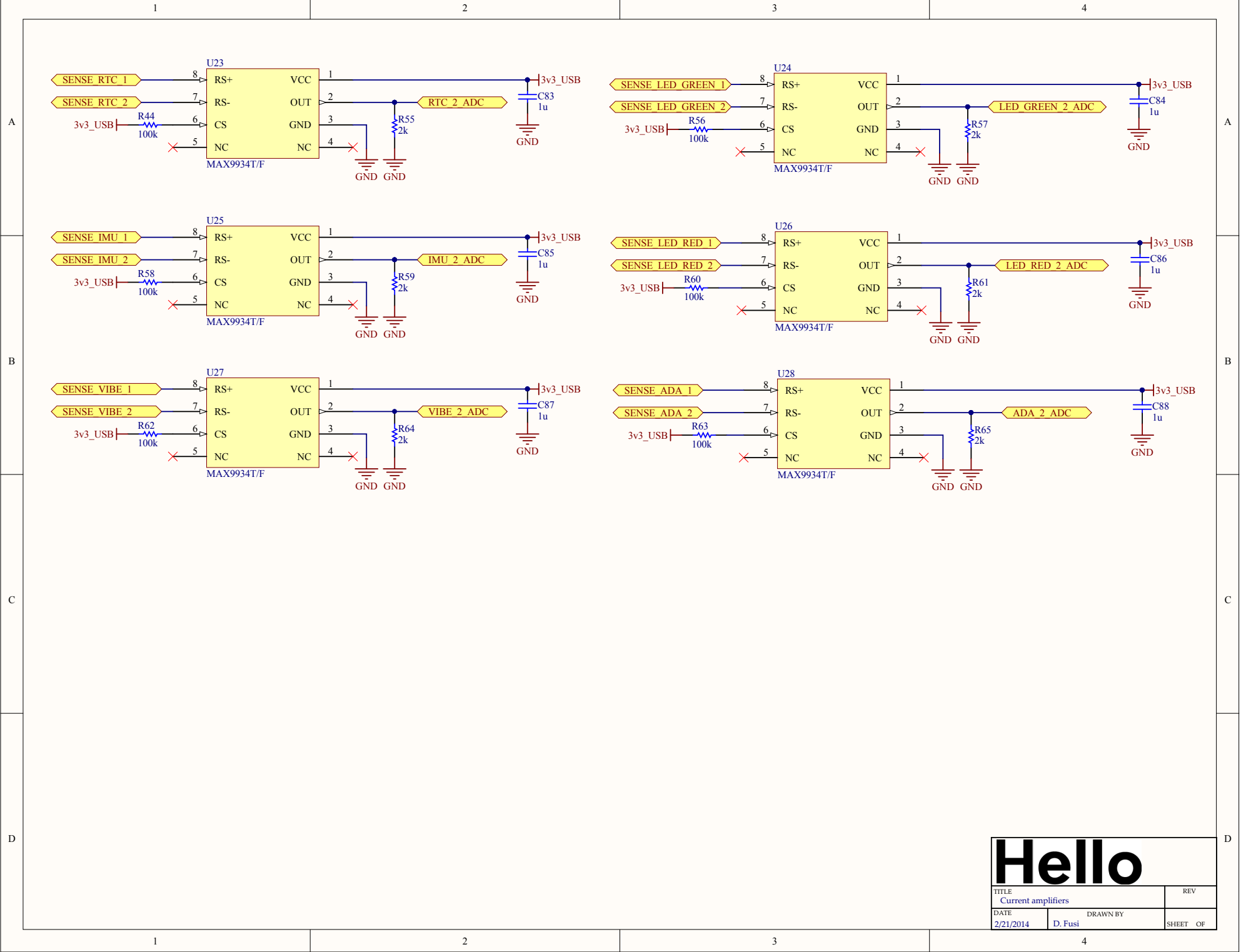






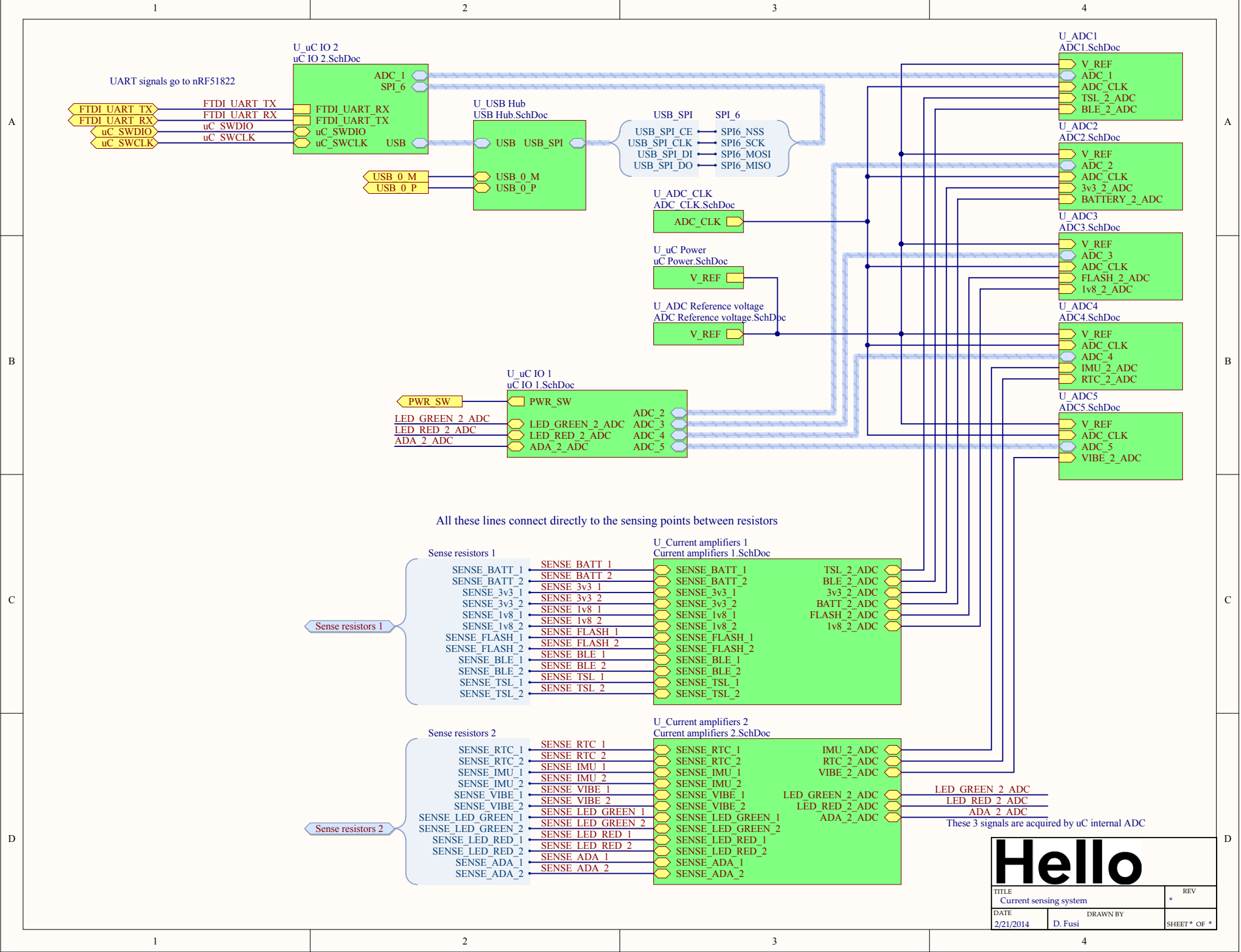


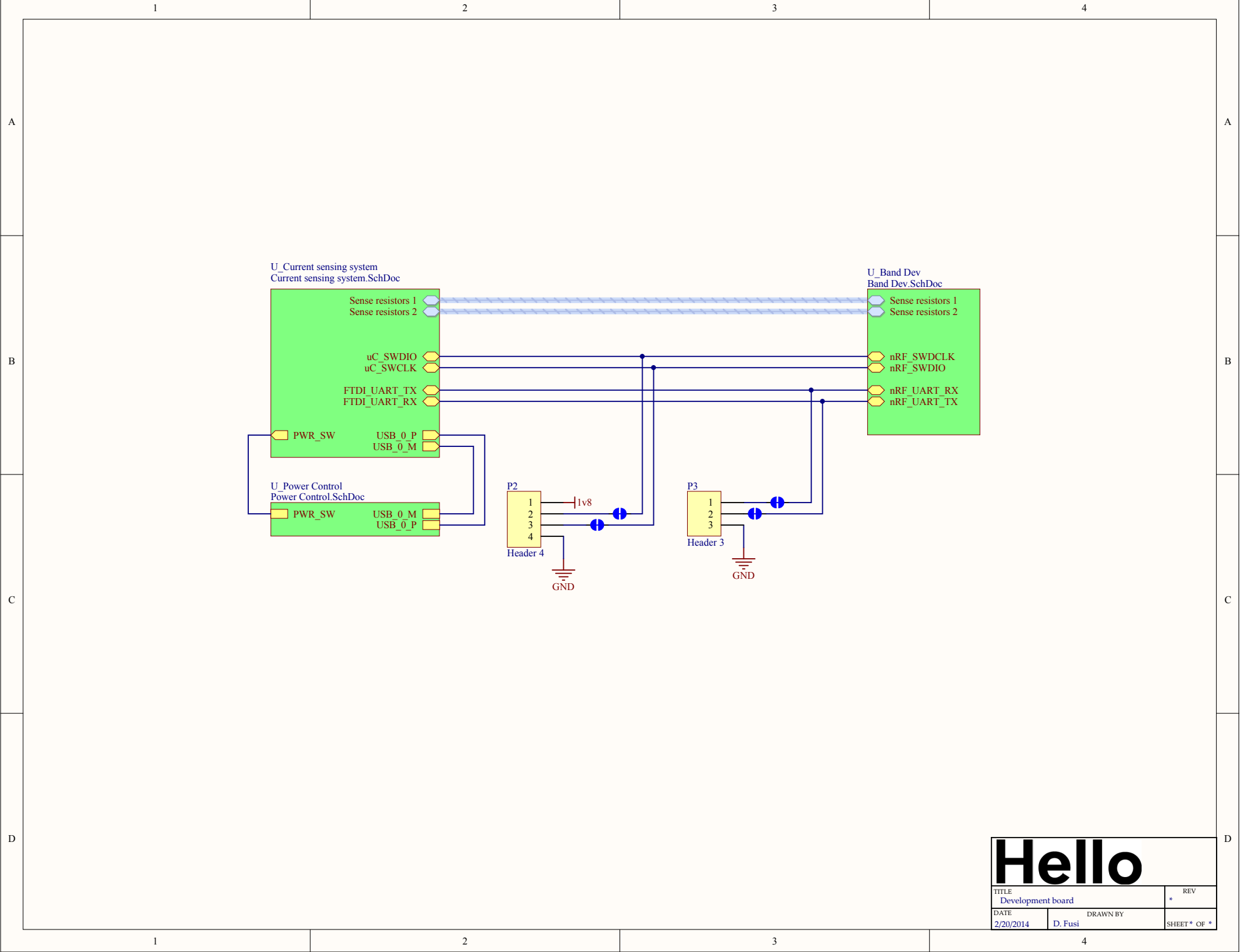
Hello		
TITLE Current amplifiers 1		* REV
DATE 2/20/2014	DRAWN BY D. Fusi	SHEET * OF *



Hello

TITLE Current amplifiers		REV
DATE 2/21/2014	DRAWN BY D. Fusi	SHEET OF



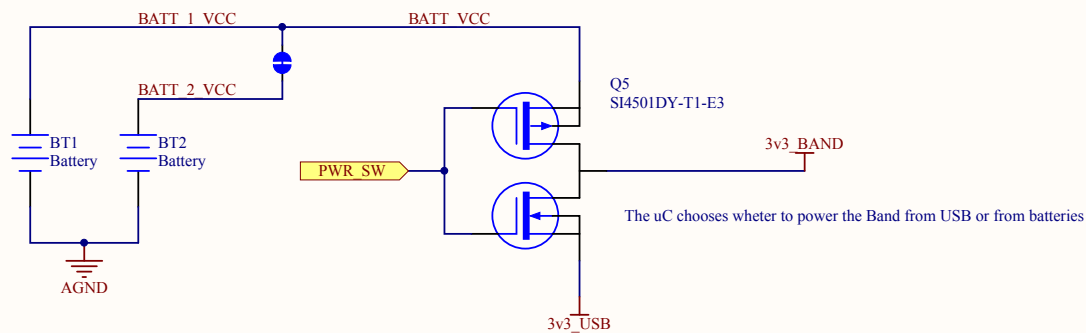
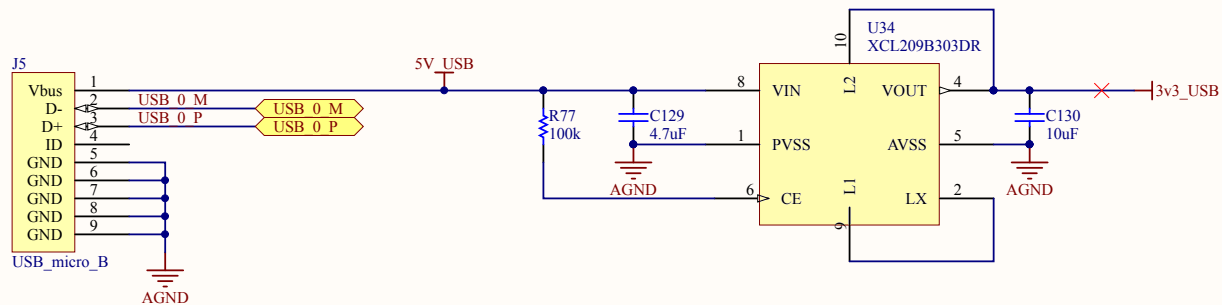


Hello

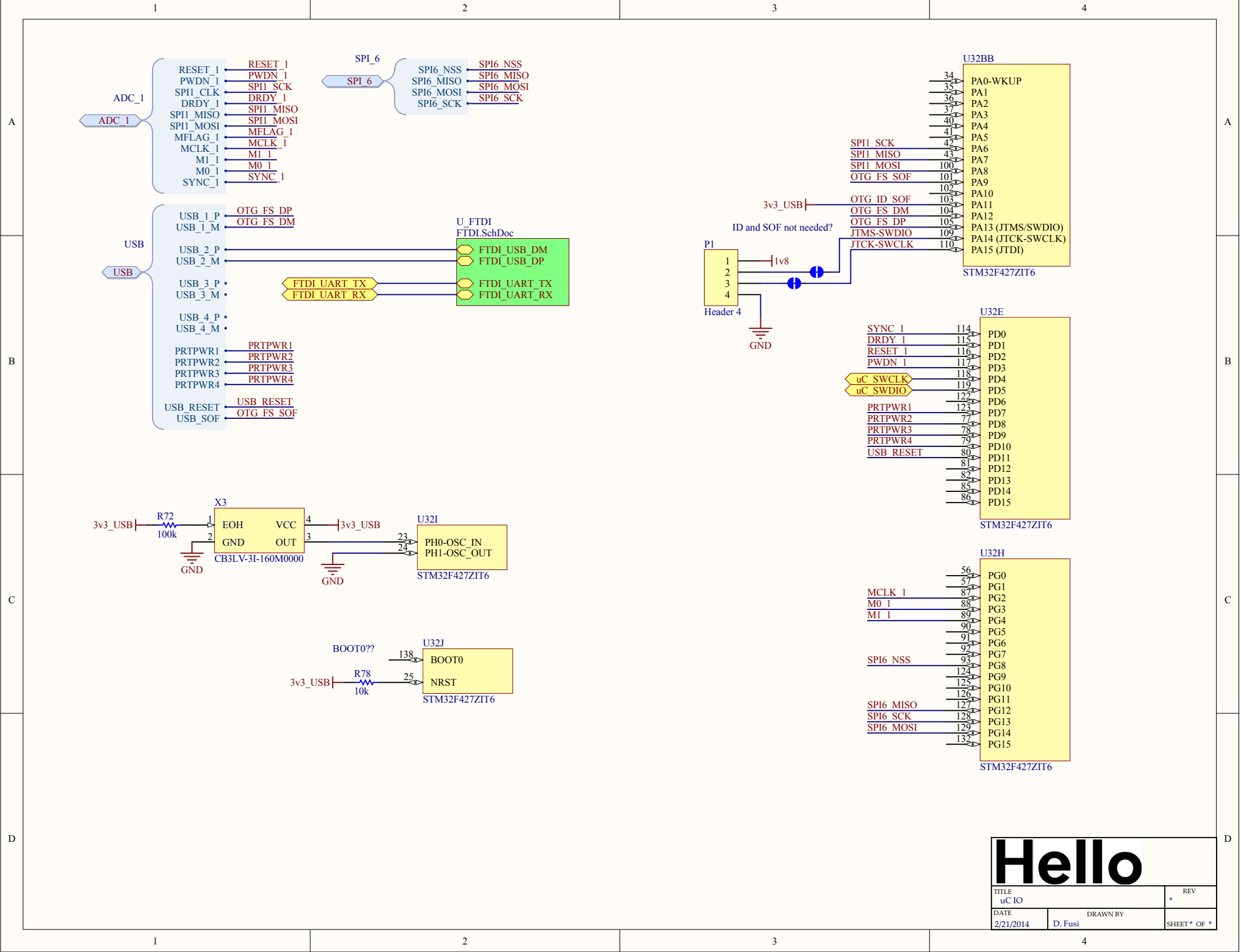
TITLE Development board		REV *
DATE 2/20/2014	DRAWN BY D. Fusi	SHEET * OF *

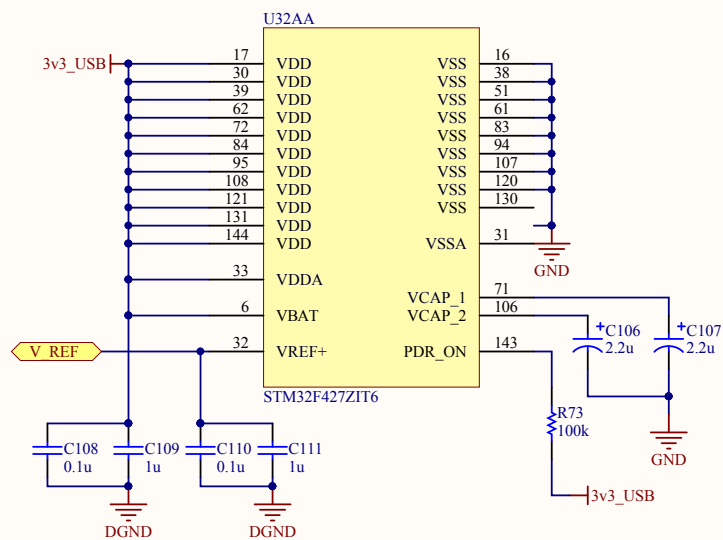
http://www.ftdichip.com/Support/Documents/DataSheets/ICs/DS_FT2232H.pdf



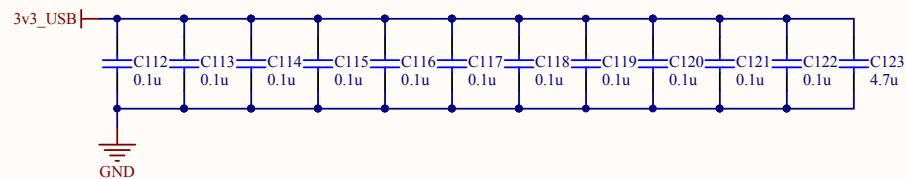


Hello		
TITLE	Power Control	REV
DATE	2/20/2014	D. Fusi
		SHEET * OF *

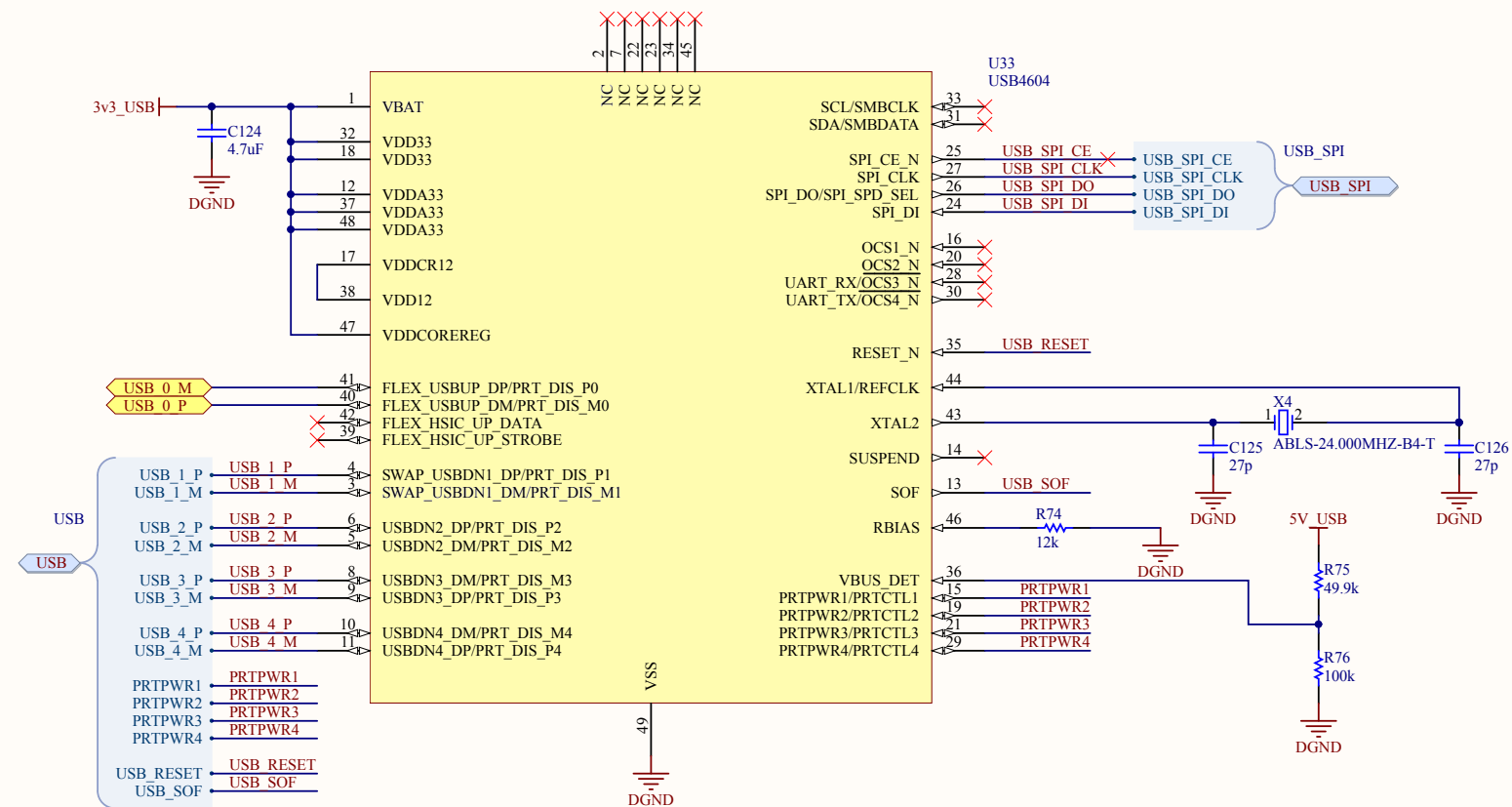




Filtering caps for VDD pins



Hello		
TITLE	REV	
uC Power	*	
DATE	DRAWN BY	SHEET * OF *
2/20/2014	D. Fusi	



Hello

TITLE USB Hub		REV
DATE 2/20/2014	DRAWN BY D. Fusi	SHEET OF

