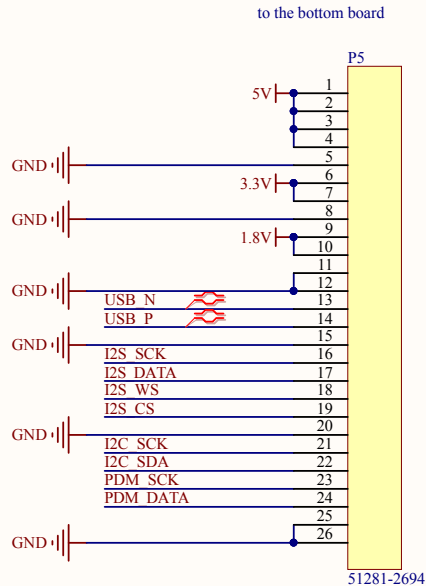
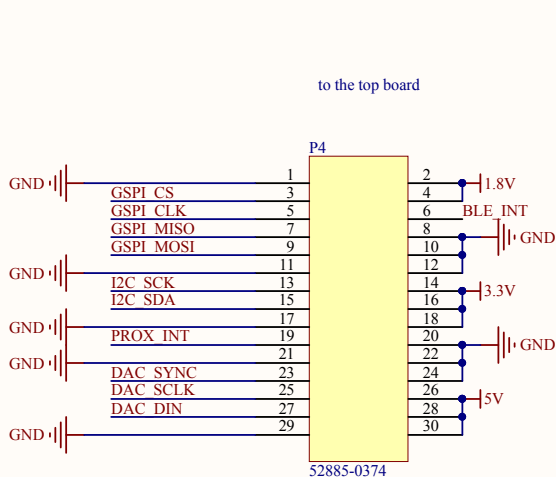
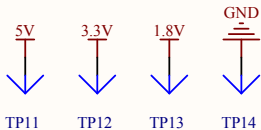
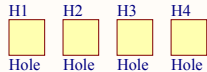
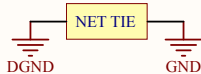
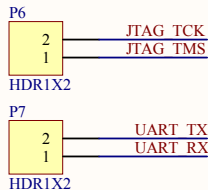


JTAG available to the developer via TMS and TCK pins
Defaults: P1 = 1-2, P2 = 2-3, P3 = 2-3

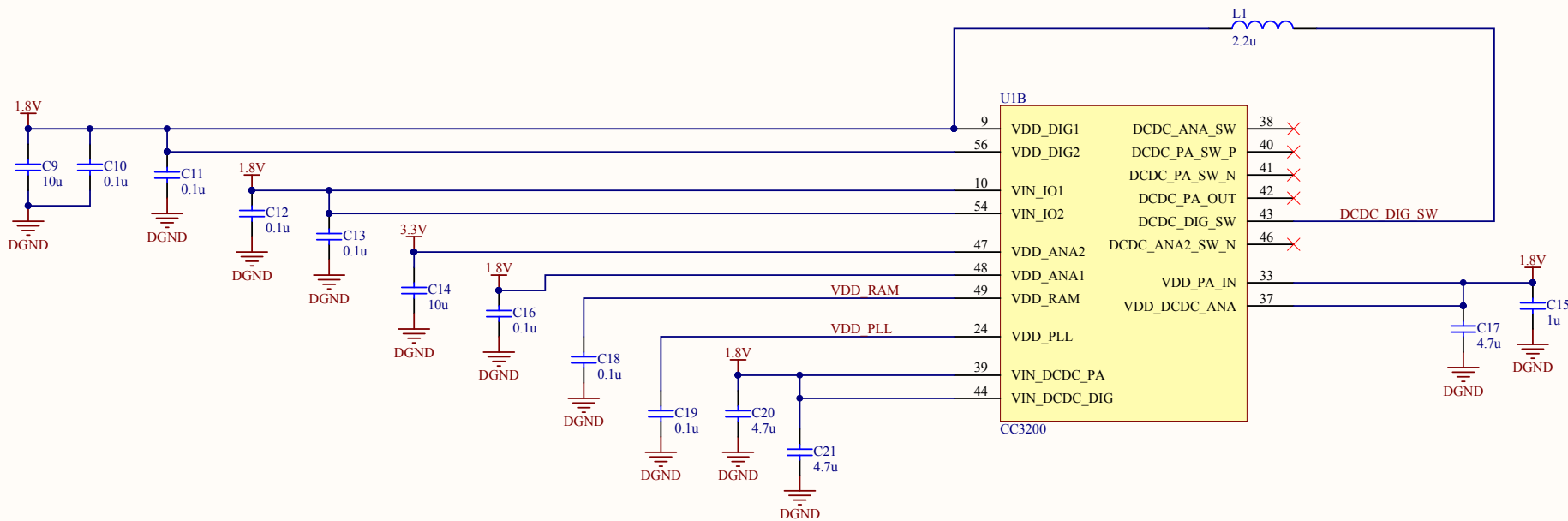




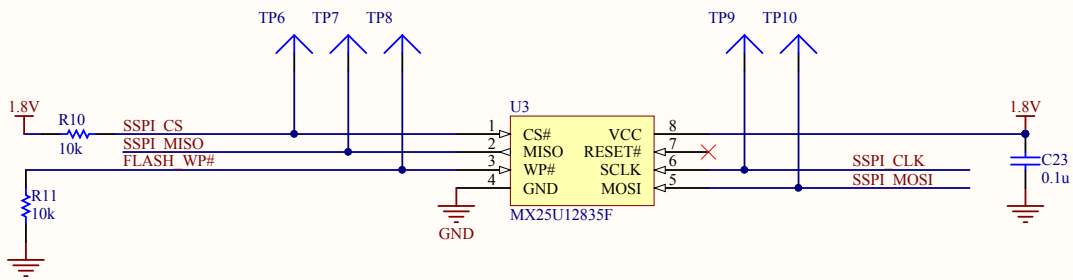
programming / debugging connectors



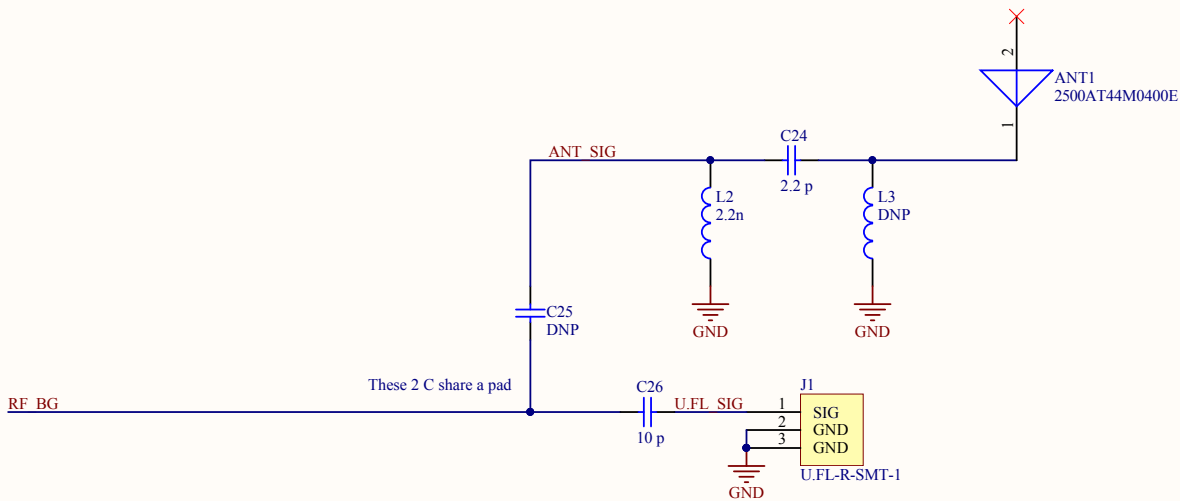
Hello		
TITLE Morpheus_middle		REV 1
DATE 6/2/2014	DRAWN BY D. Fusi	SHEET 2 OF 6







Hello		
TITLE Flash memory		REV 1
DATE 6/2/2014	DRAWN BY D. Fusi	SHEET 5 OF 6



Hello

TITLE M4F_MCU_Antennas		REV 1
DATE 5/30/2014	DRAWN BY D. Fusi	SHEET 6 OF 6

