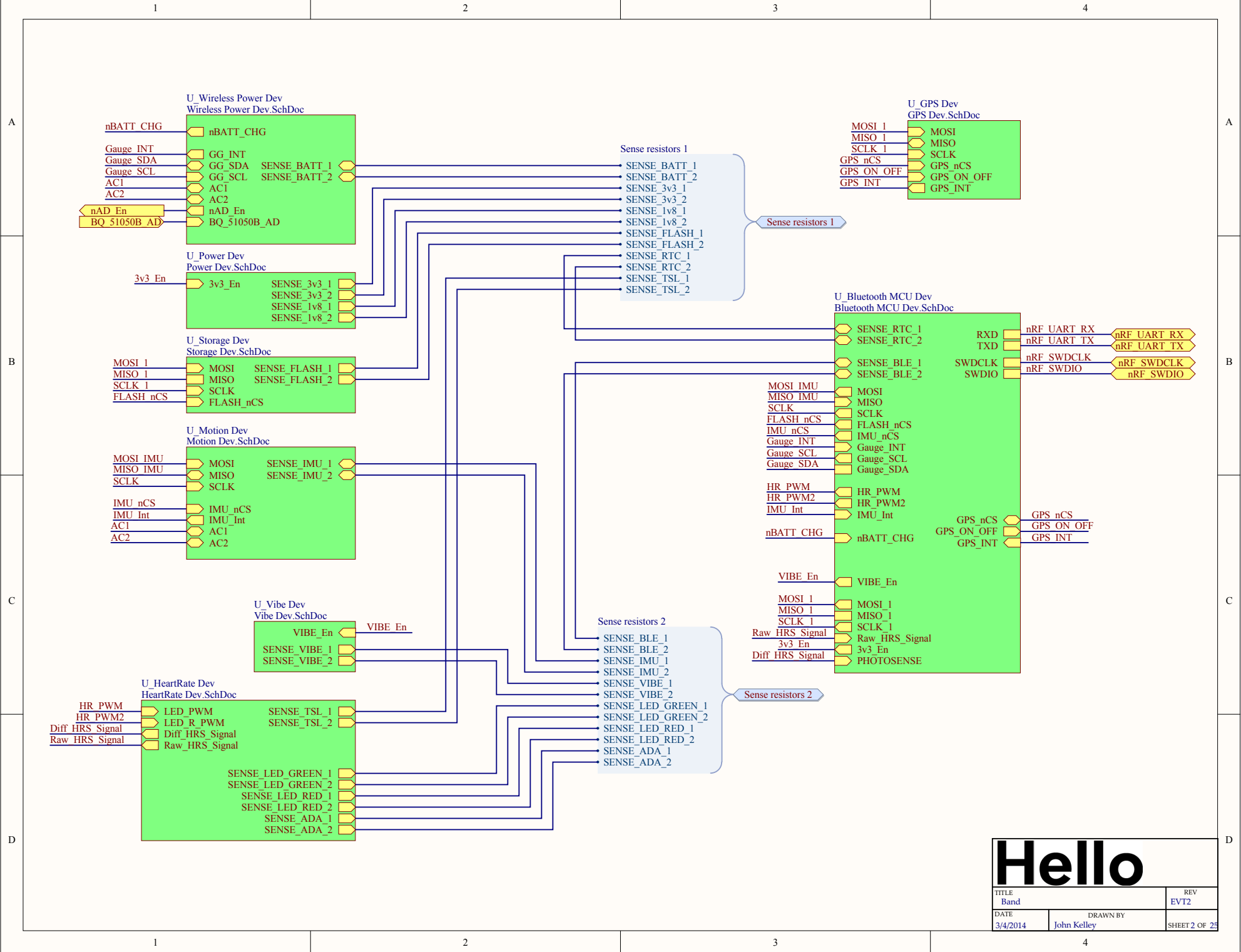
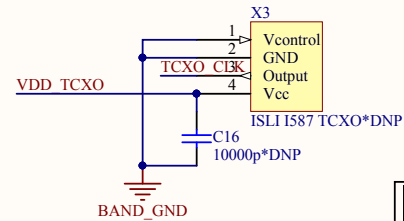


Hello

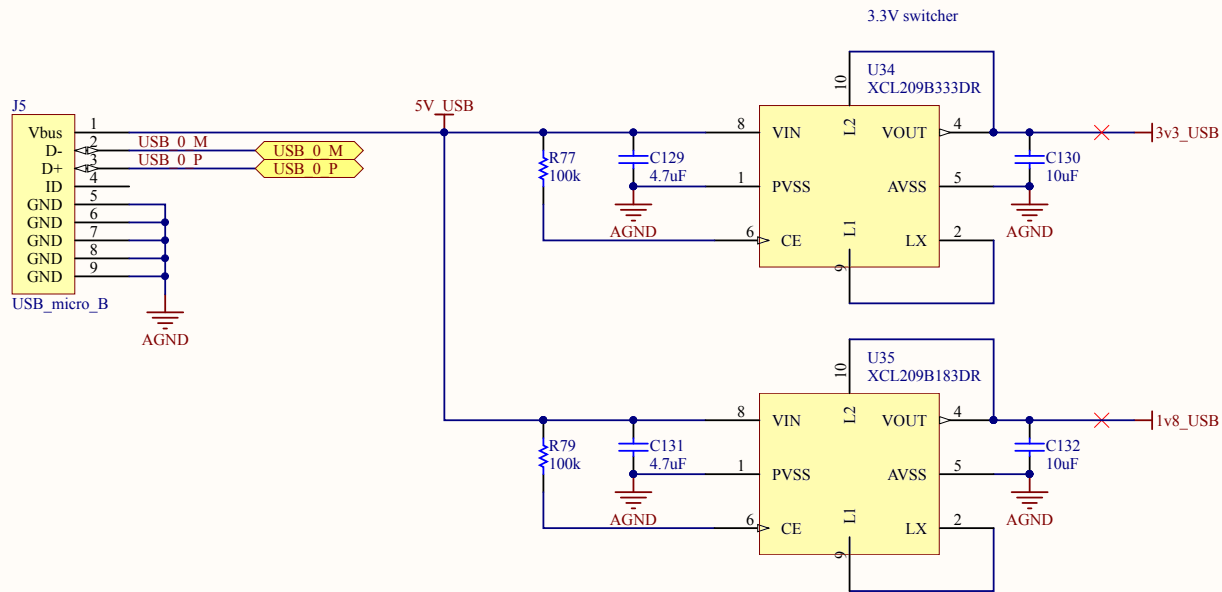
TITLE Development board		REV *
DATE 3/4/2014	DRAWN BY D. Fusi	SHEET 1 OF 25



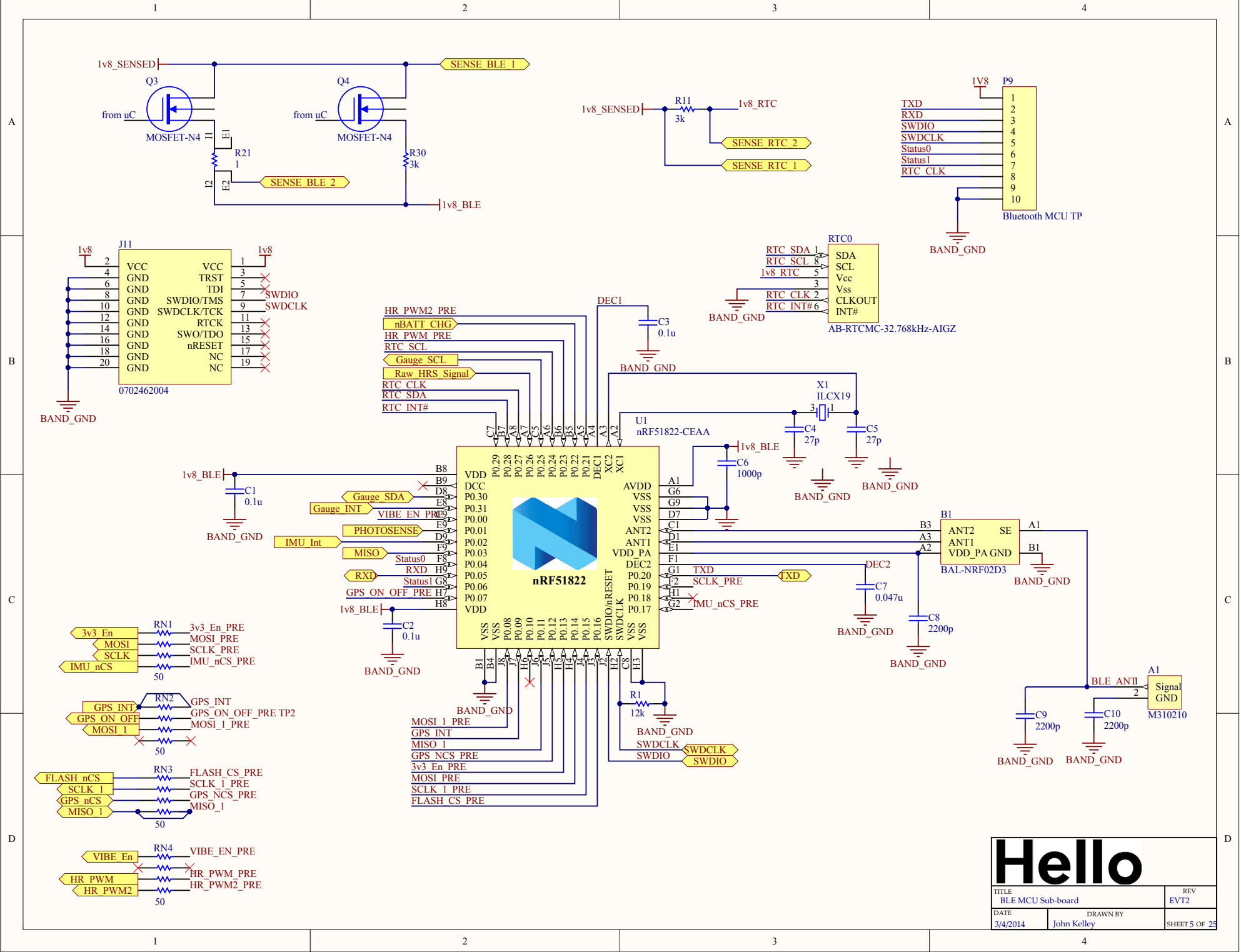




# Hello

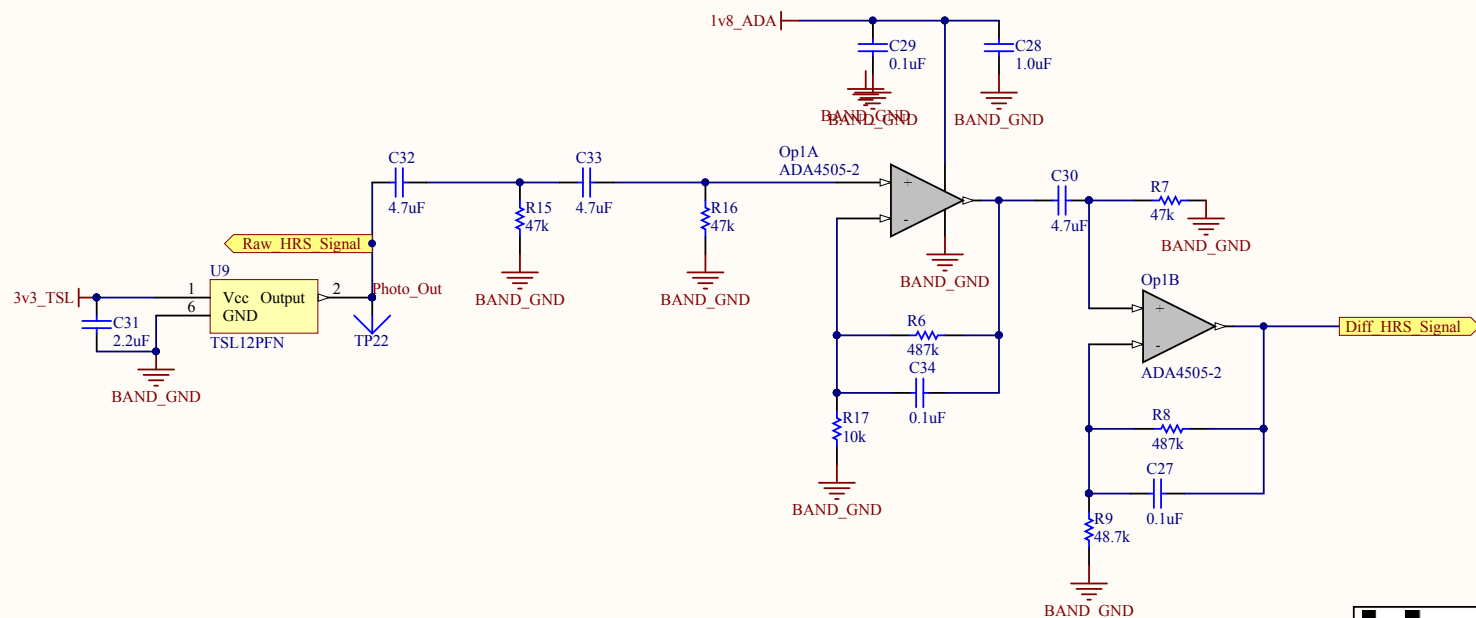
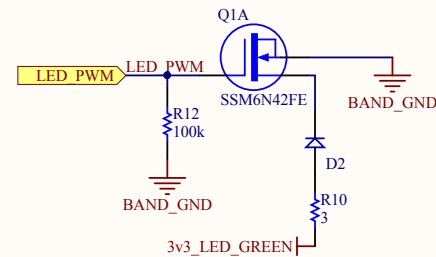
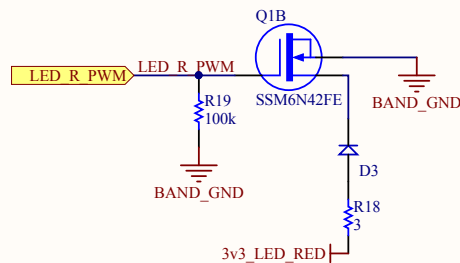
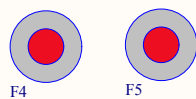
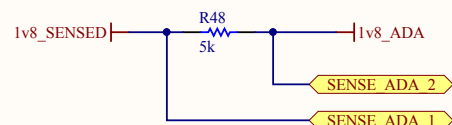
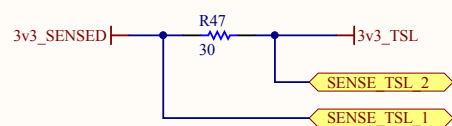
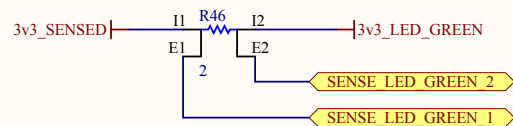
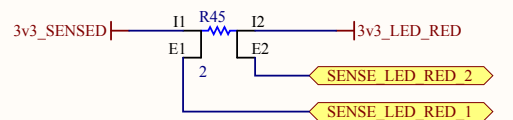


Hello		
TITLE	Dev Board Power Control	* REV
DATE	3/4/2014	D. Fusi
		SHEET 4 OF 25



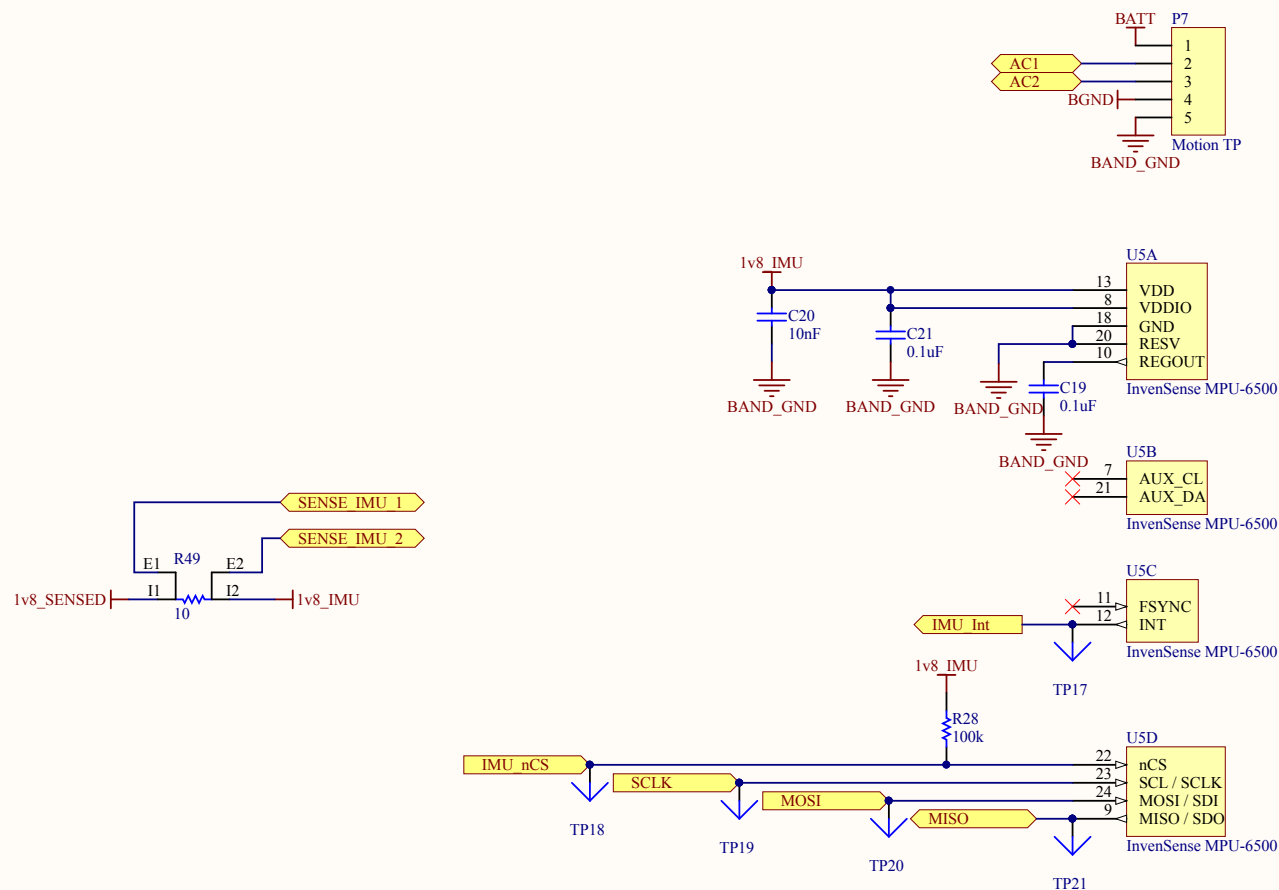
# Hello

TITLE BLE MCU Sub-board		REV EVT2
DATE 3/4/2014	DRAWN BY John Kelley	SHEET 5 OF 25



# Hello

TITLE HRS Sub-board		REV EVT2
DATE 3/4/2014	DRAWN BY John Kelley	SHEET 6 OF 25

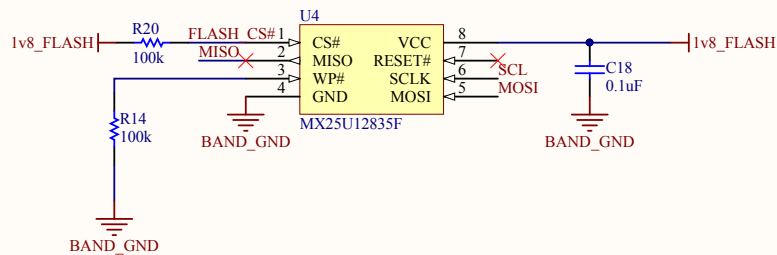
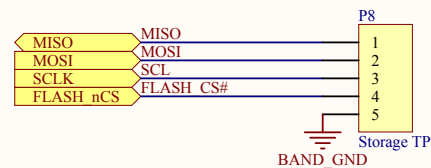
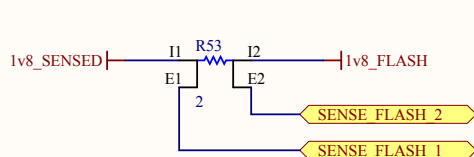


# Hello

TITLE IMU Sub-board		REV EVT2
DATE 3/4/2014	DRAWN BY John Kelley	SHEET 7 OF 25







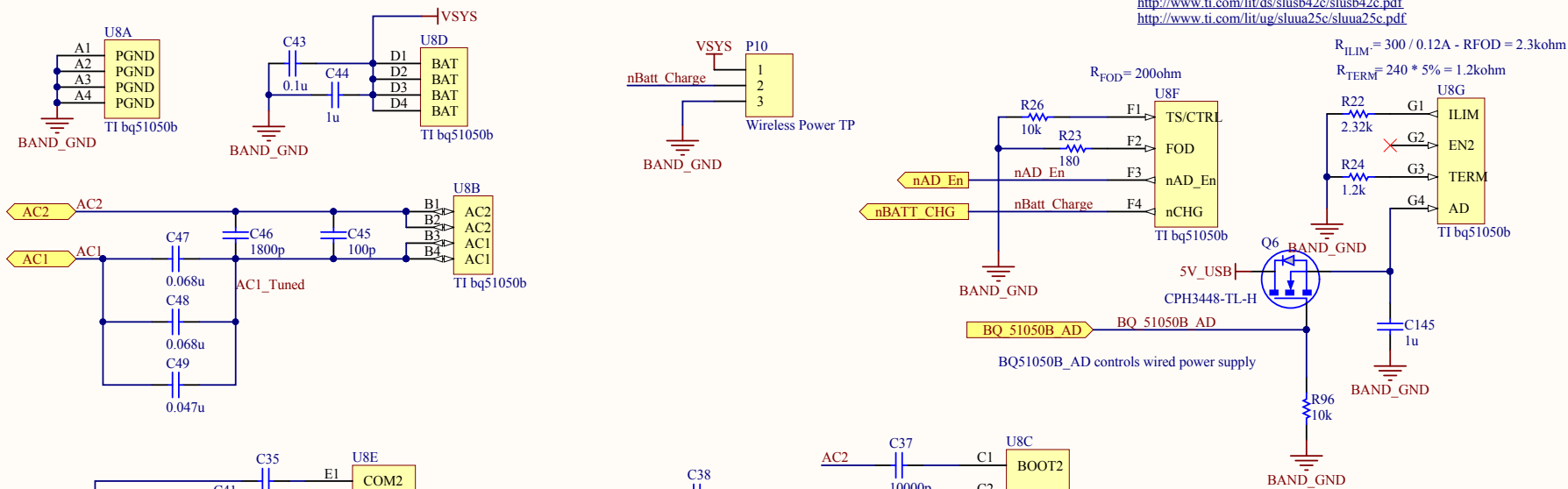
# Hello

TITLE Storage Sub-board		REV EVT2
DATE 3/4/2014	DRAWN BY John Kelley	SHEET 9 OF 25



A

A

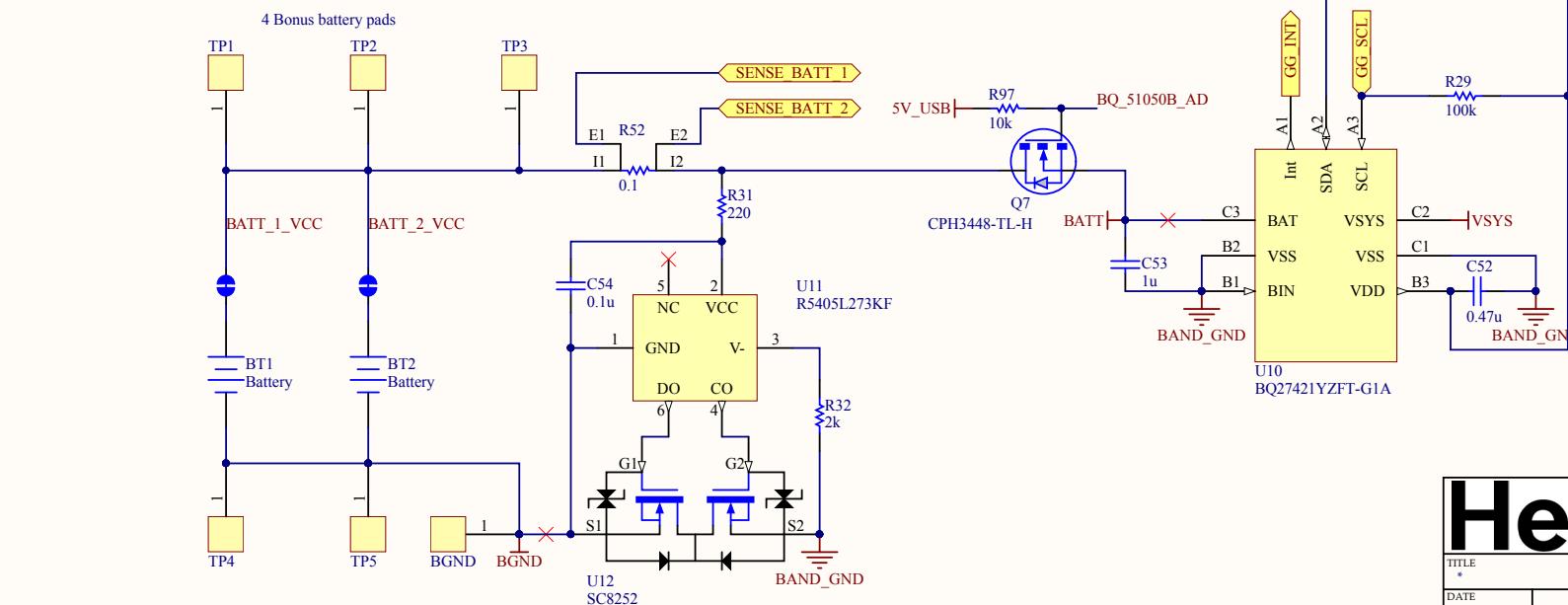


B

B

C

C

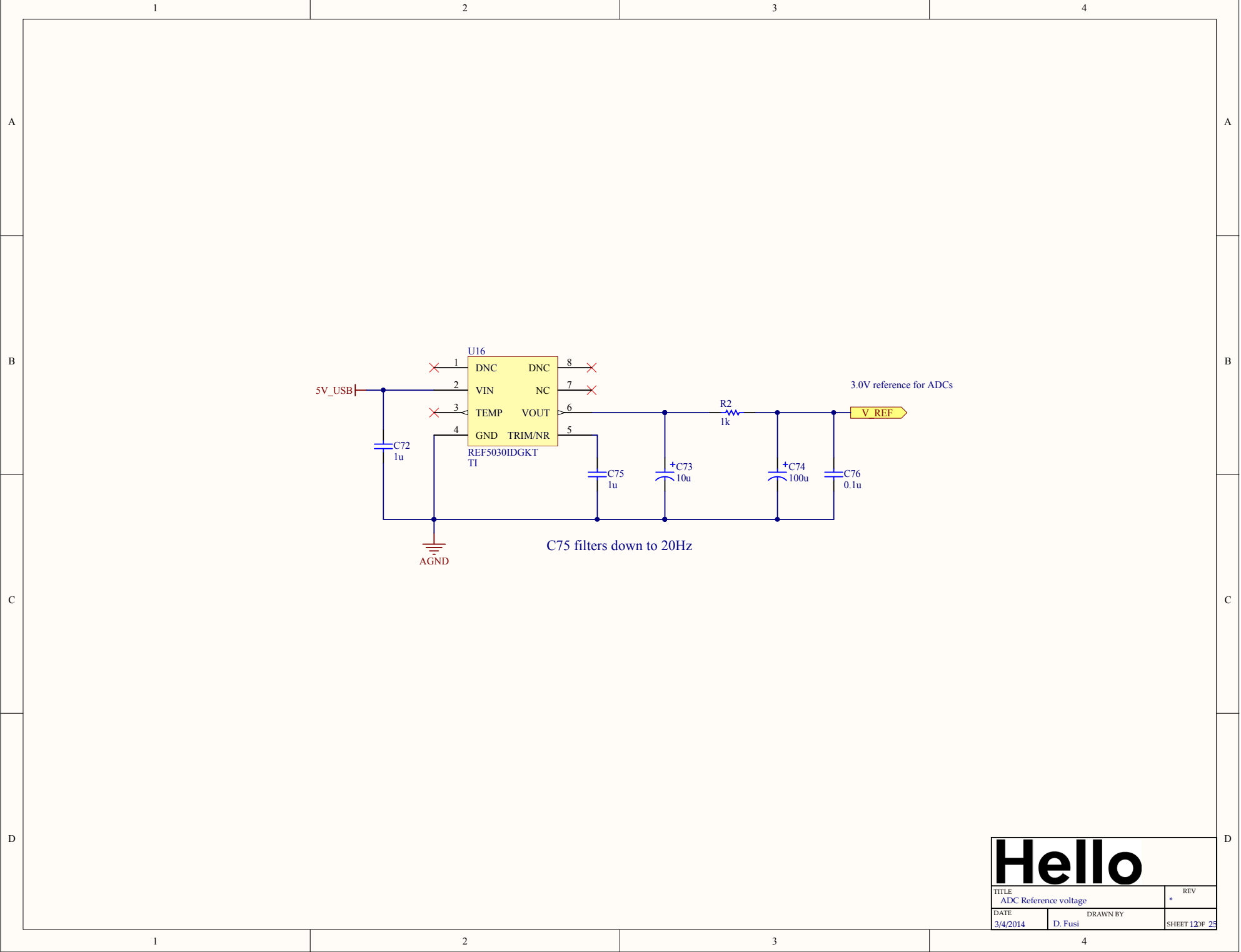


D

D

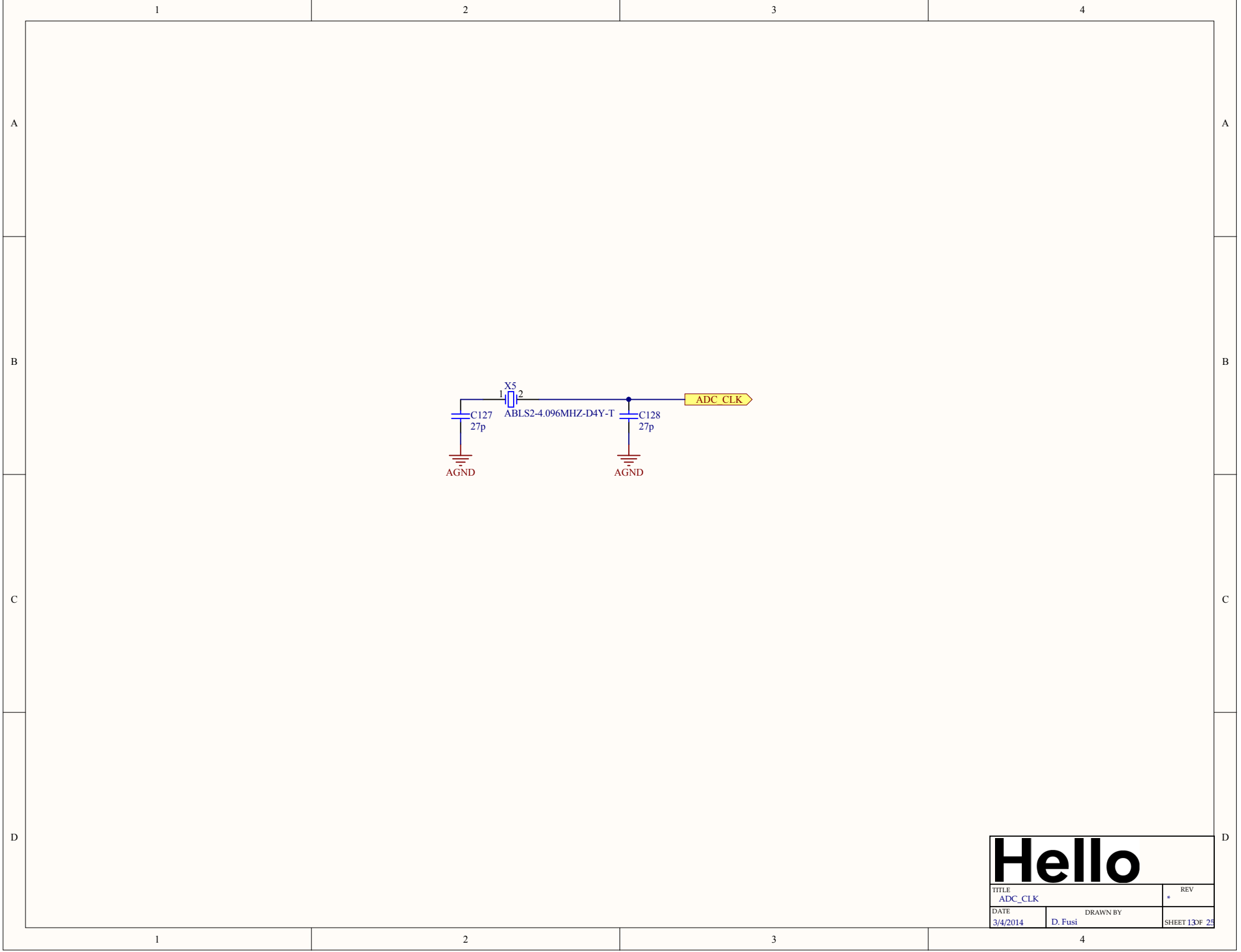
Hello

TITLE	*	REV
DATE	3/4/2014	* SHEET 1 OF 25
DRAWN BY		



Hello

TITLE ADC Reference voltage		REV
DATE 3/4/2014	DRAWN BY D. Fusi	SHEET 12 OF 25



Hello

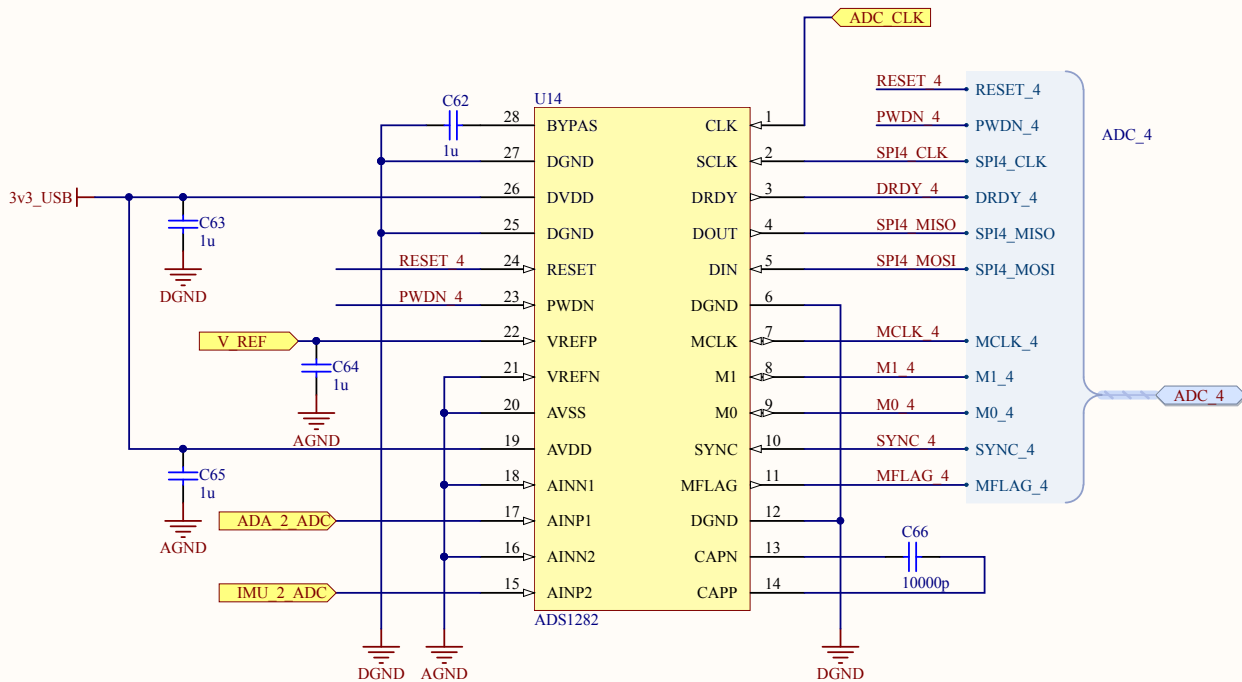
TITLE ADC_CLK		REV *
DATE 3/4/2014	DRAWN BY D. Fusi	SHEET 13 OF 25





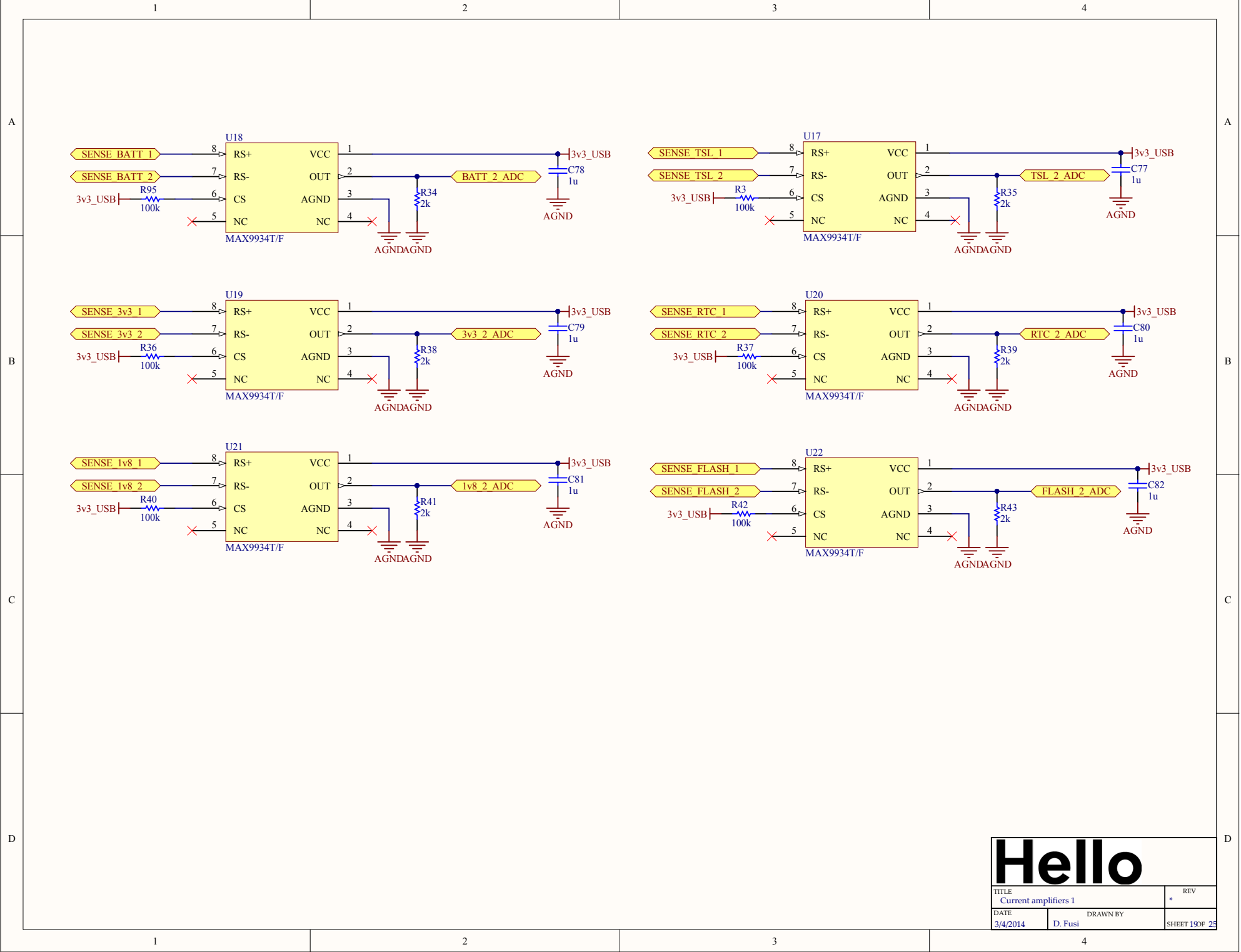






Hello		REV
TITLE	ADC4	*
DATE	3/4/2014	DRAWN BY
	D. Fusi	SHEET 17 OF 25



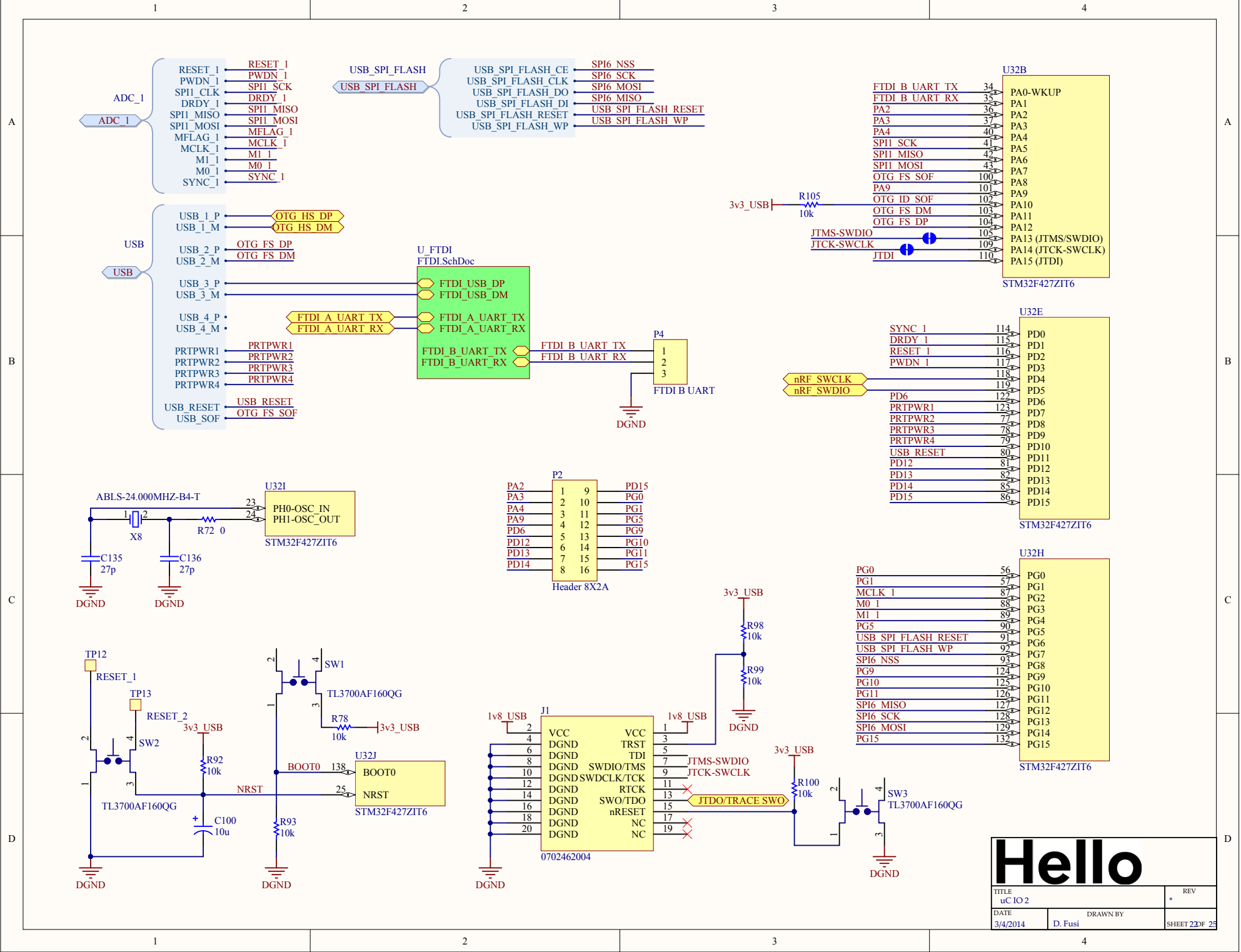


Hello

TITLE Current amplifiers 1		* REV
DATE 3/4/2014	DRAWN BY D. Fusi	SHEET 19 OF 25

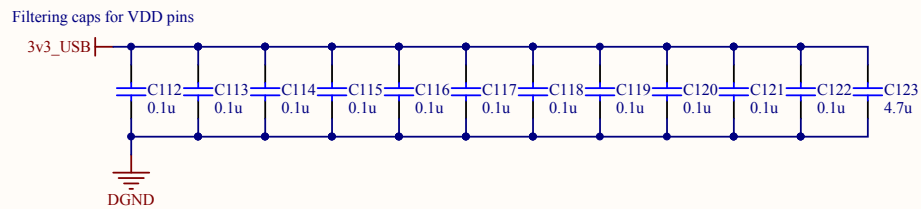






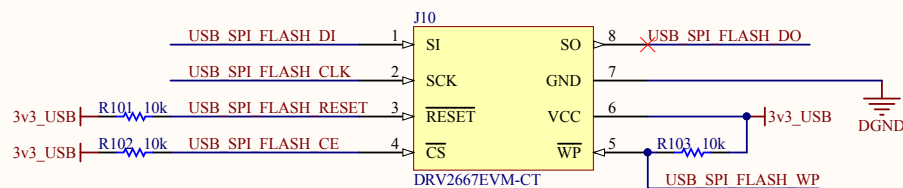
Hello

TITLE		REV
uC IO 2		*
DATE	DRAWN BY	SHEET 22 OF 25
3/4/2014	D. Fusi	



TITLE uC Power		REV *
DATE 3/4/2014	DRAWN BY D. Fusi	SHEET 23 OF 25





[http://www.ftdichip.com/Support/Documents/DataSheets/ICs/DS\\_FT2232H.pdf](http://www.ftdichip.com/Support/Documents/DataSheets/ICs/DS_FT2232H.pdf)

