

A

B

C

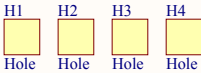
D

1

2

3

4



header / pogo pins

JTAG TCK
JTAG TMS

P5

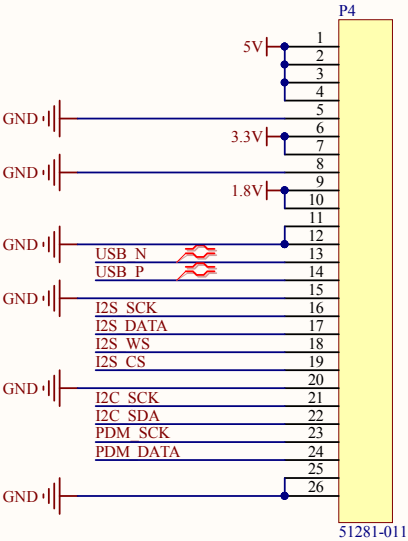
1
2

MHDR1X2

to the top board

GSPI_CS
GSPI_CLK
GSPI_MISO
GSPI_MOSI

to the bottom board



Hello

TITLE Morpheus_middle		REV 1
DATE 5/19/2014	DRAWN BY D. Fusi	SHEET 2 OF 4

1

2

3

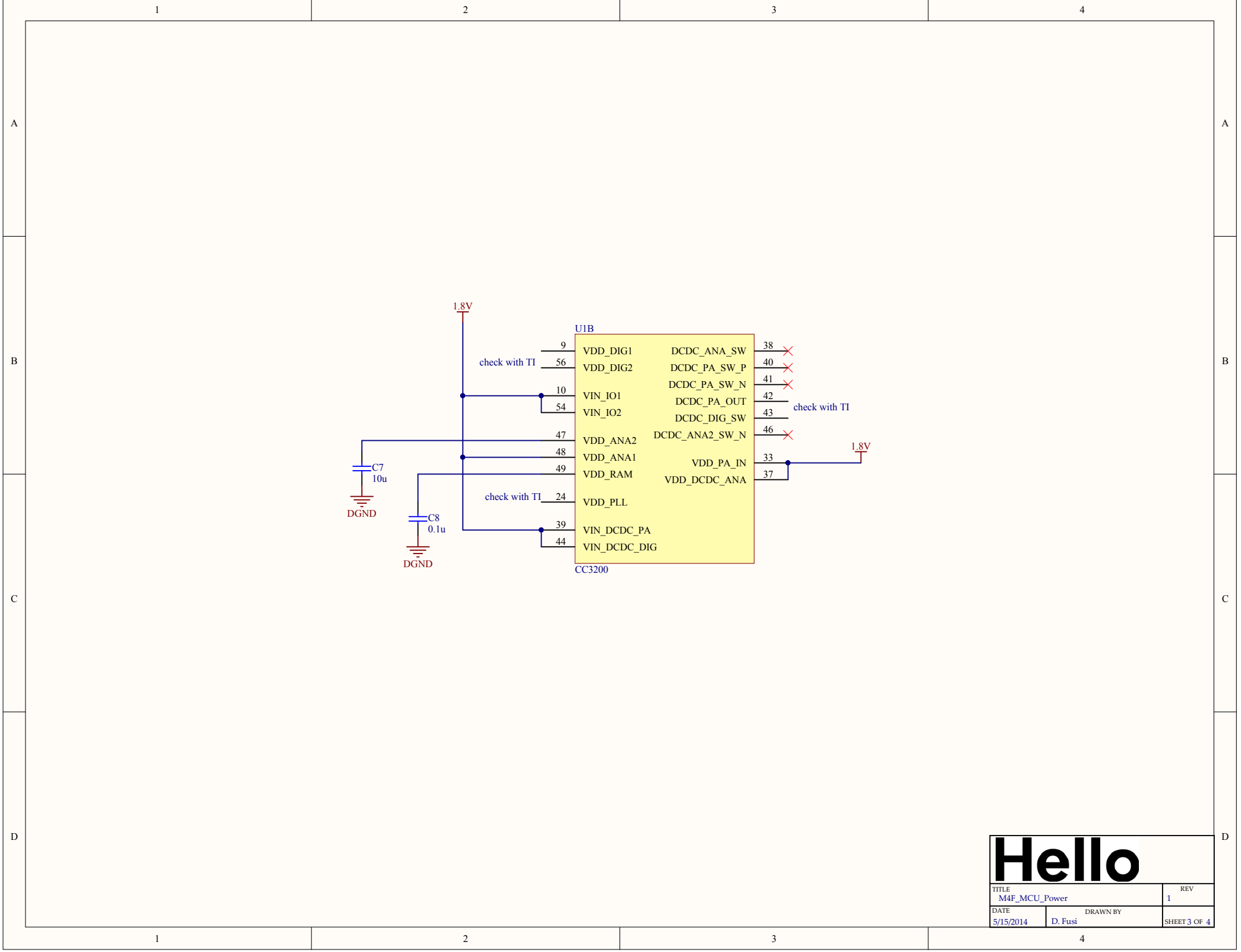
4

A

B

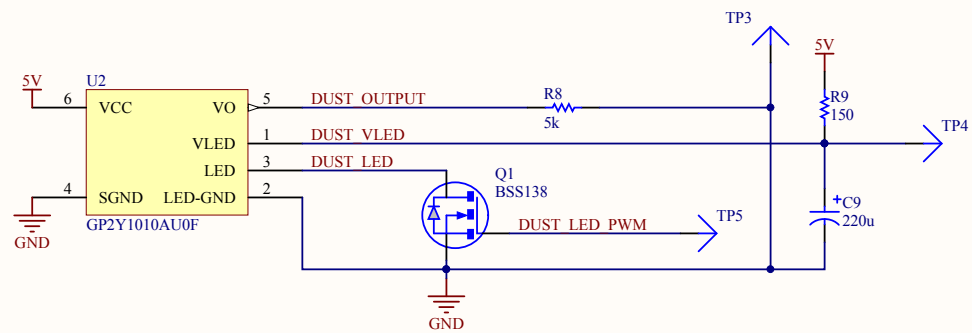
C

D

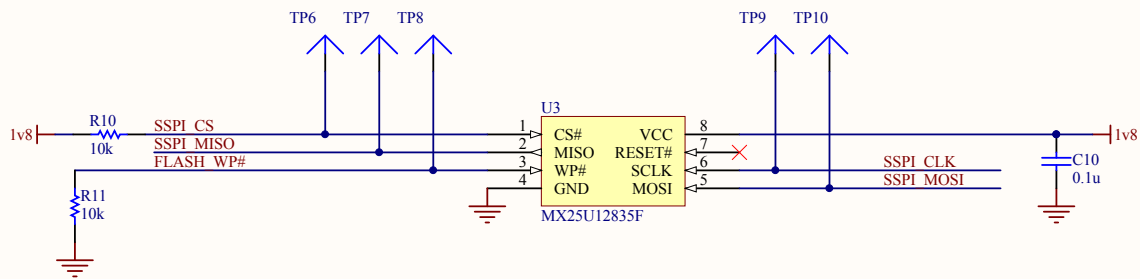


Hello

TITLE		REV
M4F_MCU_Power		1
DATE	DRAWN BY	SHEET 3 OF 4
5/15/2014	D. Fusi	



Hello		
TITLE Air_quality		REV 1
DATE 5/15/2014	DRAWN BY D. Fusi	SHEET 4 OF 4



Hello

TITLE Flash memory		REV 1
DATE 5/19/2014	DRAWN BY D. Fusi	SHEET 6 OF 6