

OSDT Community x Shanghai RISC-V Meetup

RISC-V <3 V8 Meetup (Webinar)

- 2020年7月19日 周日 11:00 AM - 12:00 AM
- Sunday, July 19, 2020 at 11:00:00 am CST UTC+8 hours Shanghai
- Saturday, July 18, 2020 at 8:00:00 pm PDT UTC-7 hours Las Vegas (USA - Nevada)
- Saturday, July 18, 2020 at 11:00:00 pm EDT UTC-4 hours New York
- Sunday, July 19, 2020 at 03:00:00 AM Corresponding UTC (GMT)



Add V8 JS Engine to the list #41

Merged

mgielda merged 1 commit into riscv:master from lazyparser:add-plct-v8 3 days ago

Conversation 0

Commits 1

Checks 0

Files changed 1



lazyparser commented 4 days ago

Contributor ...

The PLCT lab is excited to announce that we have reached the first milestone of the V8 RISC-V porting project. It can run a single line helloworld.js on RISCV64 platform now.

Although the source code is kinda messy and incomplete, we've decided to open source the ongoing work for discussion. It might be good for RISC-V community to list this ongoing project in riscv/riscv-software-list. Further, we are aiming to contribute this work to v8 upstream.

-o- Add V8 JS Engine

d3d8d64



✓ arunthomas approved these changes 3 days ago

[View changes](#)

hg mgielda merged commit 9d4ae64 into riscv:master 3 days ago

List another V8-riscv port on status page #42

Merged

mgielda merged 1 commit into `riscv:master` from `penguinwu:v8-status-update-patch-1` 2 days ago

Conversation 0

Commits 1

Checks 0

Files changed 1



penguinwu commented 3 days ago

Contributor ...

We (RIOS lab and Futurewei Technologies) just open-sourced a V8 RISCV port (RV64GC). We have completed the basic porting needed for a 64-bit RISC-V backend. And currently, our port passes over 15,000 V8 standard tests (incl. both JS and Web Assembly) using the v8-riscv simulated build.

The project is still on-going, and we would love to engage the community. Through our initial porting work, we have established a sustainable porting methodology and development best practices that we feel confident to invite broader community participation. Plenty of work is still needed for a complete and high-performing V8 on RISC-V. We would like to leverage the foundation to publicize our work, thus request to add our port to the tool's page.

For more details, please check out: <https://github.com/v8-riscv/v8/wiki> and <https://github.com/v8-riscv/v8>.

List another V8-riscv port on status page ...

Verified

2465f45

mgielda merged commit `434f414` into `riscv:master` 2 days ago

Home

Peng Wu edited this page 20 hours ago · 15 revisions

Welcome to the v8-riscv wiki

This is an on-going project to develop the RISC-V backend for the V8 JavaScript Engine. After a few months of intensive development, we have built a sufficient MVP (Minimal Viable Product) for the RISC-V64-bit port, which currently passes over 90% (10,000+) standard V8 test cases using v8-riscv64 simulated build. We have also established a sustainable porting methodology and development best practices that we feel confident invite a broader community participation. We welcome you joining our development effort. Plenty of supports are still needed for a complete and high-performing V8 on RISC-V.

This repo will be the community home for some time before the code-base is upstream to the V8 community. For general V8 information, see [V8 Dev](#). The rest of the wiki is specific to the RISC-V V8 backend.

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United as ONE Community

Contributions are Welcome!

THE ROADMAP

PENG WU (FUTUREWEI TECHNOLOGIES)

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