

David Jackson

New York, USA

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Education

Columbia University

Master of Science in Computer Engineering

Sep 2021 – May 2023 | New York, USA

GPA: 3.7/4.0

Activities: IEEE Graduate Student Chapter, Startup Weekend Mentor, Teaching Assistant

Relevant Coursework: VLSI Design, Embedded Systems, FPGA Architecture, Digital Signal Processing, Hardware Security

Summer School: UC Berkeley 2022 (GPA: 3.9/4.0), Georgia Tech 2023 (GPA: 3.8/4.0)

Work Experience

Hardware Engineer | NVIDIA | Jun 2023 - Present | Santa Clara, CA, USA

- Designed ASIC components in Verilog for GPU acceleration.
- Developed synthesis scripts and timing constraints for automated flows.
- Collaborated on RTL verification using UVM.

Skills

Hardware Description: Verilog, VHDL

Tools: Synopsys Design Suite, Vivado

Languages: Python, C

Other Skills: ASIC Design, Timing Analysis, RTL Verification, Hardware Security

Certificates

Cadence Certified Digital IC Designer

Coursera: FPGA Computing Systems

Udemy: Verilog HDL Bootcamp

Awards

NVIDIA Innovation Award 2024

Columbia Engineering Fellowship
IEEE Student Design Contest Winner