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Unit 11

Intermediate Code Generation

Summary

- **Three-Address Code**
- **Code for Assignments**
- **Code for Boolean Expressions**
- **Code for Flow-of-Control Statements**

Introduction

- A source program can be translated into intermediate code by intermediate code generator
- Intermediate code is machine-independent

Benefits of Intermediate Code

- Retargetting is facilitated
- A machine-independent code optimizer can be applied to intermediate representation

Intermediate Code

- To be done by an Intermediate Code Generator.
- Intermediate representation is decided by compiler designer
- Typical intermediate representations are:
 - Syntax Tree
 - Postfix Notation
 - Three Address Code

Intermediate Code

- As **intermediate code** we consider the *three-address code*, similar to assembly: sequence of instructions with at most *three* operands such that:
 1. There is at most one operator, in addition to the assignment (we make explicit the operators precedence).
 2. The general form is: $x := y \text{ op } z$
- x, y, z are called **addresses**, i.e. either identifiers, constants or **compiler-generated** temporary names.
 - Temporary names must be generated to compute **intermediate** operations.
 - Addresses are implemented as pointers to their symbol-table entries

Three Address Code of $t := x + y * z$

- $t_1 := y * z$
- $t := x + t_1$

Types of Three-Address Statements

- Three-Address statements are akin to assembly **code**: Statements can have *labels*
- There are statements for flow-of-control.
 1. *Assignment Statements*: $x := y \text{ op } z$.
 2. *Unary Assignment Statements*: $x := \text{op } y$.
 3. *Copy Statements*: $x := y$.
 4. *Unconditional Jump*: `goto L`, with L a label of a statement.
 5. *Conditional Jump*: `if x relop y goto L`.

Types of Three-Address Statements

- *Procedure Call*: param x , and call p,n for calling a procedure, p , with n parameters. return y is the returned value of the procedure:
 param x_1
 param x_2
 ...
 param x_n
 Call p,n
- *Indexed assignments*: $x := y[i]$ or $x[i] := y$. Note: $x[i]$ denotes the value in the location i memory units beyond location x .

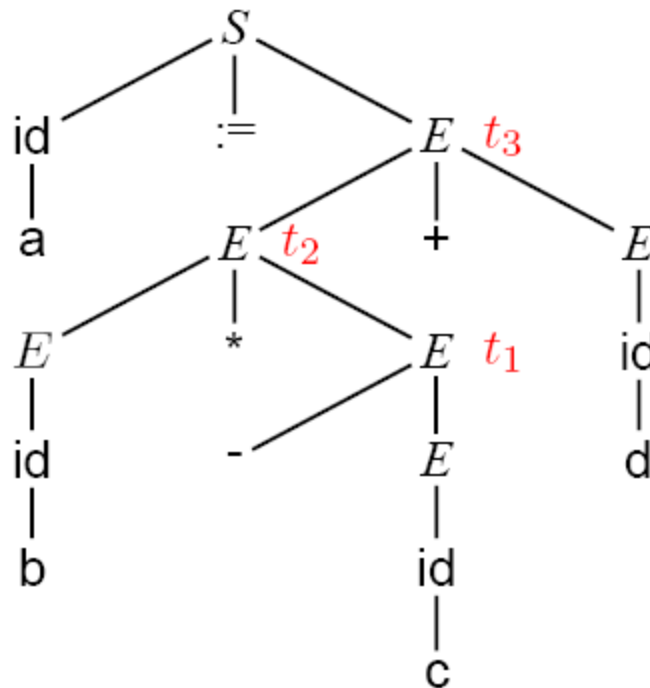
Syntax Directed Definition into Three Address Code

- The synthesized S.code represents the three address code
- Template names are generated for intermediate calculations
- The nonterminal E has two attributes
- E.place the name that will hold the value of E
- E.code the sequence of three-address statements evaluating E
- The function newtemp returns a sequence of distinct names t1, t2, . .
- The function *gen* generates three-address **code** such that:
 1. Everything quoted is taken literally;
 2. The rest is evaluated.
- In practice, **code** can be sent to **an output file** instead of being assigned to the *code* attribute.

SDD to Produce Three Address Code for Assignments

<u>Productions</u>	<u>Semantic Rules</u>
$S \rightarrow \text{id} := E$	$\{ S.code = E.code \parallel gen(id.place \text{ ':=' } E.place) \}$
$E \rightarrow E_1 + E_2$	$\{ E.place = newtemp ;$ $E.code = E_1.code \parallel E_2.code \parallel$ $\parallel gen(E.place \text{ ':=' } E_1.place \text{ '+' } E_2.place) \}$
$E \rightarrow E_1 * E_2$	$\{ E.place = newtemp ;$ $E.code = E_1.code \parallel E_2.code \parallel$ $\parallel gen(E.place \text{ ':=' } E_1.place \text{ '*' } E_2.place) \}$
$E \rightarrow - E_1$	$\{ E.place = newtemp ;$ $E.code = E_1.code \parallel$ $\parallel gen(E.place \text{ ':=' 'uminus' } E_1.place) \}$
$E \rightarrow (E_1)$	$\{ E.place = E_1.place ; E.code = E_1.code \}$
$E \rightarrow \text{id}$	$\{ E.place = id.place ; E.code = ' ' \}$

Three Address Code of $a := b * -c + d$



$t_1 := \text{uminus } c$

$t_2 := b * t_1$

$t_3 := t_2 + d$

$a := t_3$

Implementation of Three Address Statements

Quadruples

$t_1 := -c$

$t_2 := b * t_1$

$t_3 := -c$

$t_4 := b * t_3$

$t_5 := t_2 + t_4$

$a := t_5$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>	<i>result</i>
(0)	uminus	c		t_1
(1)	*	b	t_1	t_2
(2)	uminus	c		t_3
(3)	*	b	t_3	t_4
(4)	+	t_2	t_4	t_5
(5)	:=	t_5		a

Template names must be inserted into the symbol table when they are generated.

Implementation of Three Address Statements

- Triples

$t_1 := -c$

$t_2 := b * t_1$

$t_3 := -c$

$t_4 := b * t_3$

$t_5 := t_2 + t_4$

$a := t_5$

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	uminus	c	
(1)	*	b	(0)
(2)	uminus	c	
(3)	*	b	(2)
(4)	+	(1)	(3)
(5)	assign	a	(4)

Template names are not inserted into symbol tables

More Triple Representations

- Example
 $x[i] := y$

$x := y[i]$

- Use triple structures

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	[]	x	i
(1)	:=	(0)	y

	<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	[]	y	i
(1)	:=	x	(0)

Implementation of Three Address Statements

- Indirect triples is considered a listing of pointers to triples.

	<i>op</i>		<i>op</i>	<i>arg1</i>	<i>arg2</i>
(0)	(14)	(14)	uminus	c	
(1)	(15)	(15)	*	b	(14)
(2)	(16)	(16)	uminus	c	
(3)	(17)	(17)	*	b	(16)
(4)	(18)	(18)	+	(15)	(17)
(5)	(19)	(19)	assign	a	(18)

Intermediate Code for Declarations

offset is a global variable can keep track of the next available relative address.

Attribute *Type* represent a type expression constructed from basic types,

attribute *Width* indicate number of memory units taken by object of that type

Productions

Semantic Rules

$P \rightarrow M D$

{ }

$M \rightarrow \varepsilon$

{*offset*:=0 }

$D \rightarrow D; D$

$D \rightarrow \text{id} : T$

{ *enter*(*id.name*, *T.type*, *offset*)

offset:=*offset* + *T.width* }

$T \rightarrow \text{integer}$

{*T.type* = *integer*; *T.width* = 4 }

$T \rightarrow \text{real}$

{*T.type* =*real*; *T.width* = 8 }

$T \rightarrow \text{array} [\text{num}] \text{ of } T_1$

{*T.type*=*array*(1..*num.val*,*T₁.type*)

T.width = *num.val* * *T₁.width*}

Keeping track of scope information

- In a language with nested procedure, when a nested procedure is seen, processing of declarations in the enclosing procedure is temporarily suspended
- Grammar for this type of declaration:

$$P \rightarrow D$$
$$D \rightarrow D; D \mid \text{id} : T \mid \text{proc id} ; D ; S$$

should be added more semantic rules

- A new table is created when a procedure declaration $D \rightarrow \text{proc id } D1;$ is seen

Nested procedures for quick sorting

Program sort;

1) Var a: array[0..10] of integer;

2) x: integer;

3) Procedure readarray;

4) Var i: integer;

5) Begin ...a... end {readarray};

6) Procedure exchange(i, j: integer);

7) Begin {exchange} end;

8) Procedure quicksort(m, n: integer);

9) Var k, v: integer;

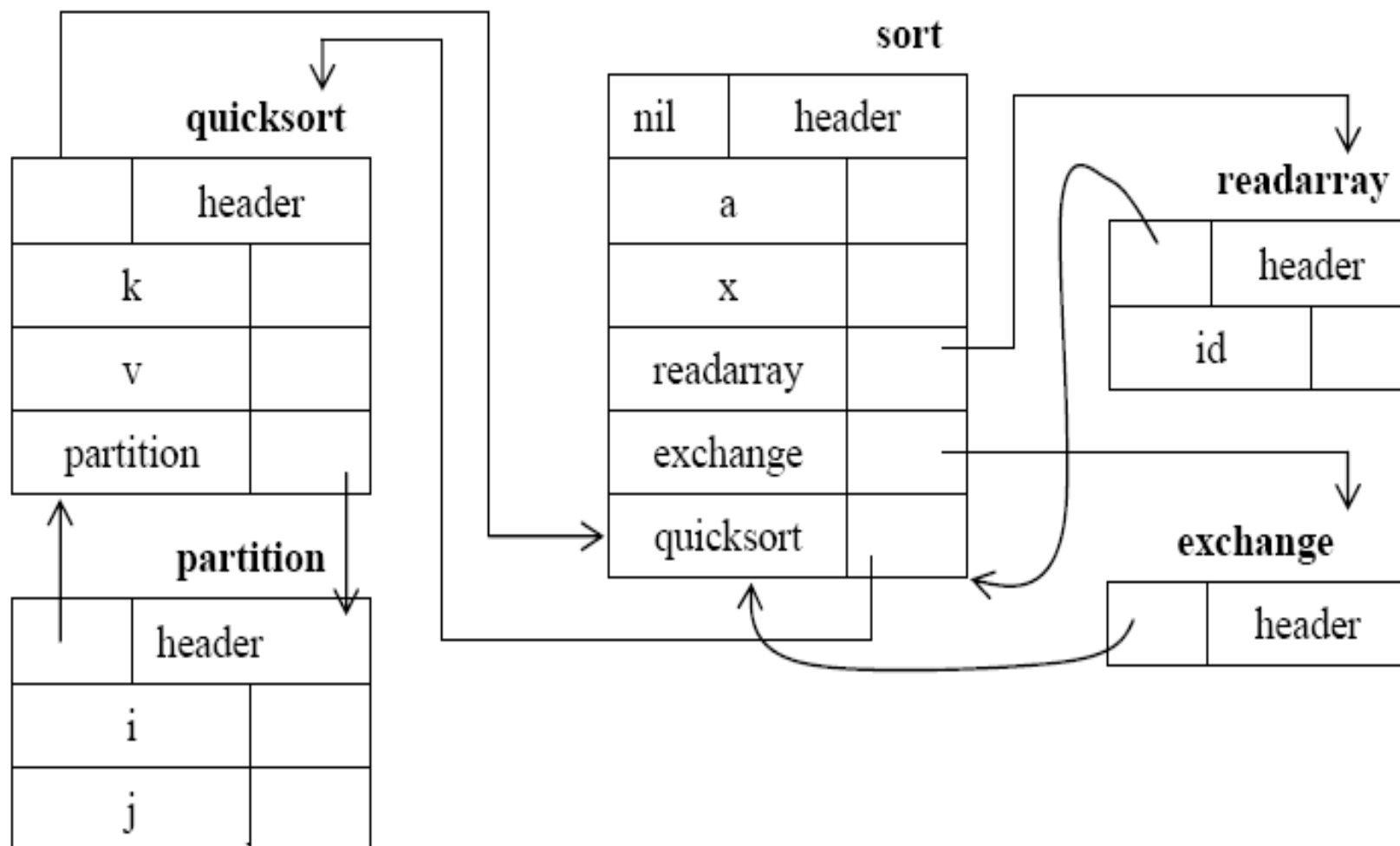
10) Function partition(y,z: integer): integer;

11) Begin ..a..v..exchange(i,j) end;
{partition}

12) Begin ... end; {quicksort}

13)Begin ... end; {sort}

Five Symbol Tables of Sort



Procedures of semantic rules

mktable(previous) – creates a new symbol table and returns a pointer to the new table. The argument *previous* point to previously created symbol table.

enter(table,name,type,offset) – creates a new entry for name *name* in the symbol table pointed to by *table*, places *type* and relative address *offset* in fields within the entry

enterproc(table,name,newbtable) – creates a new entry for procedure *name* in the symbol table pointed to by *table*. The argument *newtable* points to the symbol table for this procedure *name*

addwidth(table,width) – records the cumulative width of all the entries in table in the header associate with this symbol table.

Declaration Processing in Nested Procedures

$P \rightarrow M D$ { *addwidth*(*top*(tblptr), *top*(offset)); *pop*(tblptr); *pop*(offset) }

$M \rightarrow \varepsilon$ { *t*:=*mktable*(null); *push*(*t*, tblptr); *push*(0, offset)}

$D \rightarrow D_1 ; D_2$

$D \rightarrow \text{proc id} ; N D_1 ; S$ { *t*:=*top*(tblptr); *addwidth*(*t*,*top*(offset));
 pop(tblptr); *pop*(offset);
 enterproc(*top*(tblptr), *id.name*, *t*)}

$N \rightarrow \varepsilon$ {*t*:=*mktable*(*top*(tblptr)); *push*(*t*,tblptr); *push*(0,offset);}

$D \rightarrow \text{id} : T$ {*enter*(*top*(tblptr), *id.name*, *T.type*, *top*(offset);
 top(offset):=*top*(offset) + *T.width*

Syntax directed definition for assignment statements

$S \rightarrow id := E$ $\{ p := \text{lookup}(id.name);$
 $\text{if } p \neq \text{nil then emit}(p := E.place) \text{ else error} \}$

$E \rightarrow E_1 + E_2$ $\{ E.place := \text{newtemp};$
 $\text{emit}(E.place := E_1.place + E_2.place) \}$

$E \rightarrow E_1 * E_2$ $\{ E.place := \text{newtemp};$
 $\text{emit}(E.place := E_1.place * E_2.place) \}$

$E \rightarrow - E_1$ $\{ E.place := \text{newtemp};$
 $\text{emit}(E.place := \text{'unimus'} E_1.place) \}$

$E \rightarrow (E_1)$ $\{ E.place := E_1.place \}$

$E \rightarrow id$ $\{ p := \text{lookup}(id.name);$
 $\text{if } p \neq \text{nil then } E.place := p \text{ else error} \}$

Names in the symbol table

- Names in an assignment generated by S must have been declared in either the procedure that S appears in or in some enclosing procedures.
- The *lookup* operation first check if there is an entry for attribute *id.name* in the symbol table. If so, a pointer to the entry is returned. If the name cannot be found, then lookup returns nil.
- Emit procedure used to emit three-address statements to an output file rather than building up *code* attributes for some nonterminals. Translation can be done by emitting to an output file if the *code* attributes of the nonterminals on the left sides of productions are formed by concatenating the code attributes of the nonterminals on the right side, perhaps with some additional strings in between

Names in the symbol table (cont'd)

- Consider productions: $D \rightarrow \text{proc id}; ND_1; S$
- Names in an assignment generated by S must have been declared in either the procedure that S appears in or in some enclosing procedure.
- When apply to *name*, lookup operation checks if name appears in the current symbol table accessible through $top(tblptr)$. If not, lookup uses the pointer in the header of the table to find the symbol table for the enclosing procedure and looks for the name there. If the name cannot be found in any of these scopes, *lookup* will return nil

Addressing array element

- Elements of an array can be accessed quickly if the elements are stored in a block of consecutive locations. If the width of each array element is w then the i^{th} element of array A begins in location:
- $A[i] = \text{base} + (i - \text{low}) * w$
- Where:
 - Low is the lower bound of the subscript
 - Base is the relative address of the storage allocated for the array (the relative address of $A[\text{low}]$)
- $A[i] = i * w + (\text{base} - \text{low} * w)$
- Where $c = \text{base} - \text{low} * w$ can be evaluated when the declaration of the array is seen. Assume c is saved in the symbol table entry for A ,
- $\Rightarrow A[i] = i * w + c$

Layouts for a two dimensional array

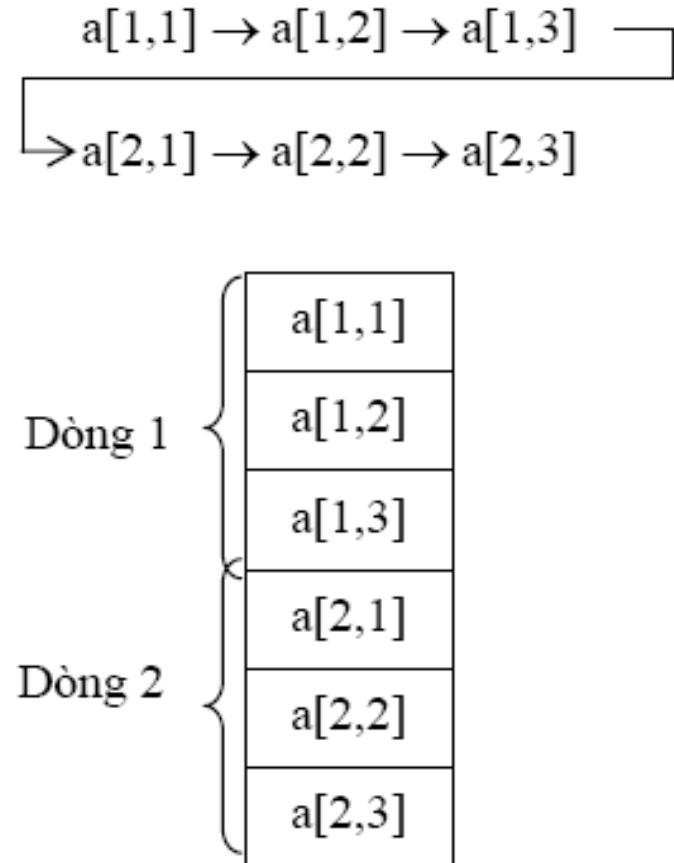
- By row

Relative address of $A[i_1, i_2] =$

$\text{base} + ((i_1 - \text{low}_1) * n_2 + i_2 - \text{low}_2) * w$

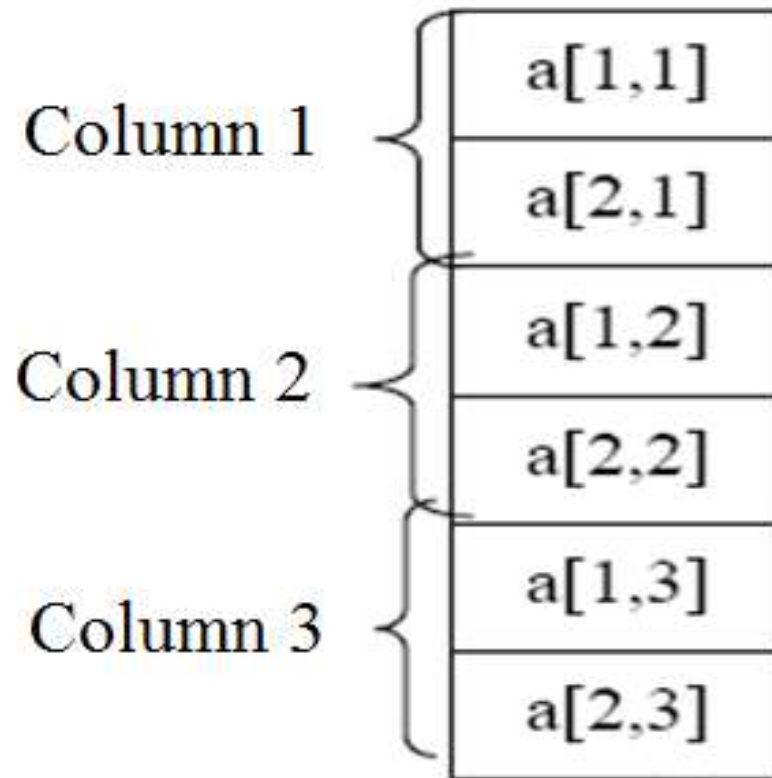
$\text{low}_1, \text{low}_2$: lower bound of i_1 và i_2

n_2 : the number of values that i_2 can take. If high_2 is upper bound of i_2 then $n_2 = \text{high}_2 - \text{low}_2 + 1$



Layouts for a two dimensional array

- By column



Boolean Expressions

- **Boolean Expressions** are used to either compute logical values or as conditional expressions in flow-of-control statements.
- We consider Boolean Expressions with the following grammar:
- $E \rightarrow E \text{ or } E \mid E \text{ and } E \mid \text{not } E \mid (E) \mid \text{id relop id} \mid \text{true} \mid \text{false}$
- There are two methods to evaluate Boolean Expressions
 1. *Numerical Representation*. Encode true with '1' and false with '0' and we proceed analogously to arithmetic expressions.
 2. *Jumping Code*. We represent the value of a Boolean Expression by a position reached in a program.

Numerical Representation of Boolean Expressions

- Expressions will be evaluated from left to right assuming that: or and are left-associative, and that or has lowest precedence, then and, then not
- Example 1: The translation for *a or b and not c* is
 - t1 = not c
 - t2 = b and t1
 - t3 = a or t2
- A relational expression such as $a < b$ is equivalent to the conditional statement if $a < b$ then 1 else 0 and its translation involves *jumps to labeled statements*:
- Example 2
 - 100: if $a < b$ goto 103
 - 101: t:=0
 - 102: goto 104
 - 103: t:= 1
 - 104:

Jumping Code for Boolean Expressions

- The value of a Boolean Expression is represented by a position in the **code**.
- Consider Example 2: We can tell what value t will have by whether we reach statement 101 or statement 103.
- Jumping **code** is extremely useful when Boolean Expressions are in the context of flow-of-control statements.

Translating Boolean expressions

- If E has form : $a < b$ the the code generated is
If $a < b$ then goto E.true else goto E.false
- If E has form: E1 or E2 then
 - If E1 is true then E is true
 - If E1 is false then evaluate E2; Value of E is true or false depends on E2
- Similarly for E1 and E2

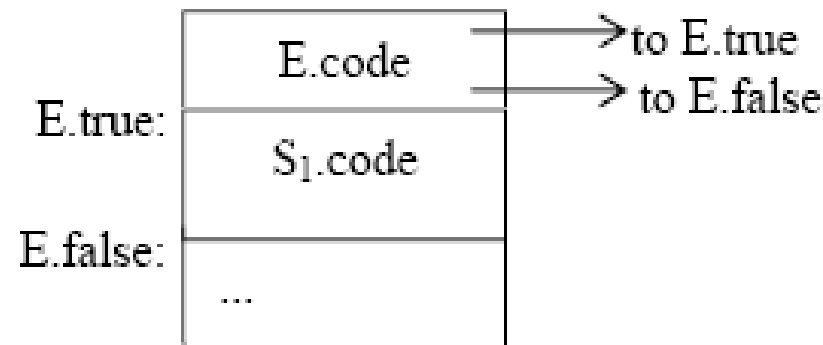
Translating Boolean expressions

PRODUCTION	SEMANTIC RULES
$E \rightarrow E_1 \text{ or } E_2$	$E_1.true := E.true;$ $E_1.false := newlabel;$ $E_2.true := E.true;$ $E_2.false := E.false;$ $E.code := E_1.code \parallel gen(E.false ':') \parallel E_2.code$
$E \rightarrow E_1 \text{ and } E_2$	$E_1.true := newlabel;$ $E_1.false := E.false;$ $E_2.true := E.true;$ $E_2.false := E.false;$ $E.code := E_1.code \parallel gen(E.true ':') \parallel E_2.code$
$E \rightarrow \text{not } E_1$	$E_1.true := E.false;$ $E_1.false := E.true;$ $E.code := E_1.code$
$E \rightarrow (E_1)$	$E_1.true := E.true;$ $E_1.false := E.false;$ $E.code := E_1.code$
$E \rightarrow id_1 \text{ relop } id_2$	$E.code := gen('if' id_1.place \text{ relop } op id_2.place$ $\quad 'goto' E.true) \parallel gen('goto' E.false)$
$E \rightarrow \text{true}$	$E.code := gen('goto' E.true)$
$E \rightarrow \text{false}$	$E.code := gen('goto' E.false)$

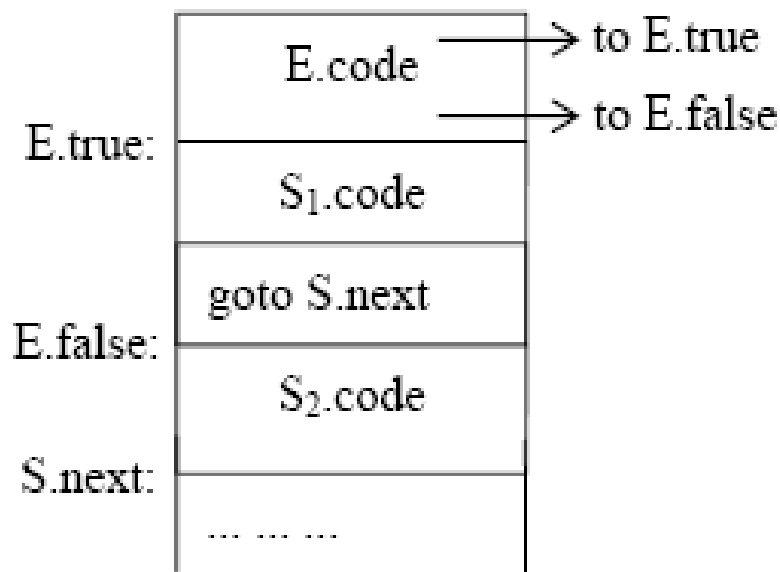
Flow-of-Control Statements

- In the translation, we assume that a three-address **code** statement can have a *symbolic label*, and that the function *newlabel* generates such labels.
- We associate with E two labels using inherited attributes:
 1. *E.true*, the label to which control flows if E is true;
 2. *E.false*, the label to which control flows if E is false.
- We associate to S the inherited attribute *S.next* that represents the label attached to the first statement after the **code** for S.

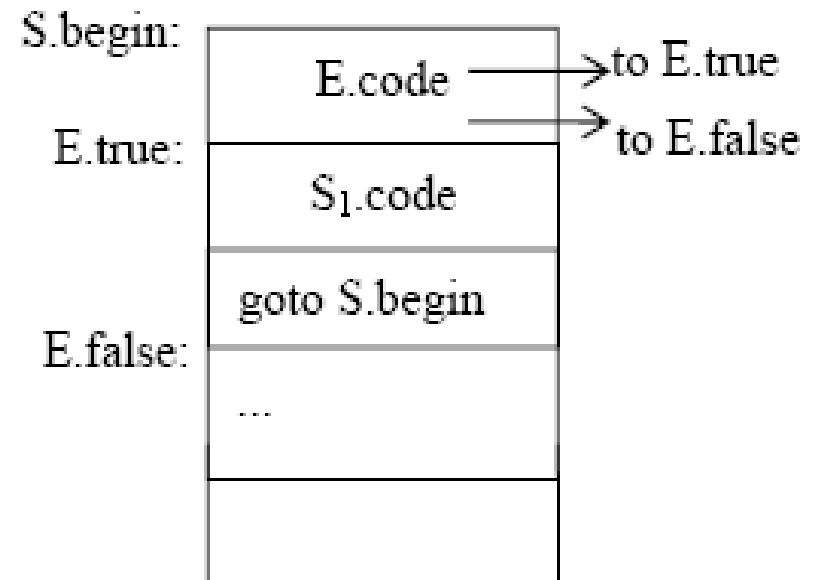
Flow of Control Statements



(a) if -then



(b) if -then-else



(c) while-do

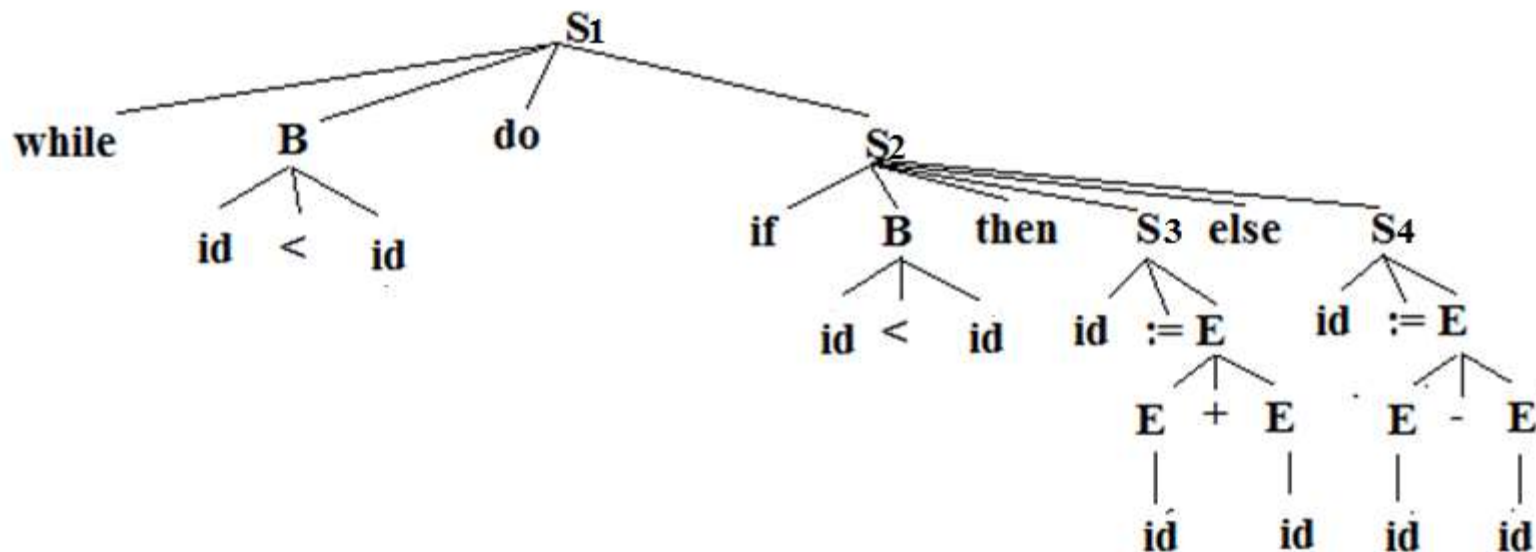
SDT for flow of control statements

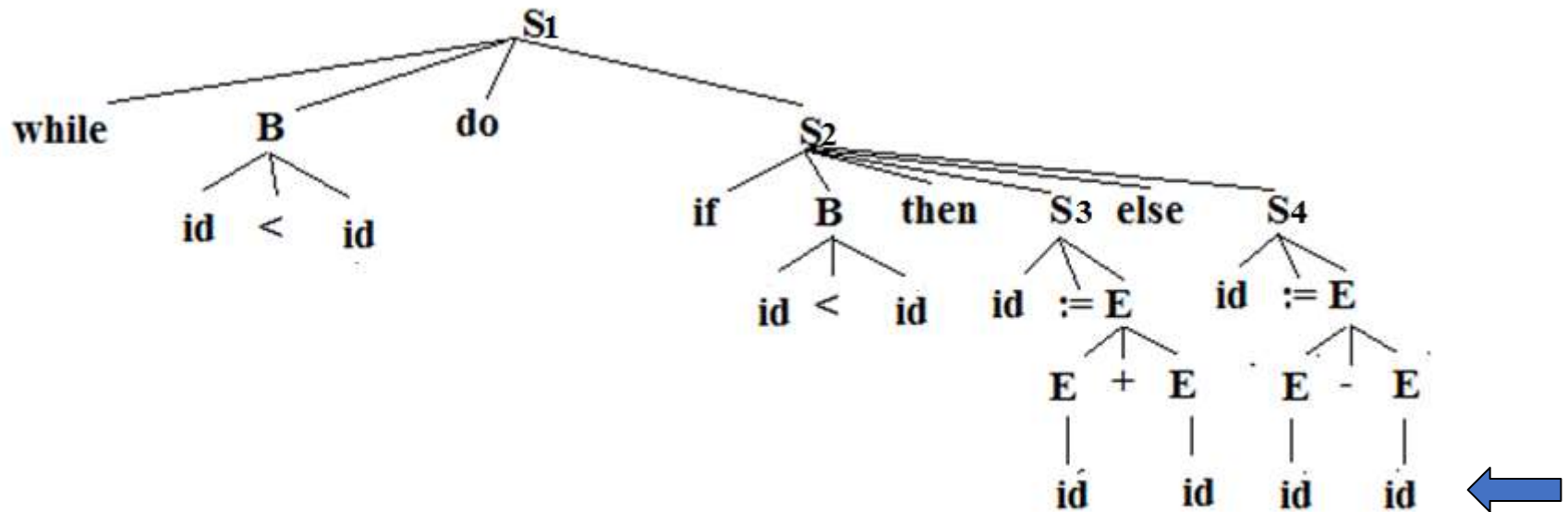
PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \parallel label(S.next)$
$S \rightarrow \text{assign}$	$S.code = \text{assign.code}$
$S \rightarrow \text{if } B \text{ then } S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \parallel label(B.true) \parallel S_1.code$
$S \rightarrow \text{if } B \text{ then } S_1 \text{ else } S_2$	$B.true = newlabel()$ $B.false = newlabel()$ $S_1.next = S_2.next = S.next$ $S.code = B.code$ $\parallel label(B.true) \parallel S_1.code$ $\parallel gen('goto' S.next)$ $\parallel label(B.false) \parallel S_2.code$
$S \rightarrow \text{while } B \text{ do } S_1$	$begin = newlabel()$ $B.true = newlabel()$ $B.false = S.next$ $S_1.next = begin$ $S.code = label(begin) \parallel B.code$ $\parallel label(B.true) \parallel S_1.code$ $\parallel gen('goto' begin)$
$S \rightarrow S_1 S_2$	$S_1.next = newlabel()$ $S_2.next = S.next$ $S.code = S_1.code \parallel label(S_1.next) \parallel S_2.code$

Example

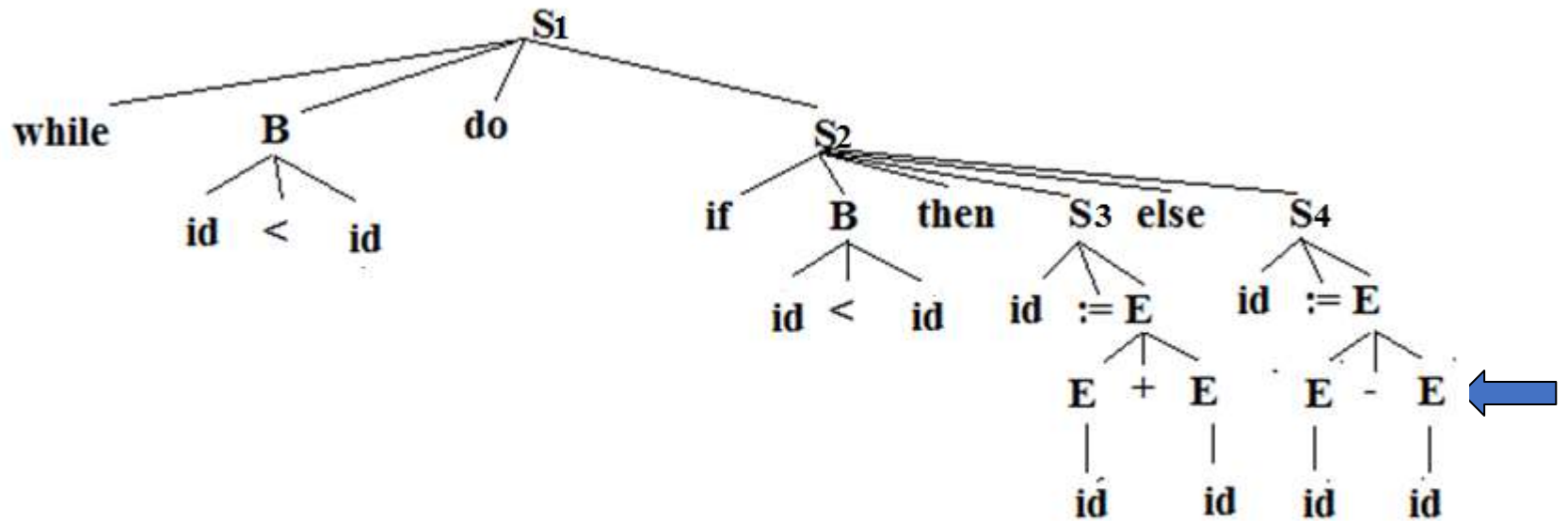
- Translate the following statement:
 while $a < b$ do
 if $c < d$ then
 $x := y + z$
 else
 $x := y - z$

Parse Tree of St





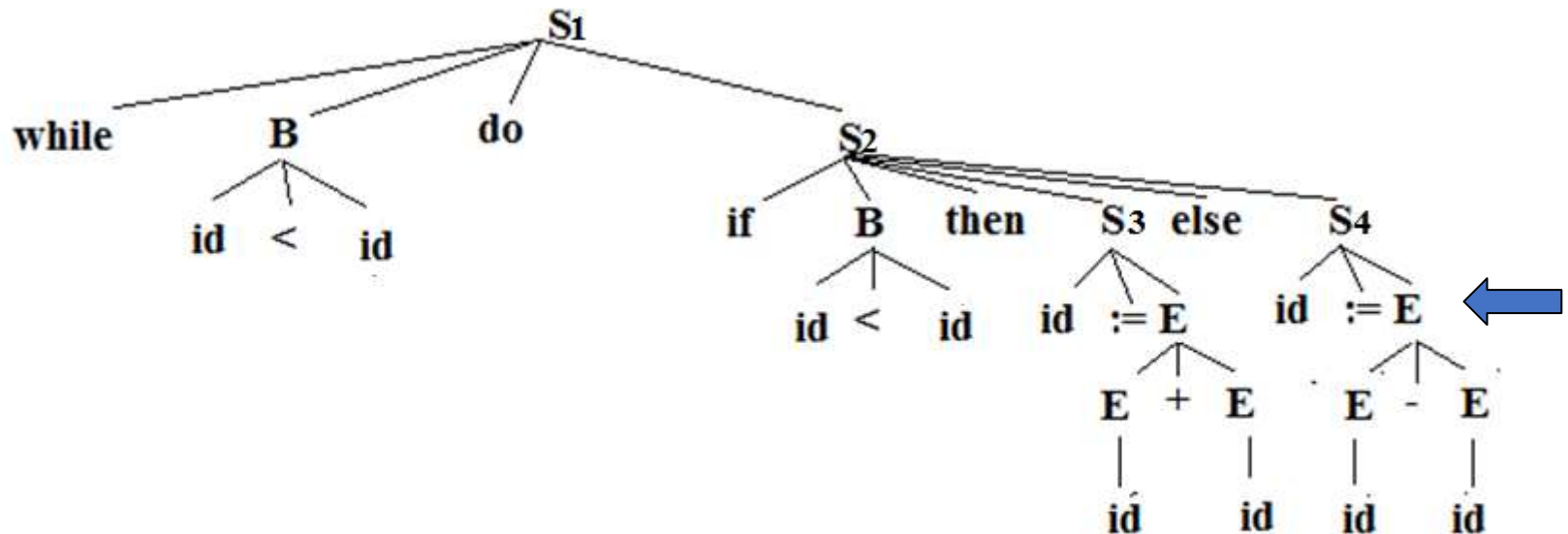
Attributes
id.place: z



Attributes

E.place = z

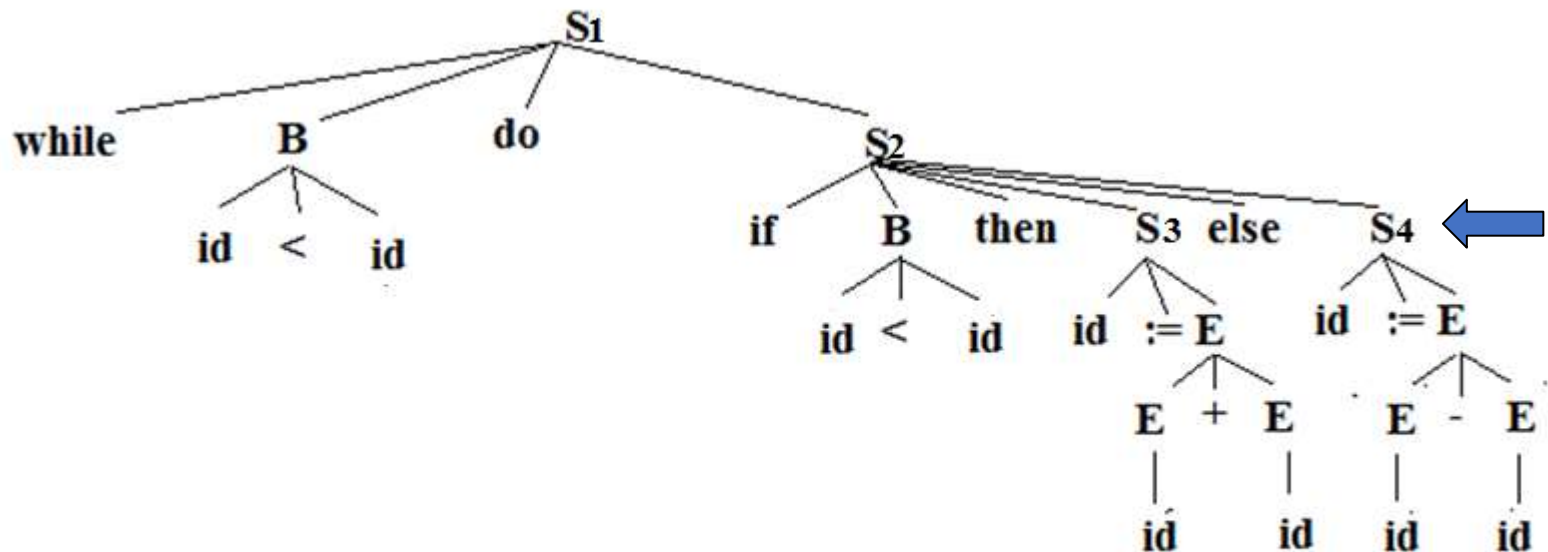
E.code = ""



Attributes

E.place = newtemp() t1

E.code= "t1=y-z"



Semantic rules

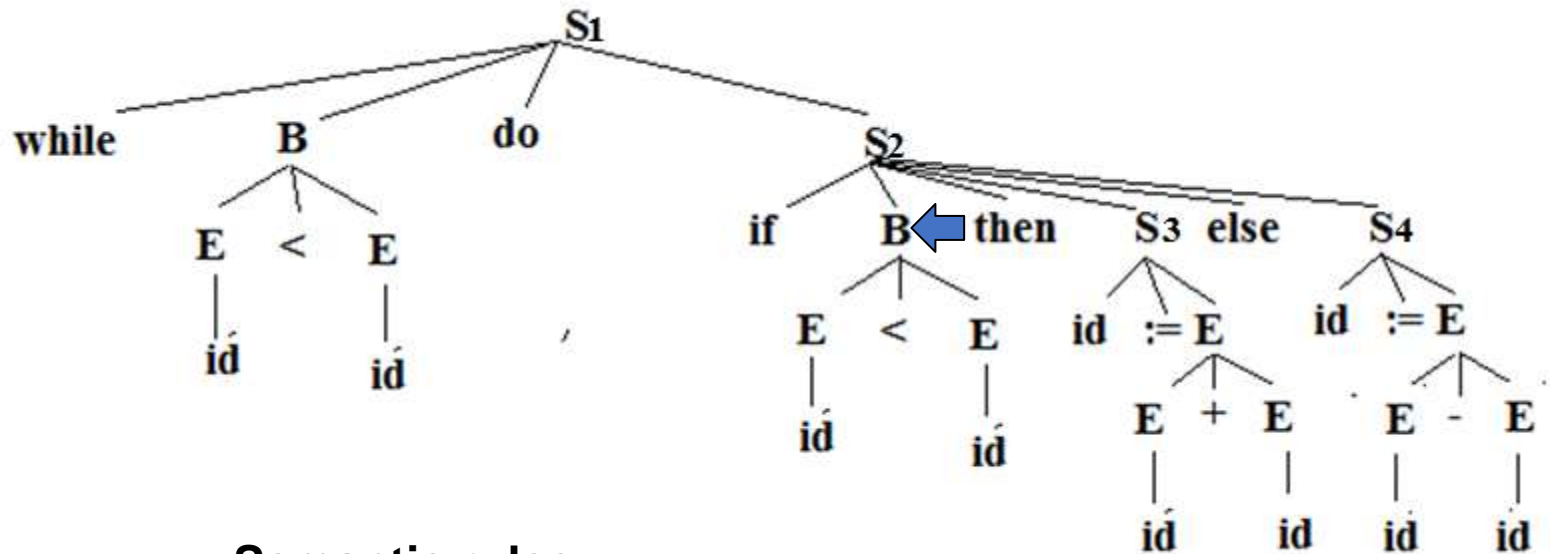
$S \rightarrow \text{id} := E \quad \{ \begin{array}{l} S.\text{place} = \text{id}.\text{place} \\ S.\text{code} = E.\text{code} || \text{gen}(S.\text{place} \text{ ':=' } E.\text{place}) \end{array} \}$

Attributes

$S.\text{place} = \text{id}.\text{place} = x$

$S.\text{code} = \text{"t1=y-z"}$

$x = \text{t1}$



Semantic rules

$E \rightarrow id_1 \text{ relop } id_2$

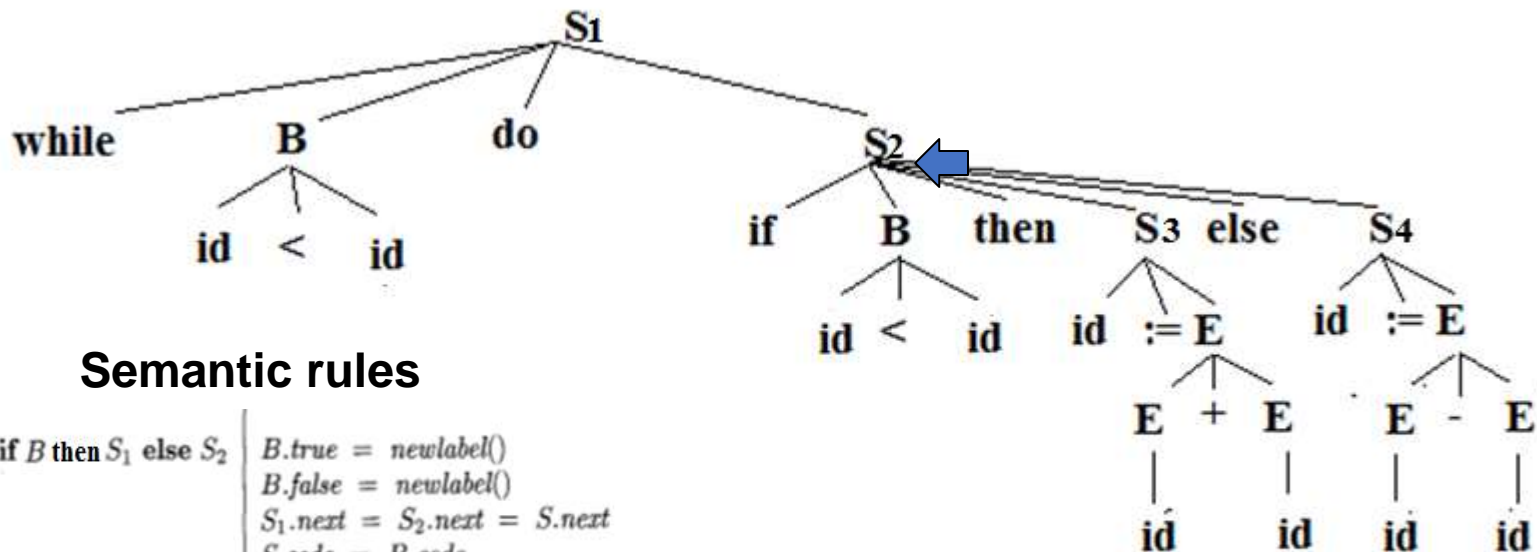
$$E.code := gen('if' id_1.place \text{ relop } op \ id_2.place \\ 'goto' E.true) \parallel gen('goto' E.false)$$

Attribute

.code

if c < d goto L1

Goto L2

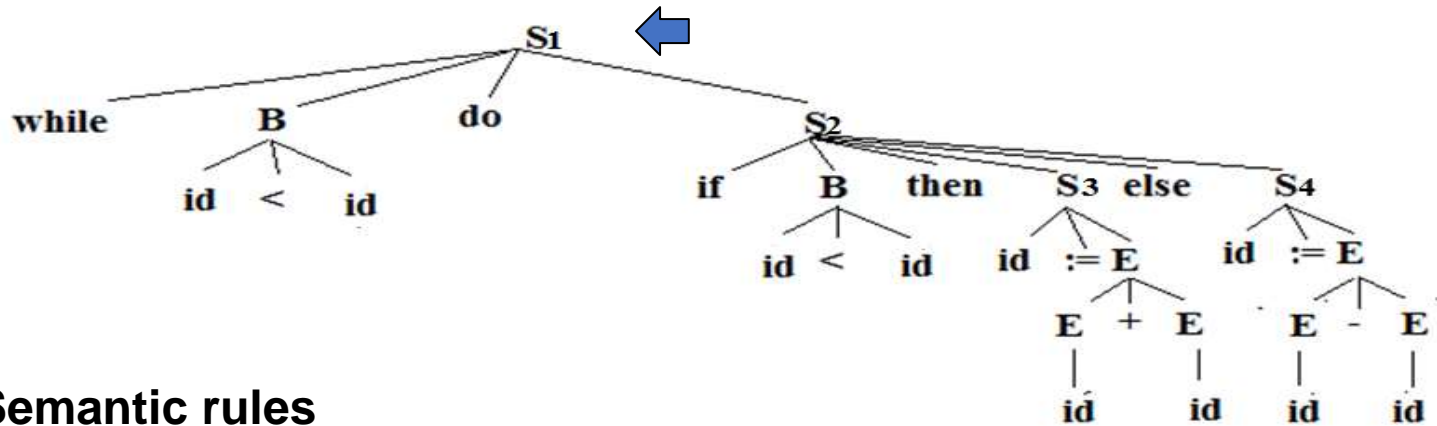


Semantic rules

$$S \rightarrow \text{if } B \text{ then } S_1 \text{ else } S_2 \quad \left| \begin{array}{l} B.\text{true} = \text{newlabel}() \\ B.\text{false} = \text{newlabel}() \\ S_1.\text{next} = S_2.\text{next} = S.\text{next} \\ S.\text{code} = B.\text{code} \\ \quad || \text{label}(B.\text{true}) || S_1.\text{code} \\ \quad || \text{gen}(\text{'goto' } S.\text{next}) \\ \quad || \text{label}(B.\text{false}) || S_2.\text{code} \end{array} \right.$$

Attributes

$B.\text{true} = \text{newlabel}() \rightarrow L1$
 $B.\text{false} = \text{newlabel}() \rightarrow L2$
 $S_1.\text{next} = S_2.\text{next} = S.\text{next}$
 $S.\text{code}$
 if $c < d$ goto $L1$
 goto $L2$
 $L1: t1 = y + z$
 $x = t1$
 goto $S.\text{next}$
 $L2: t2 = y - z$
 $x = t2$



Semantic rules

$S \rightarrow \text{while } B \text{ do } S_1$

S	$\begin{aligned} & \text{begin} = \text{newlabel}() \\ & B.\text{true} = \text{newlabel}() \\ & B.\text{false} = S.\text{next} \\ & S_1.\text{next} = \text{begin} \\ & S.\text{code} = \text{label}(\text{begin}) \parallel B.\text{code} \\ & \quad \parallel \text{label}(B.\text{true}) \parallel S_1.\text{code} \\ & \quad \parallel \text{gen}(\text{'goto' } \text{begin}) \end{aligned}$
-----	---

Attributes

$\text{begin} = \text{newlabel}() \rightarrow L3$
 $B.\text{true} = \text{newlabel}() \rightarrow L4$
 $B.\text{False} = S.\text{next}$
 $S1.\text{next} = \text{begin} = L3 \Rightarrow S2.\text{next} = S3.\text{next} = L3$
 $S.\text{code}$
 $L3: \text{if } a < b \text{ goto } L4$
 $\quad \text{goto } L0$
 $L4: \text{if } c < d \text{ goto } L1$
 $\quad \text{goto } L2$
 $L1: t1 = y + z$
 $\quad x = t1$
 $\quad \text{goto } L3$
 $L2: t2 = y - z$
 $\quad x = t1$
 $\quad \text{goto } L3$
 Label L0 will appear in the intermediate program after generating code using semantic rules of production $S \rightarrow S_1 S_2$