

Connection schema for USRP-1 as radar digitizing card,
using 1 LFRX board. All inputs need over-voltage protection.

4 x Serial I/O
(digital
0.0 to 3.3V
LVCMOS):

SERIN3 RxD
SERIN3 TxD
SERIN2 RxD
SERIN2 TxD
SERIN1 RxD
SERIN1 TxD
SERIN0 RxD
SERIN0 TxD

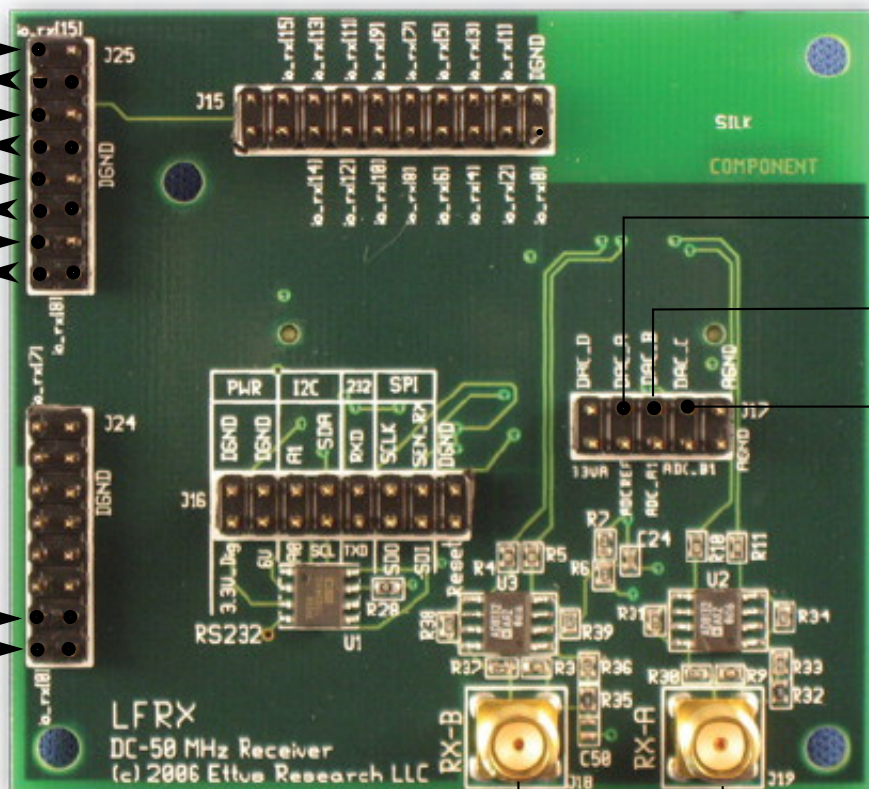
ACP (digital,
0.0 to 3.3V,
LVCMOS)

ARP (digital,
0.0 to 3.3V,
LVCMOS)

video bias
(analog,
0.0 to 3.3V,
1.0 mA)

ARP gain
(analog,
0.0 to 3.3V,
1.0 mA)

ACP gain
(analog,
0.0 to 3.3V,
1.0 mA)



LFRX

video
(-1.0 to +1.0 V)

trigger
(-1.0 to +1.0 V)