Hema C. P. Movva

hemacp@utexas.edu	Mar 2018, Austin, TX, USA
Education	Ph.D., Electrical Engineering, The University of Texas at Austin Expected 2018 M.S.E., Electrical Engineering, The University of Texas at Austin 2012 B.Tech., Electrical Engineering, Indian Institute of Technology Bombay 2009
Recent Publications	HCP Movva et al., Tunable $\Gamma-K$ Valley Populations in Hole-Doped Trilayer WSe2, Phys. Rev. Lett. 120, 107703 (2018)
	HCP Movva et al., Density-Dependent Quantum Hall States and Zeeman Splitting in Monolayer and Bilayer WSe ₂ , Phys. Rev. Lett. 118, 247701 (2017)
	HCP Movva et al., High-Mobility Holes in Dual-Gated WSe $_2$ Field-Effect Transistors, ACS Nano 9, 10402 (2015)
Experience	Research Assistant, The University of Texas at Austin • Ph.D. advisors: Emanuel Tutuc, Sanjay Banerjee • Electrical transport in transition metal dichalcogenide (TMD) heterostructures • Fabrication of FETs using atomically thin TMDs such as WSe ₂ , MoS ₂ , etc. • Magnetotransport measurements in high-mobility TMD FETs • Tunneling phenomena in TMD heterostructure FETs
	Research Assistant, The University of Texas at Austin • M.S.E. advisor: Sanjay Banerjee • Surface charge transfer doping of graphene for use in self-aligned FETs • Fabricated graphene FETs with chemically doped source/drain access regions
	 Engineer R&D, Solar Semiconductor Inc., Hyderabad, India Jun '09 - Aug '10 Modeled wattage losses in c-Si photovoltaic modules Developed distributed circuit models for c-Si solar cells and modules Managed sun simulators for solar cell and module characterization
	 Circuit Design Intern, Texas Instruments, Bangalore, India May '08 - Aug '08 Designed a high-speed dynamic latch based voltage comparator Designed a 250 MSPS 6-bit SAR analog-to-digital convertor
Honors	Best Paper Award, 74 th IEEE Device Research Conference 2016 Best Poster Award, 73 rd IEEE Device Research Conference 2015 Best Poster Award, 9 th International Nanotechnology Conference (INC9) 2013 Silver Medal, 37 th International Physics Olympiad 2005 National Talent Search scholarship, India 2003
Service	Reviewer, IEEE Trans. Electron Dev., ACS Appl. Mater. Interfaces, Nanoscale Mentor, Research Experience for High School Young Scholars Sum. '17 President, Japan Karate Association of Austin May '13 - May '15
Skills	<u>Fabrication:</u> Electron-beam lithography, standard cleanroom processing Electron-beam lithography, standard cleanroom processing Semiconductor parameter analyzers probe stations. He cryostate

Electrical: Metrology:

Software:

Semiconductor parameter analyzers, probe stations, He cryostats

Atomic force microscopy, scanning electron microscopy

MATLAB, LabVIEW, C