## Hema C. P. Movva

hemacp@utexas.edu	Mar 2018, Austin, TX, USA
Education	Ph.D., Electrical Engineering, The University of Texas at Austin Expected 2018 M.S.E., Electrical Engineering, The University of Texas at Austin 2012 B.Tech., Electrical Engineering, Indian Institute of Technology Bombay 2009
Publications	HCP Movva et al., Tunable $\Gamma-K$ Valley Populations in Hole-Doped Trilayer WSe2, Phys. Rev. Lett. 120, 107703 (2018)
	HCP Movva et al., Density-Dependent Quantum Hall States and Zeeman Splitting in Monolayer and Bilayer WSe <sub>2</sub> , Phys. Rev. Lett. 118, 247701 (2017)
	HCP Movva et al., High-Mobility Holes in Dual-Gated WSe $_2$ Field-Effect Transistors, ACS Nano 9, 10402 (2015)
Experience	Research Assistant, The University of Texas at Austin  • Ph.D. advisors: Emanuel Tutuc, Sanjay Banerjee  • Electrical contact engineering of transition metal dichalcogenides (TMDs)  • Fabrication of dual-gated FETs using monolayer/few-layer TMDs such as WSe <sub>2</sub> • Quantum Hall effect measurements in high-quality TMD FETs  • Tunneling phenomena in TMD heterostructure FETs
	<ul> <li>Research Assistant, The University of Texas at Austin Aug '10 - May '12</li> <li>M.S.E. advisor: Sanjay Banerjee</li> <li>Surface charge transfer doping of graphene for use in self-aligned FETs</li> <li>Fabricated graphene FETs with chemically doped source/drain access regions</li> </ul>
	<ul> <li>Engineer R&amp;D, Solar Semiconductor Inc., Hyderabad, India Jun '09 - Aug '10</li> <li>Modeled wattage losses in c-Si photovoltaic modules</li> <li>Developed distributed circuit models for c-Si solar cells and modules</li> <li>Managed sun simulators for solar cell and module characterization</li> </ul>
	<ul> <li>Circuit Design Intern, Texas Instruments, Bangalore, India May '08 - Aug '08</li> <li>Designed a high-speed dynamic latch based voltage comparator</li> <li>Designed a 250 MSPS 6-bit SAR analog-to-digital convertor</li> </ul>
Honors	Best Paper Award, 74 <sup>th</sup> IEEE Device Research Conference 2016 Best Poster Award, 73 <sup>rd</sup> IEEE Device Research Conference 2015 Best Poster Award, 9 <sup>th</sup> International Nanotechnology Conference (INC9) 2013 Silver Medal, 37 <sup>th</sup> International Physics Olympiad 2005 National Talent Search scholarship, India 2003
Service	Reviewer, IEEE Trans. Electron Dev., ACS Appl. Mater. Interfaces, Nanoscale Mentor, Research Experience for High School Young Scholars  President, Japan Karate Association of Austin  May '13 - May '15
Skills	Fabrication: Electron-beam lithography, standard cleanroom processing Electrical: Semiconductor parameter analyzers, probe stations, He cryostats  Atomic force microscopy, scanning electron microscopy.

Atomic force microscopy, scanning electron microscopy

MATLAB, LabVIEW, C

Electrical: Metrology:

Software: