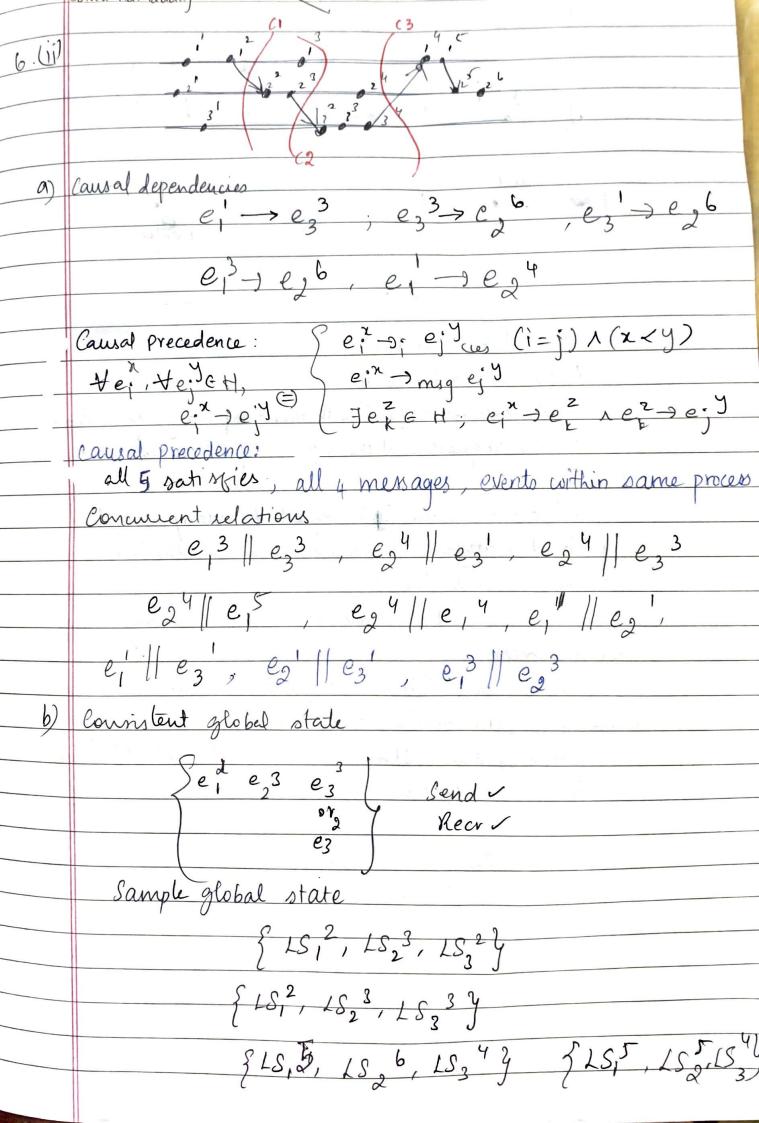


(0) b) si exits cs
(25) (1,00)(2,51) (2.5) SI 1
SI (215) enters CS
T(1/52)
S2 S2 enters/
(CX(S3) 11
53 (1/52)(2/51) (2/51) (X/53)
$(1/S2)(2/S1) \qquad (2/S1) \qquad (2/S3)$
S3 enters es

	How Cs will be executed:
	A sites; will enter ce y
₹	1) Si receives request with timestamp, larger than itself
	i) Si's request is at the top of request-queue;
	St will execute when St's request timestamps greater. It already satisfies L2 because St, S2 are the first ones wanting to execute the C8.
5	It already satisfies L2 because St, S2 are the first ones
	CILEDO SIU. CO
	The Stree St. 5 done it will send release mung to
	Discor (199 Silve).
	Even in this case, S. I has requested CS before S3, so
	tunitant) is omaler =) of a precited
`	Finally after S 15 done, it sends RELEASE mag.
	S3 gets to enter the CS.
	Ovda
~	Order generation
	S2, S1, S3
er.	11



So for causal precedence, all 5 causal dependencies satisfy third condition. By first condition all events within a process
By second condition all events - meg
satisfy arusal precedence.