

This is a tpx3-daq ThresholdScan for chip W15-G6.
Run name: ThresholdScan_2025-12-10_12-31-43

Parameter	Value
Ibias_CP_PLL	128
Ibias_DiscS2_OFF	8
Ibias_PixelDAC	100
Ibias_TPbufferIn	128
Sense_DAC	29
VTP_fine	307
Vthreshold_fine	465
n_injections	100

Parameter	Value
Ibias_DiscS1_OFF	8
Ibias_DiscS2_ON	128
Ibias_Preamp_OFF	8
Ibias_TPbufferOut	128
VPreamp_NCAS	128
Vfbk	132
Vthreshold_start	1600
mask_step	16

Parameter	Value
Ibias_DiscS1_ON	100
Ibias_Ikrum	3
Ibias_Preamp_ON	150
PLL_Vcntrl	128
VTP_coarse	100
Vthreshold_coarse	6
Vthreshold_stop	1760
tp_period	3

Have a good day!

Equalisation:
/home/ixpe_user/Timepix3/equalisations/WorkingEqualisation-27-09-2025.h5

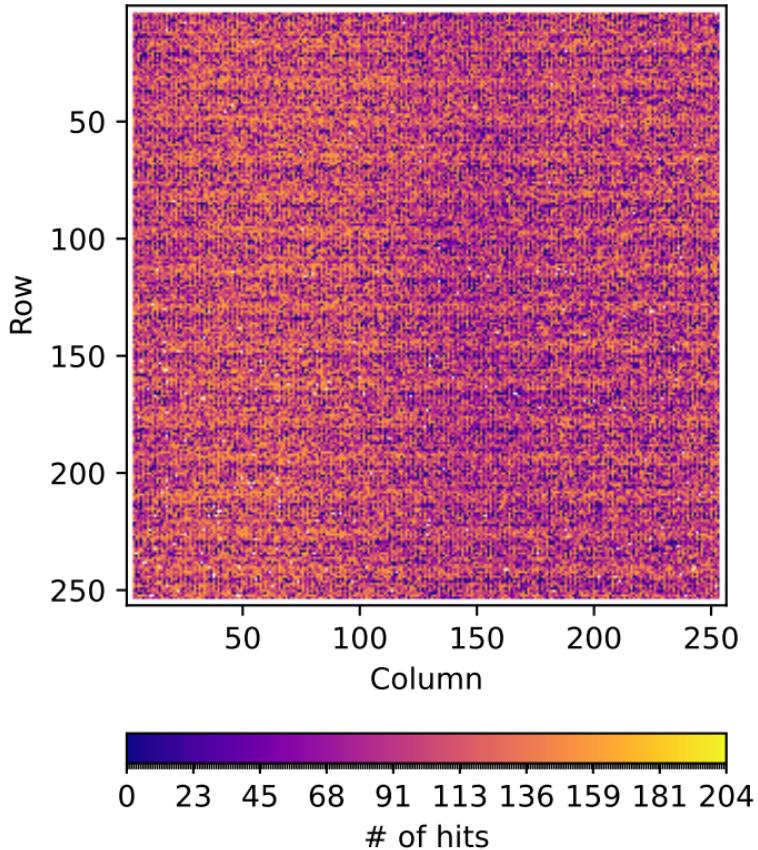
Maskfile:
/home/ixpe_user/Timepix3/masks/WorkingMask-27-09-2025.h5

Software version: 0.9.0
Readout board: FECv6
Firmware version: 4

Timepix3 draft

Chip: W15-G6

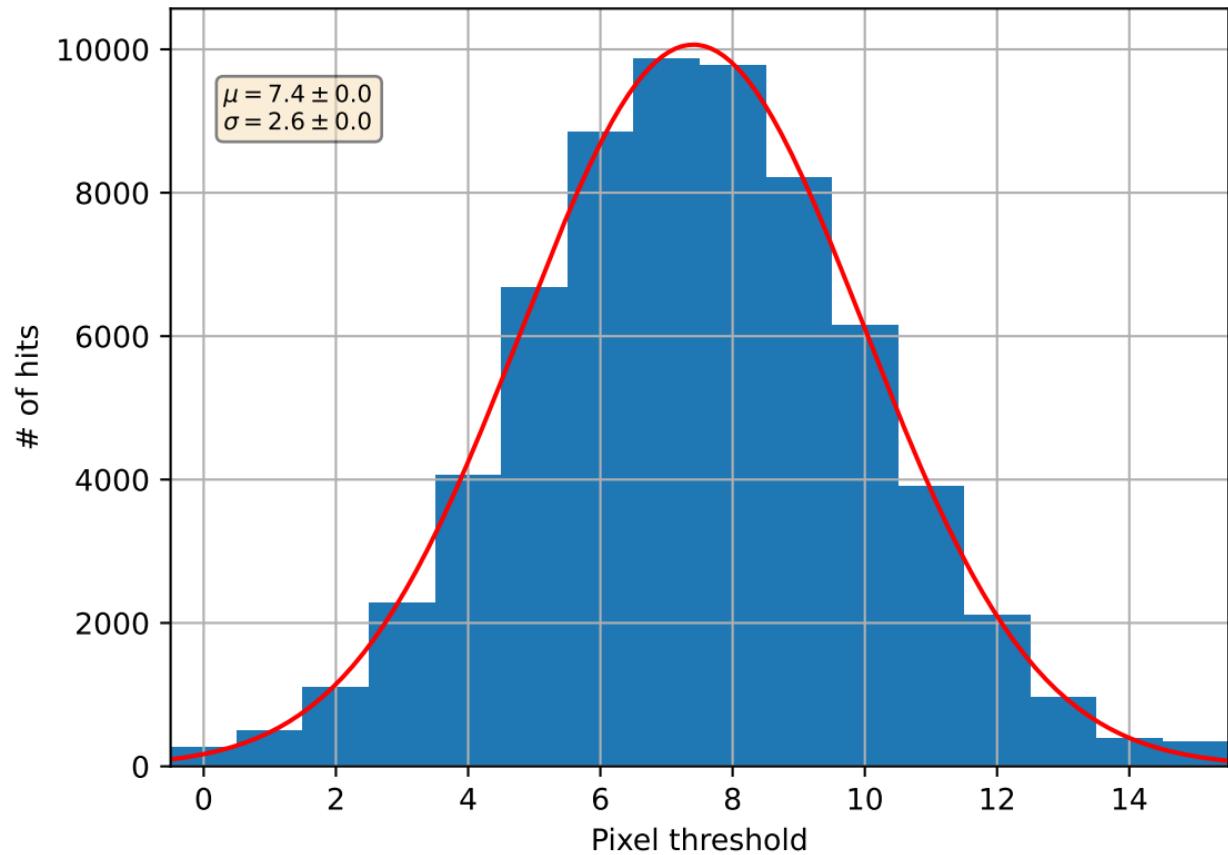
Integrated Occupancy ($\Sigma = 6094242$)



Timepix3 draft

Chip: W15-G6

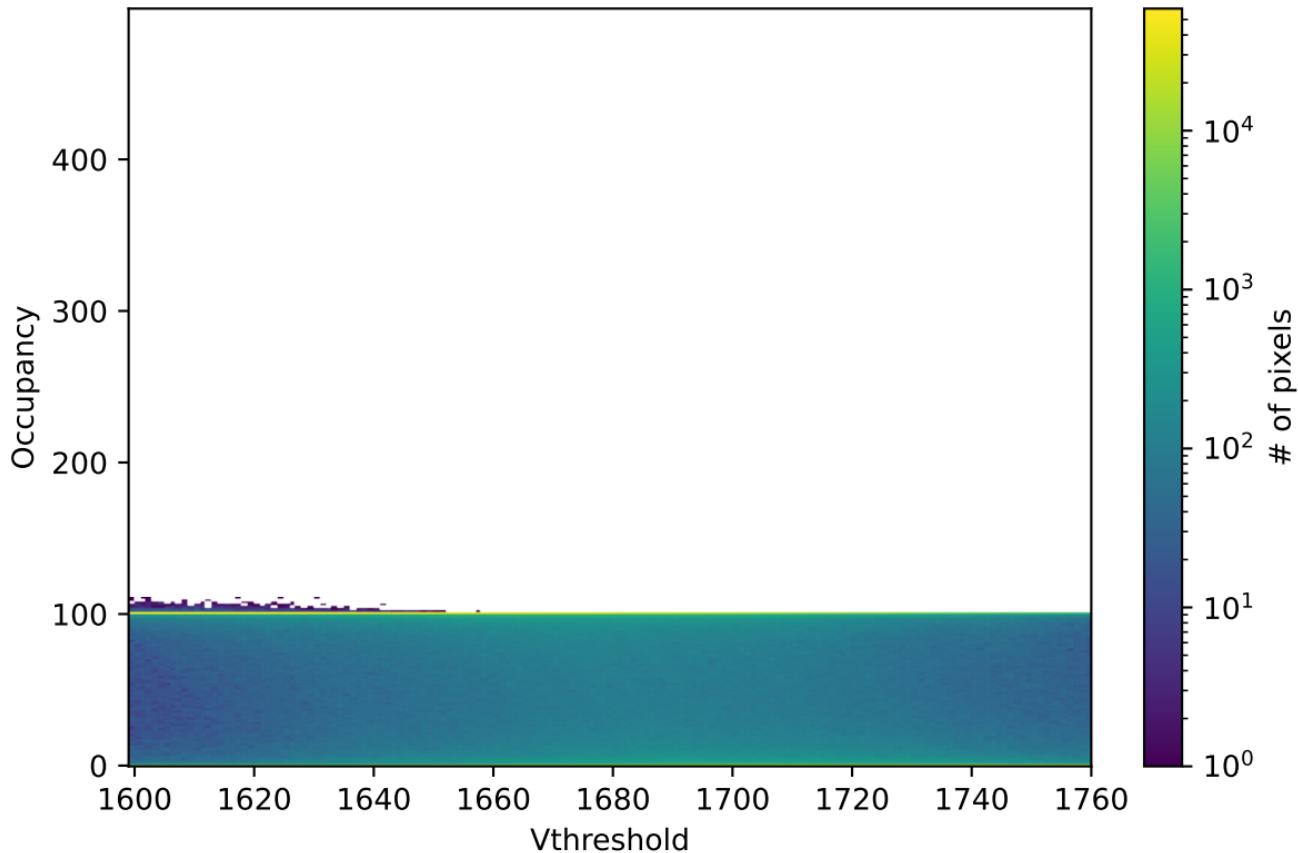
Pixel threshold distribution



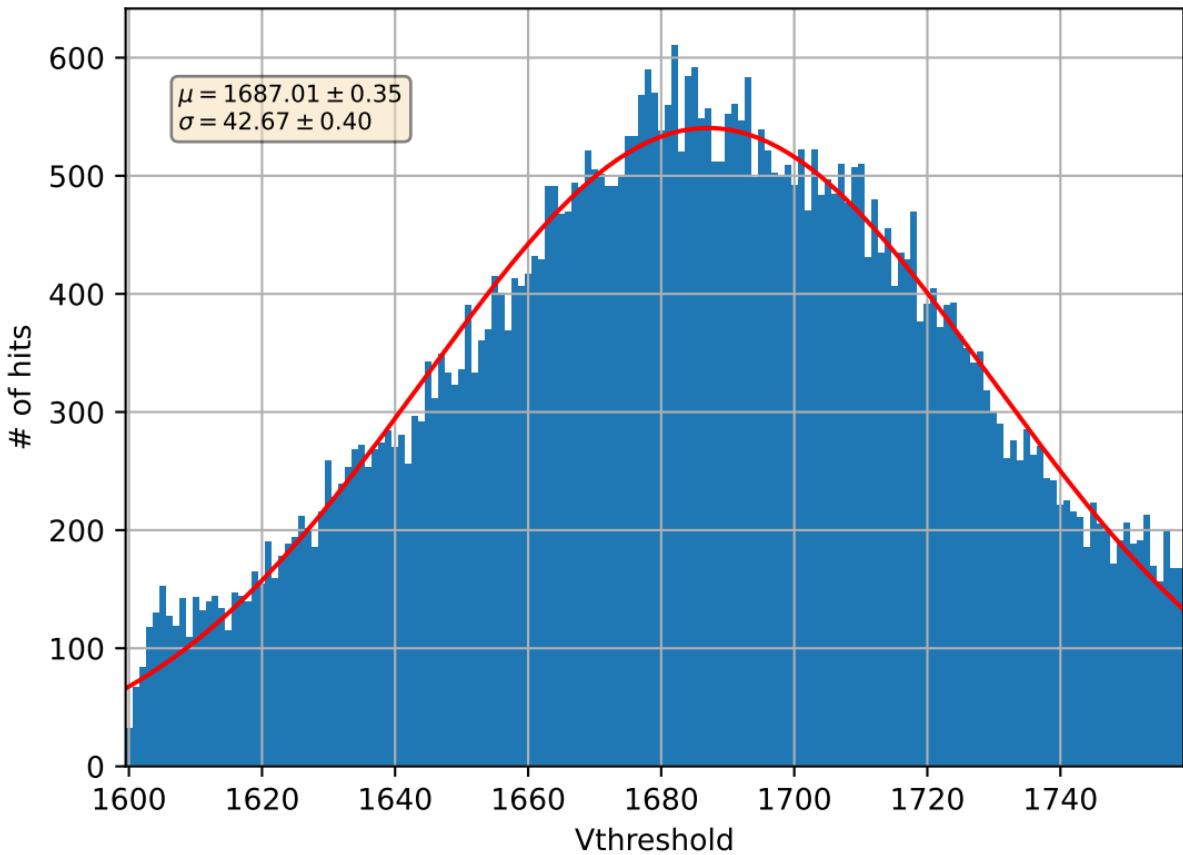
Timepix3 draft

Chip: W15-G6

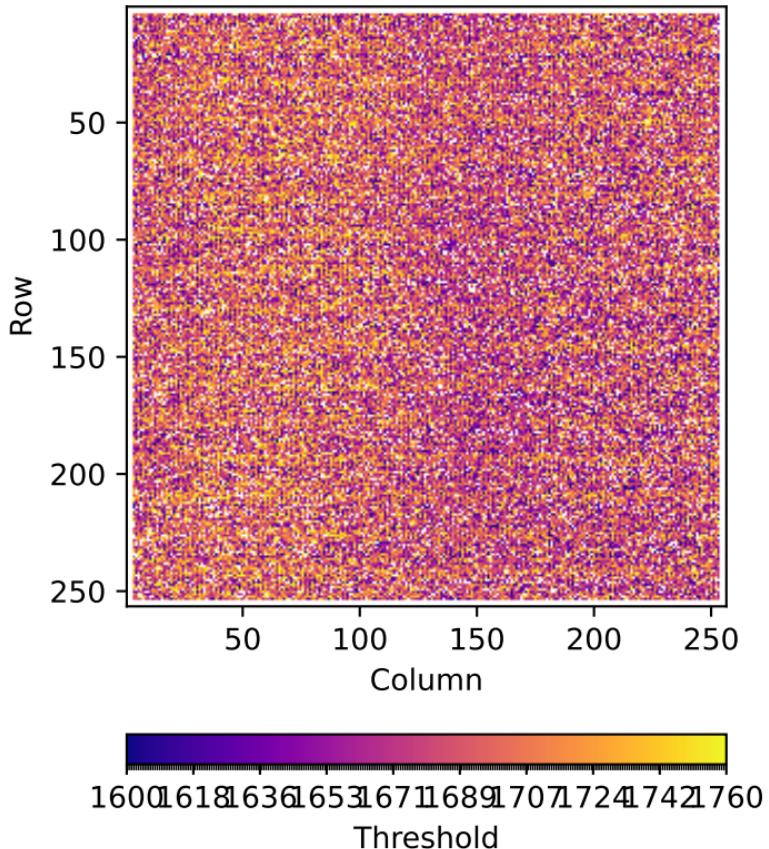
S-curves for 65536 pixel(s)



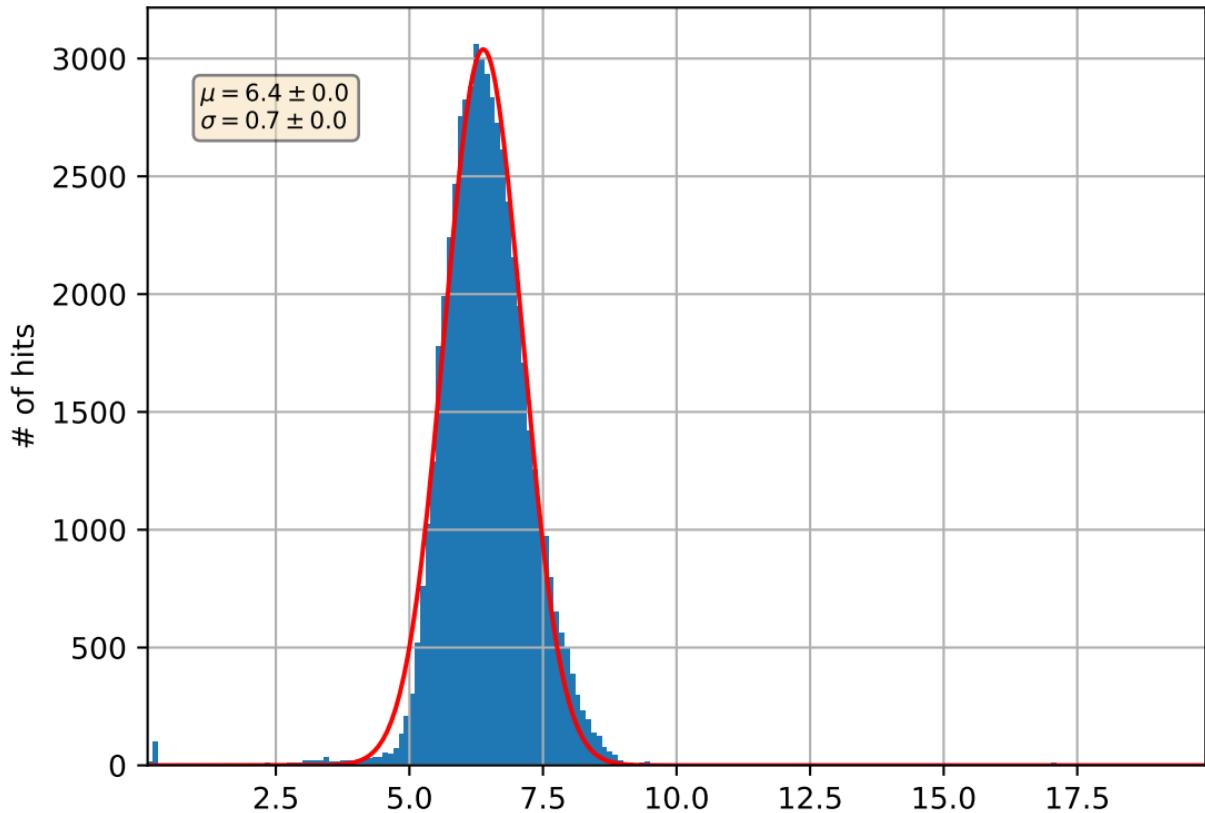
Threshold distribution



Threshold



Noise distribution



Noise

