

This is a tpx3-daq ThresholdCalib for chip W15-G6.
Run name: ThresholdCalib_2025-12-04_15-06-15

Parameter	Value
Ibias_CP_PLL	128
Ibias_DiscS2_OFF	8
Ibias_PixelDAC	100
Ibias_TPbufferIn	128
Sense_DAC	29
VTP_fine	300
Vthreshold_fine	383
n_injections	100
n_pulse_heights	4

Parameter	Value
Ibias_DiscS1_OFF	8
Ibias_DiscS2_ON	128
Ibias_Preamp_OFF	8
Ibias_TPbufferOut	128
VPreamp_NCAS	128
Vfbk	132
Vthreshold_start	1300
mask_step	16

Parameter	Value
Ibias_DiscS1_ON	100
Ibias_Ikrum	3
Ibias_Preamp_ON	150
PLL_Vcntrl	128
VTP_coarse	100
Vthreshold_coarse	6
Vthreshold_stop	2600
tp_period	3

Have a good day!

Equalisation:
`/home/ixpe_user/Timepix3/equalisations/W15-G6_equal-Bonn.h5`

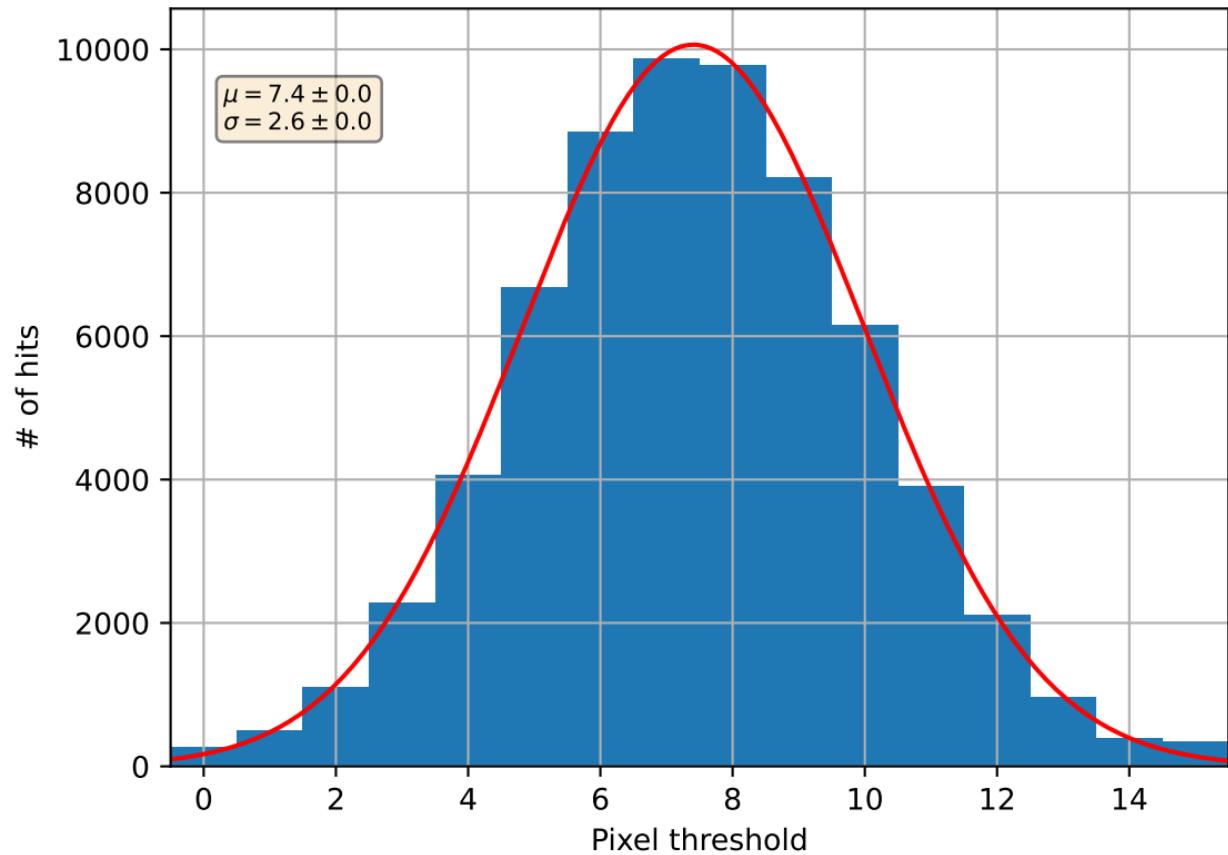
Maskfile:
`/home/ixpe_user/Timepix3/masks/W15-G6_mask-Bonn.h5`

Software version: 0.9.0
Readout board: FECv6
Firmware version: 4

Timepix3 draft

Chip: W15-G6

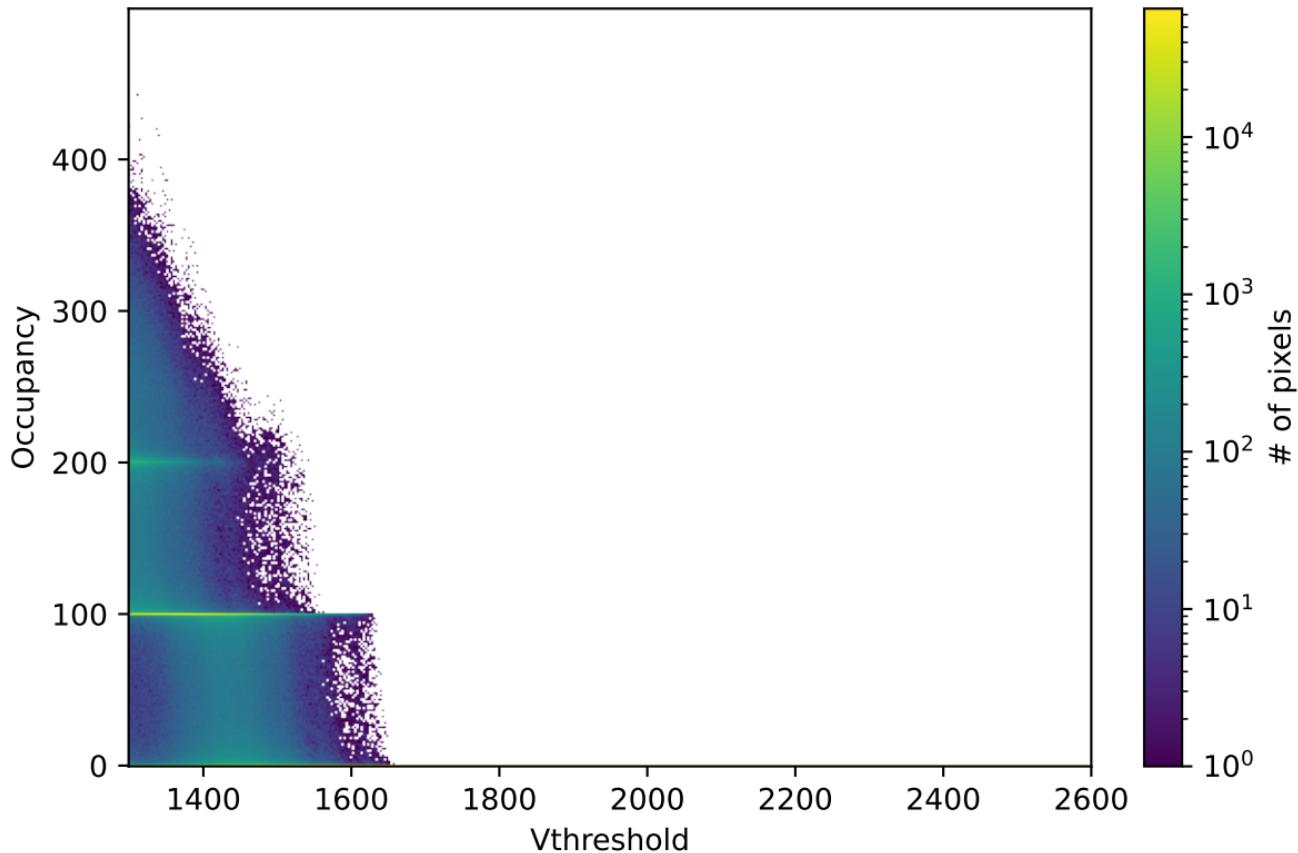
Pixel threshold distribution



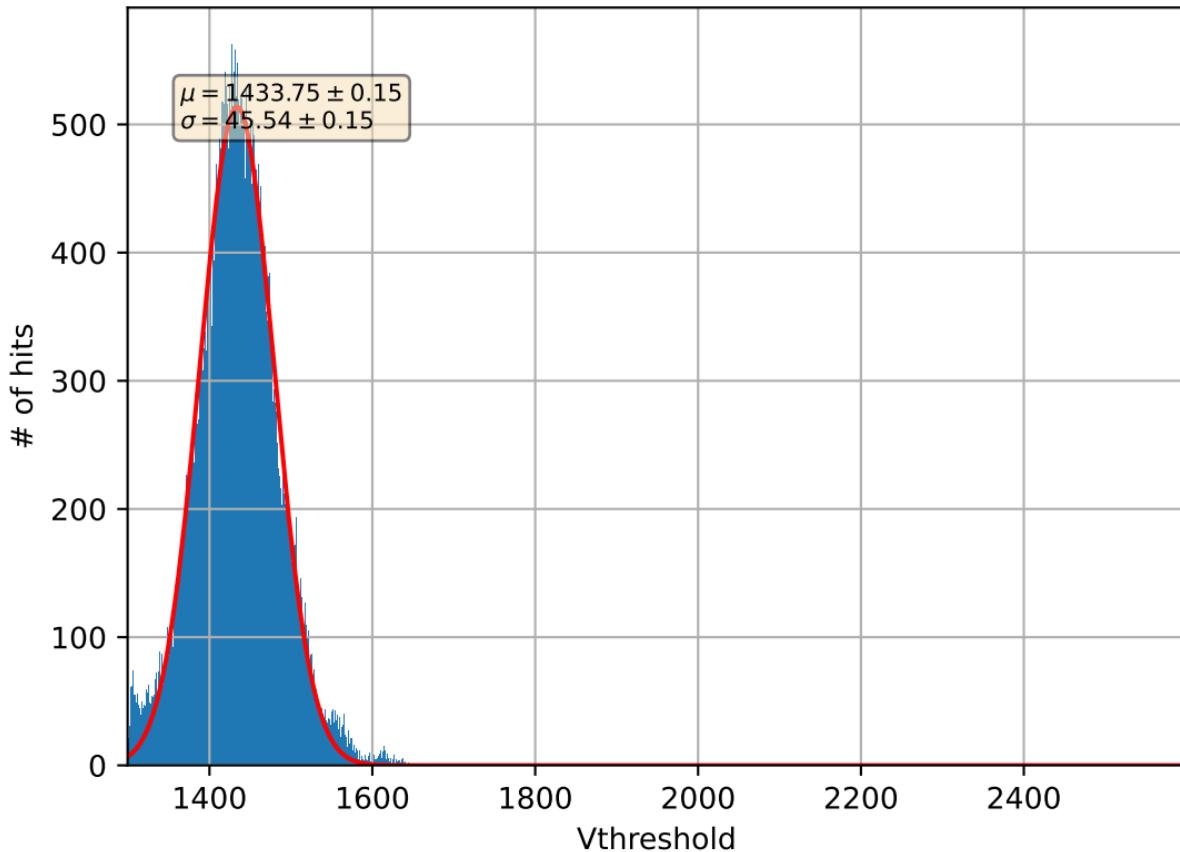
Timepix3 draft

Chip: W15-G6

S-curves for 65536 pixel(s)



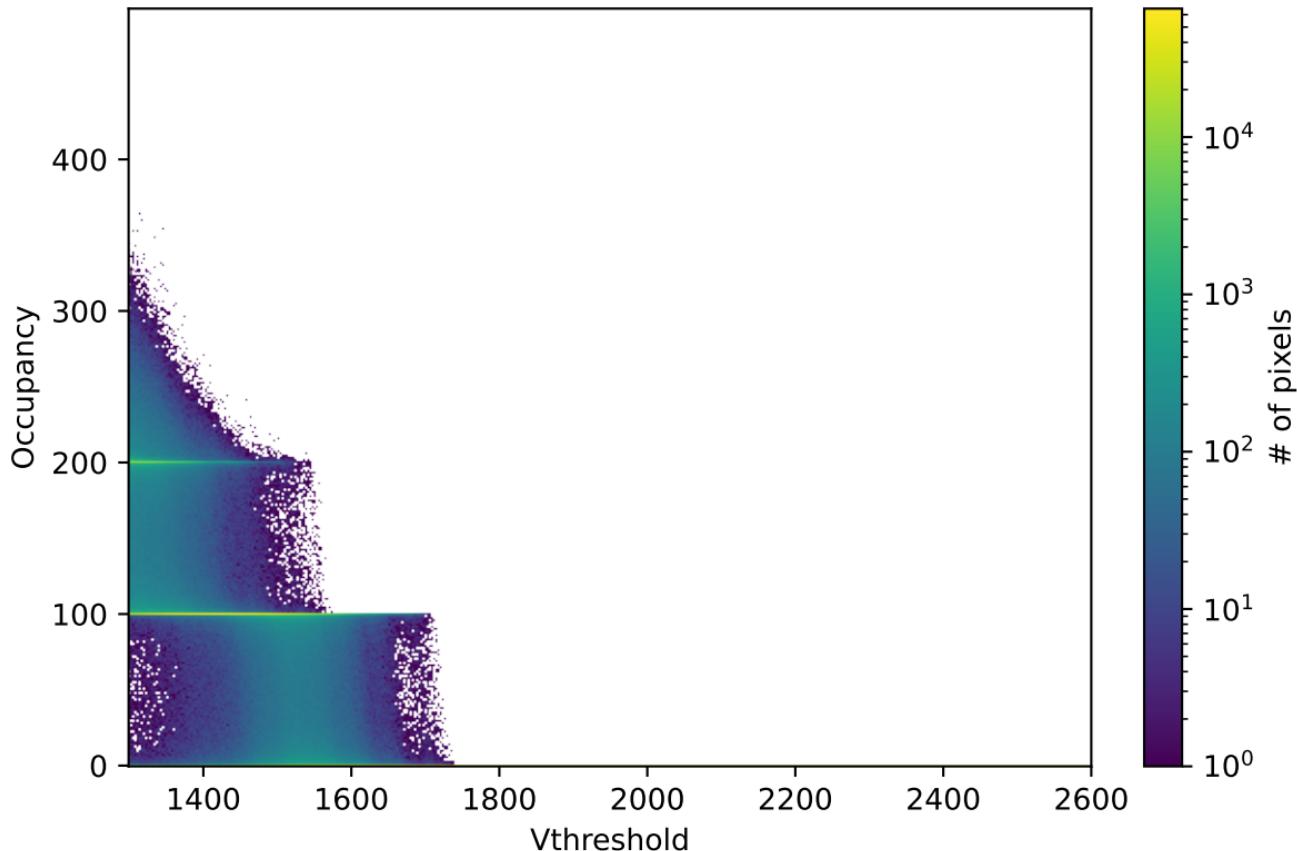
Threshold distribution



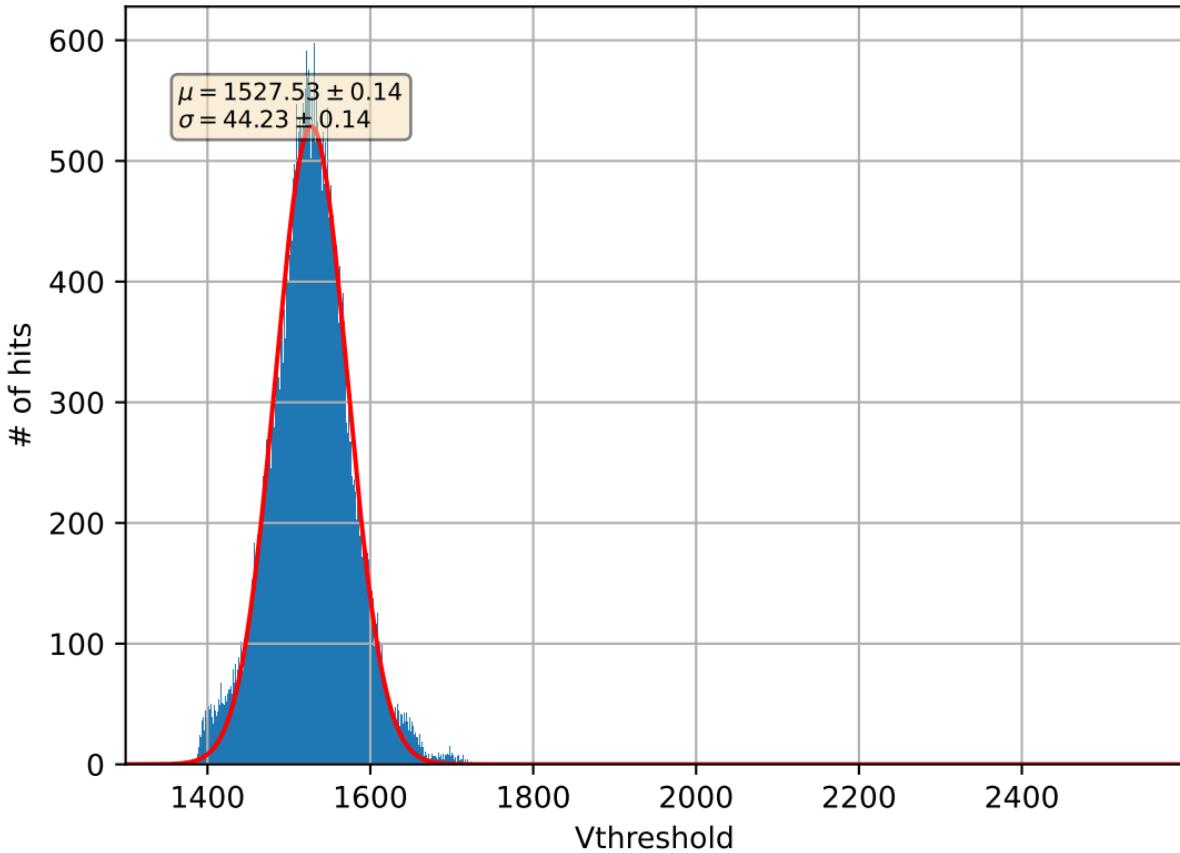
Timepix3 draft

Chip: W15-G6

S-curves for 65536 pixel(s)



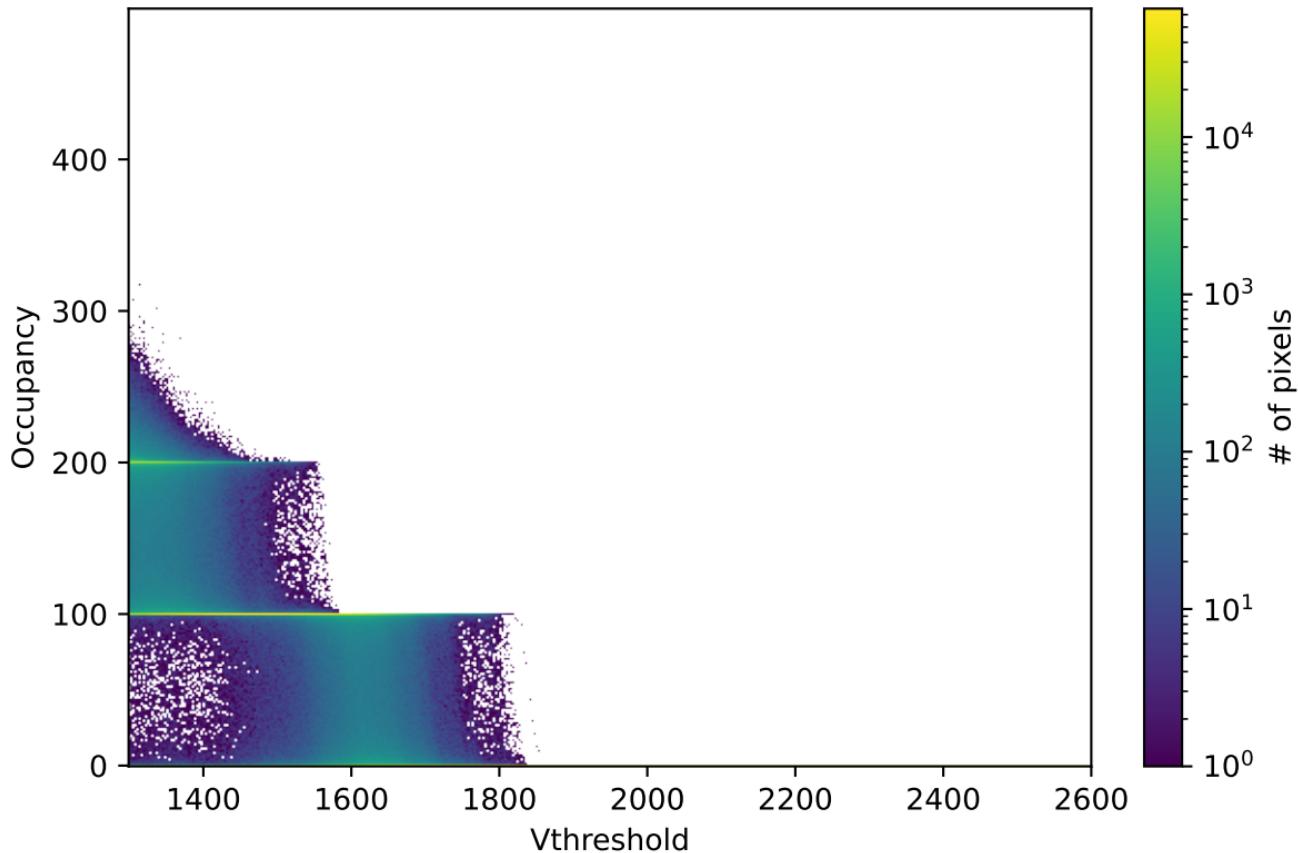
Threshold distribution



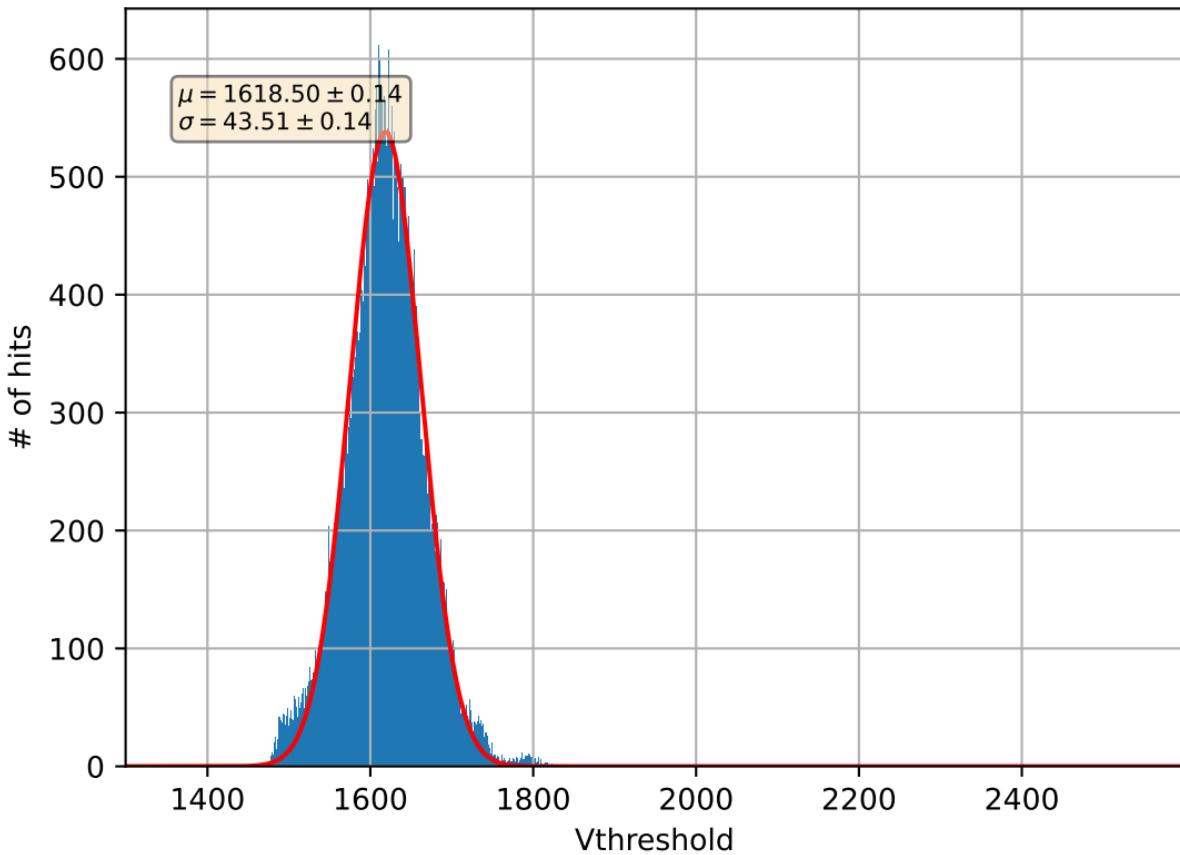
Timepix3 draft

Chip: W15-G6

S-curves for 65536 pixel(s)



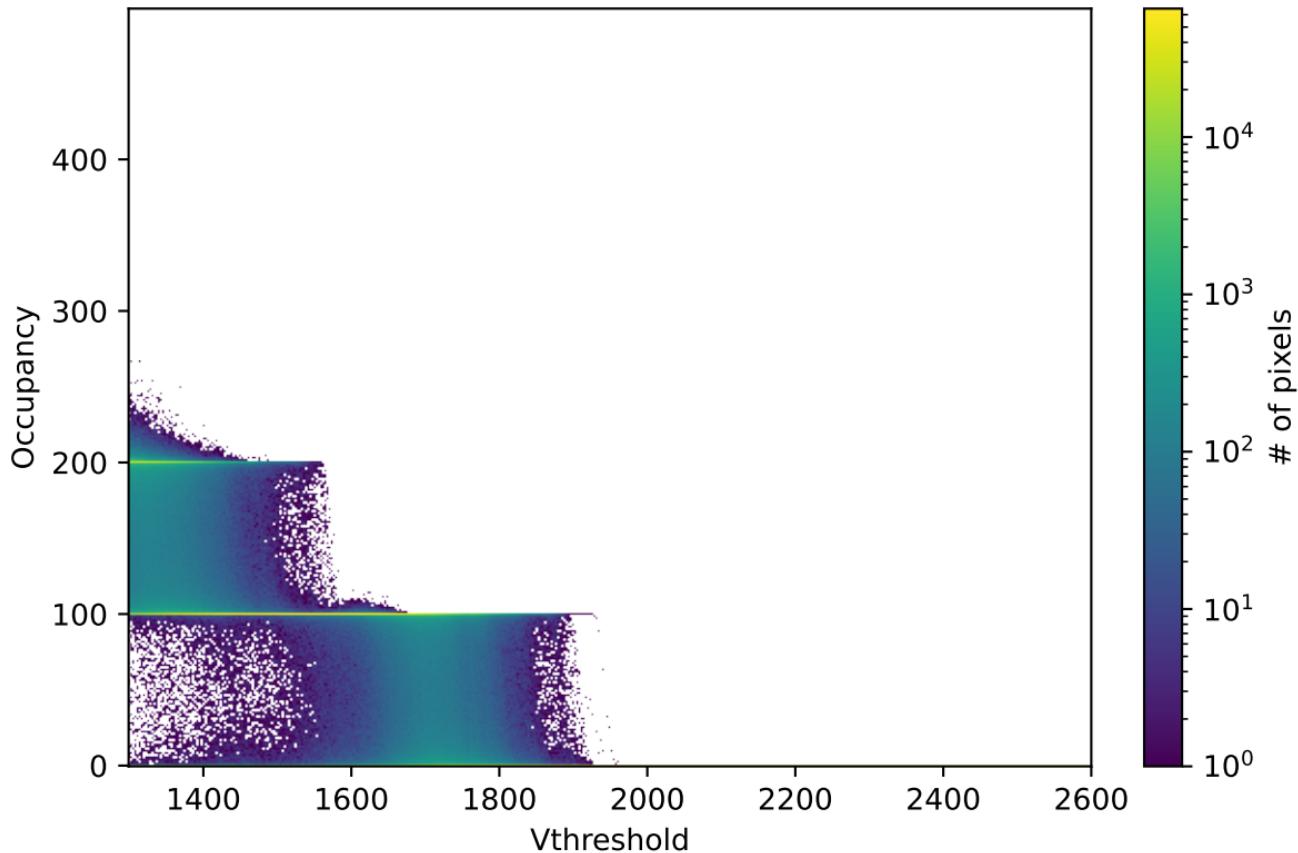
Threshold distribution



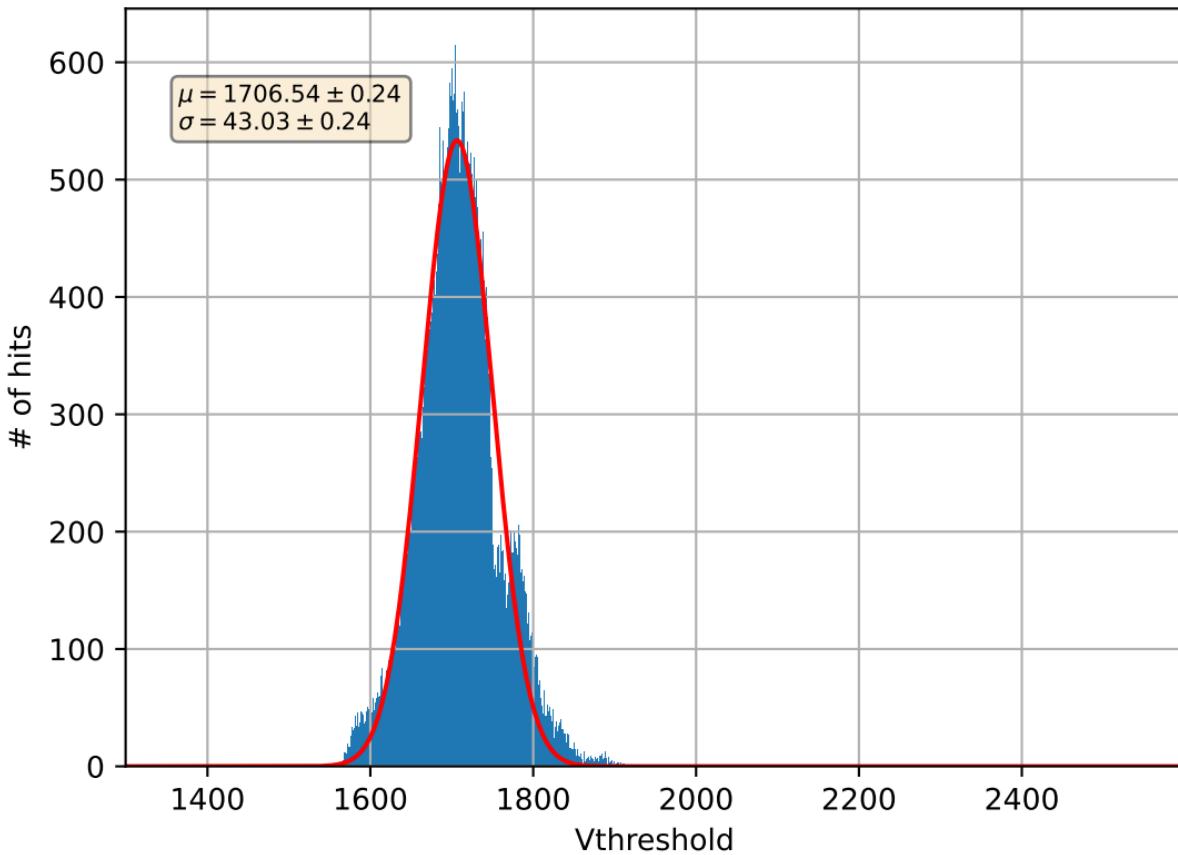
Timepix3 draft

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Threshold distribution



Threshold calibration

