

This is a tpx3-daq ToTCalib for chip W15-G6.
Run name: ToTCalib_2025-12-05_17-11-20

Parameter	Value
Ibias_CP_PLL	128
Ibias_DiscS2_OFF	8
Ibias_PixelDAC	100
Ibias_TPbufferIn	128
Sense_DAC	29
VTP_fine	300
Vthreshold_fine	383
mask_step	4

Parameter	Value
Ibias_DiscS1_OFF	8
Ibias_DiscS2_ON	128
Ibias_Preamp_OFF	8
Ibias_TPbufferOut	128
VPreamp_NCAS	128
Vfbk	132
VTP_fine_start	221
tp_period	3

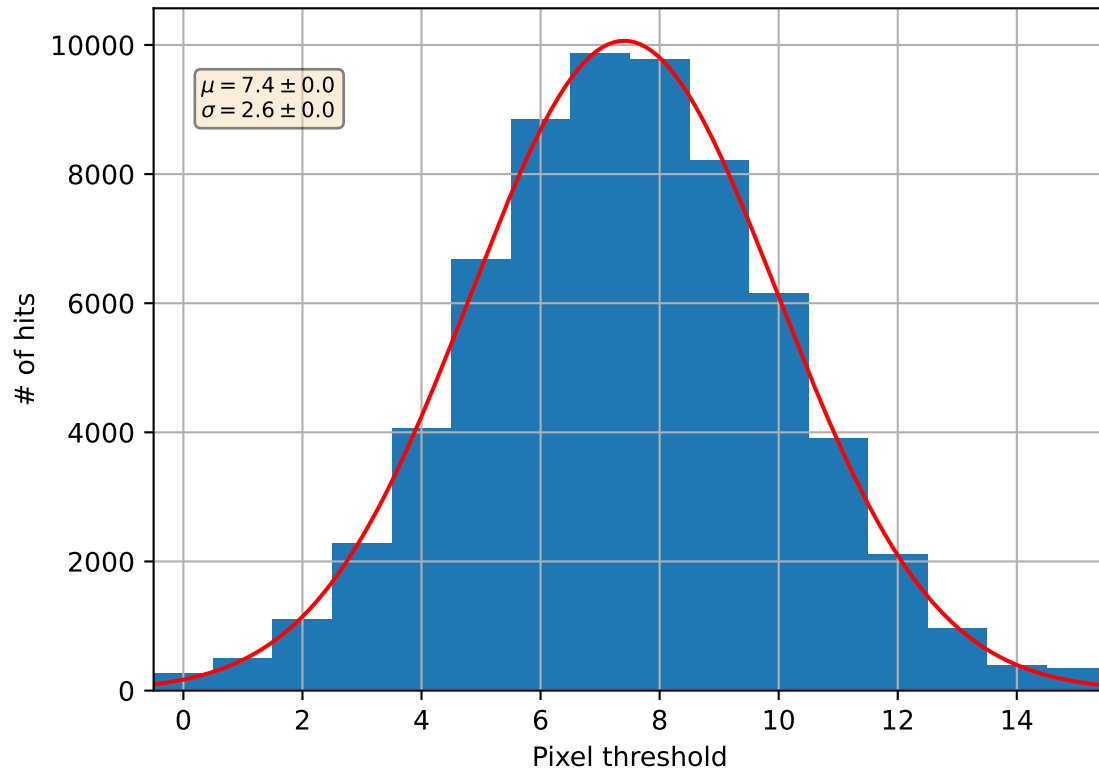
Parameter	Value
Ibias_DiscS1_ON	100
Ibias_Ikrum	3
Ibias_Preamp_ON	150
PLL_Vcntrl	128
VTP_coarse	100
Vthreshold_coarse	6
VTP_fine_stop	511

Have a good day!

Equalisation:
/home/ixpe_user/Timepix3/equalisations/W15-G6_equal-Bonn.h5
Maskfile:
/home/ixpe_user/Timepix3/masks/W15-G6_mask-Bonn.h5

Software version: 0.9.0
Readout board: FECv6
Firmware version: 4

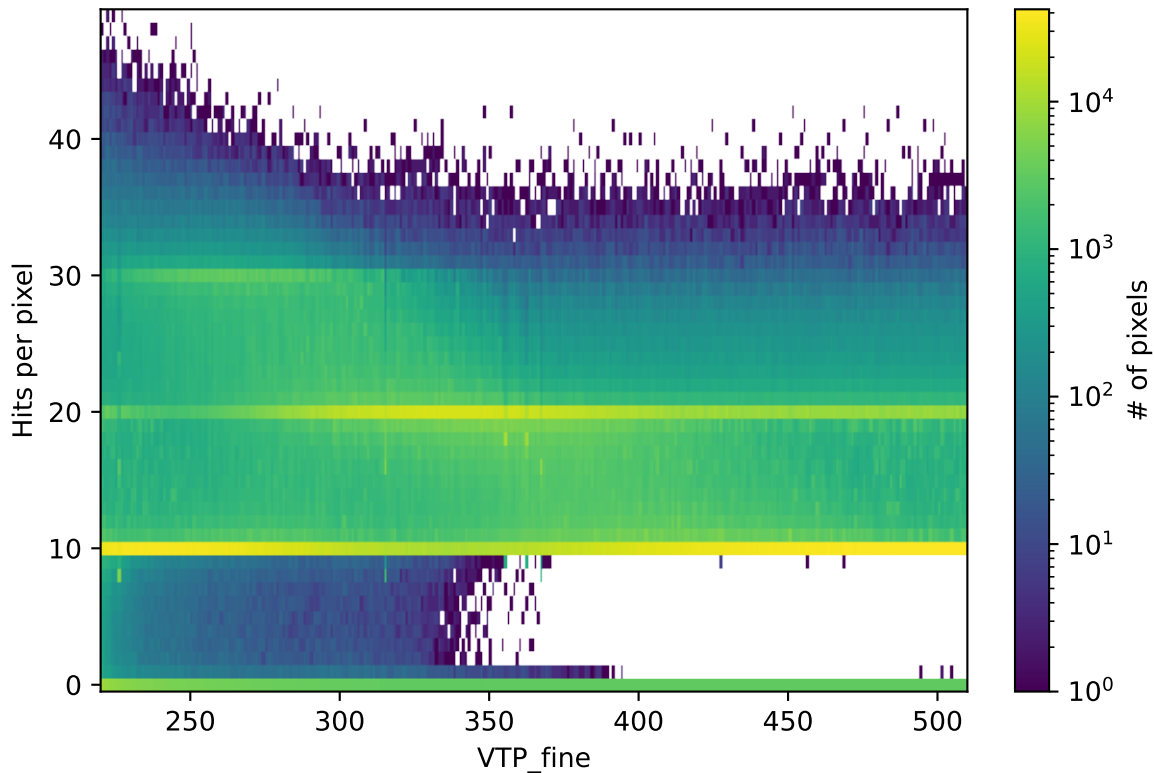
Pixel threshold distribution



Timepix3 draft

Chip: W15-G6

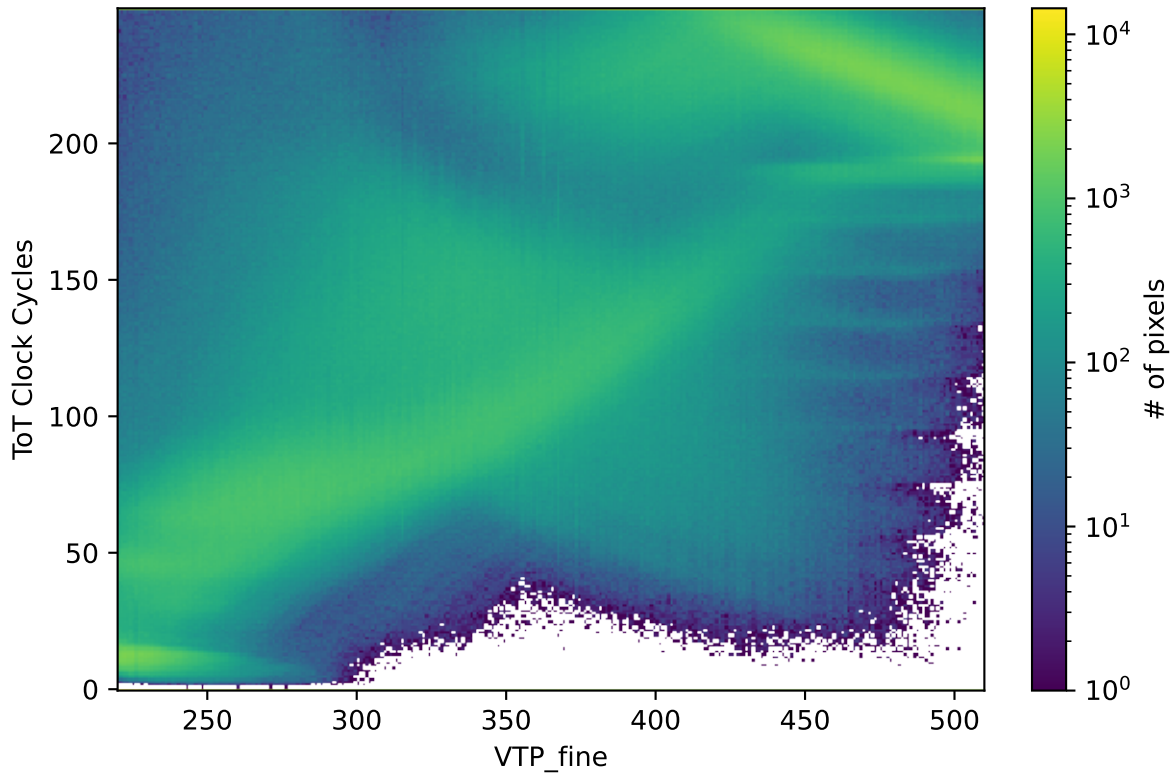
Hit curves for 65536 pixel(s)



Timepix3 draft

Chip: W15-G6

ToT curves for 65536 pixel(s)



ToT fit

