

This is a tpx3-daq ThresholdCalib for chip W15-G6.  
Run name: ThresholdCalib\_2025-12-10\_20-17-13

Parameter	Value
Ibias_CP_PLL	128
Ibias_DiscS2_OFF	8
Ibias_PixelDAC	100
Ibias_TPbufferIn	128
Sense_DAC	29
VTP_fine	307
Vthreshold_fine	500
n_injections	100
n_pulse_heights	12

Parameter	Value
Ibias_DiscS1_OFF	8
Ibias_DiscS2_ON	128
Ibias_Preamp_OFF	8
Ibias_TPbufferOut	128
VPreamp_NCAS	128
Vfbk	132
Vthreshold_start	1300
mask_step	16

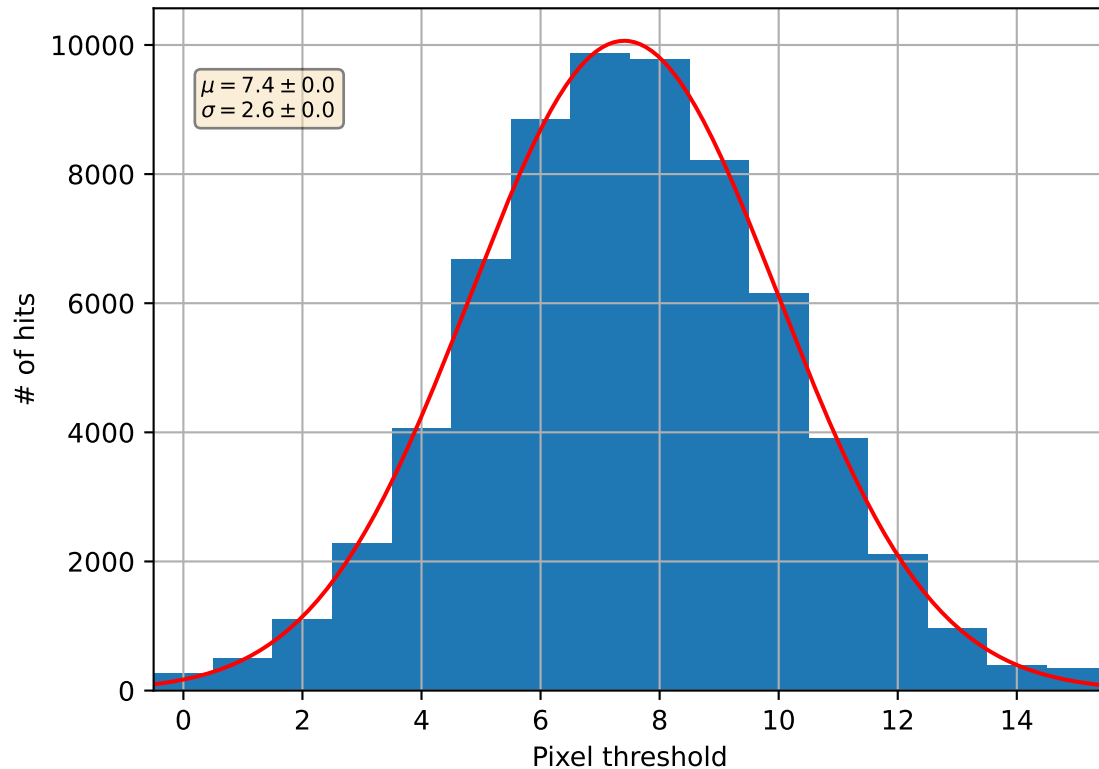
Parameter	Value
Ibias_DiscS1_ON	100
Ibias_Ikrum	3
Ibias_Preamp_ON	150
PLL_Vcntrl	128
VTP_coarse	100
Vthreshold_coarse	5
Vthreshold_stop	1500
tp_period	3

Have a good day!

Equalisation:  
/home/ixpe\_user/Timepix3/equalisations/WorkingEqualisation-27-09-2025.h5  
Maskfile:  
/home/ixpe\_user/Timepix3/masks/WorkingMask-27-09-2025.h5

Software version: 0.9.0  
Readout board: FECv6  
Firmware version: 4

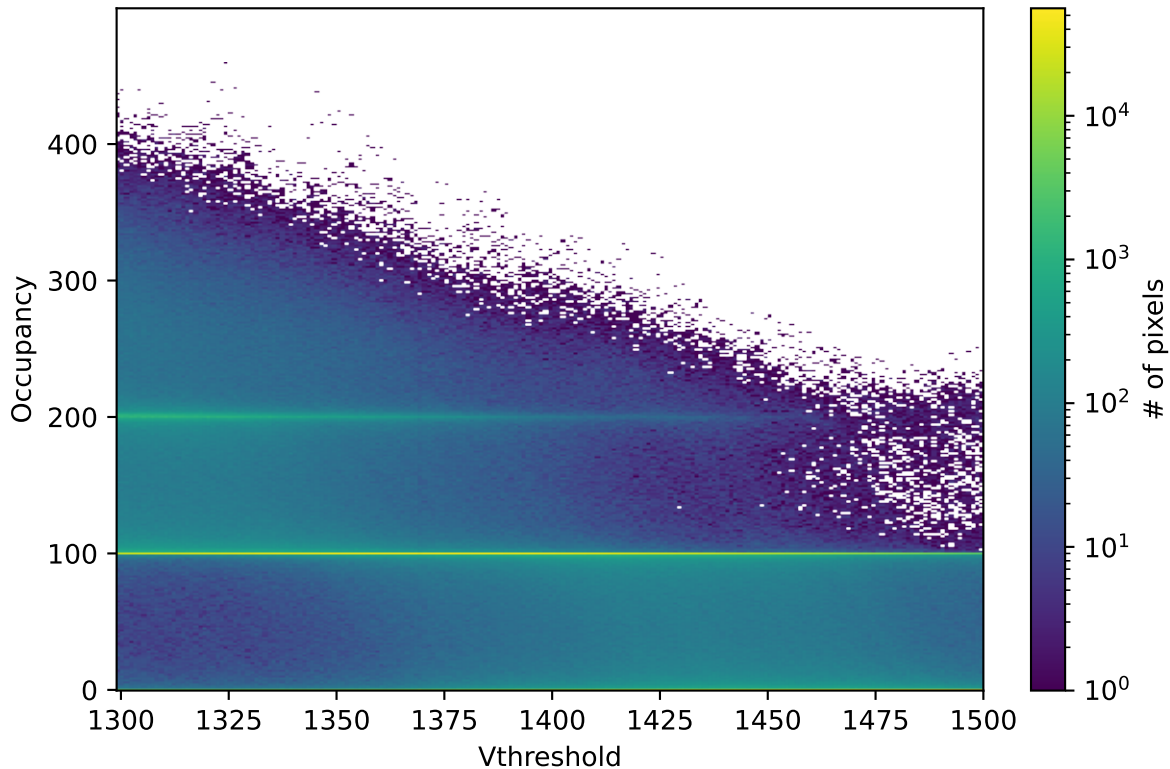
## Pixel threshold distribution



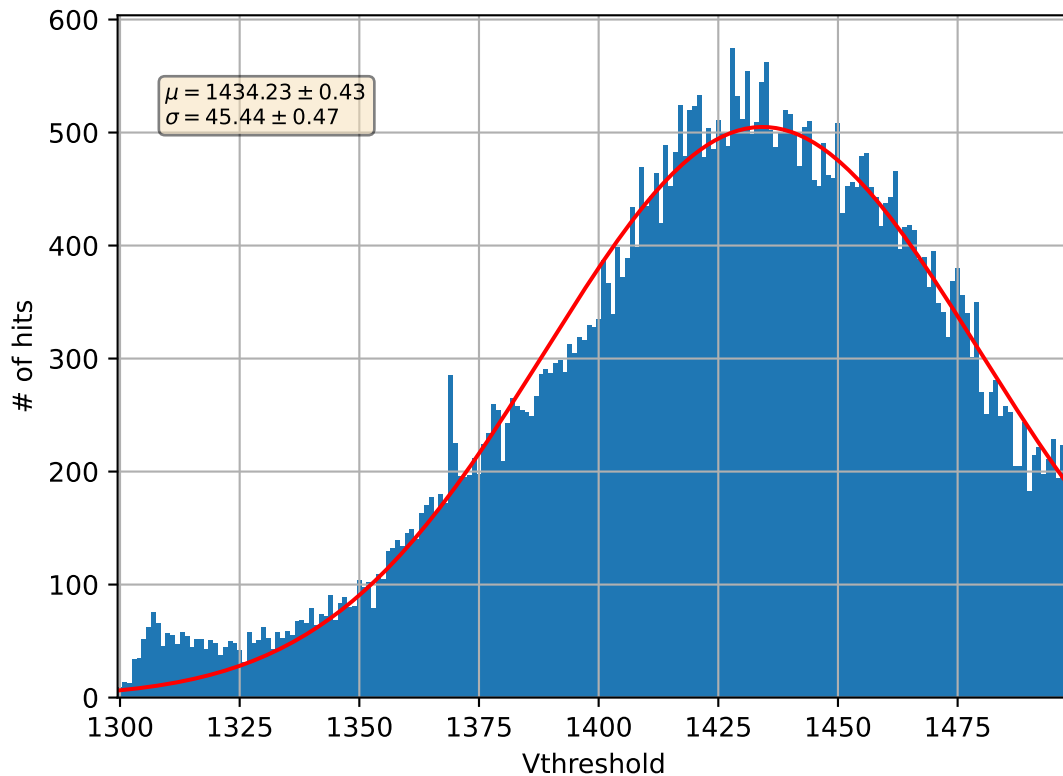
Timepix3 draft

Chip: W15-G6

S-curves for 65536 pixel(s)



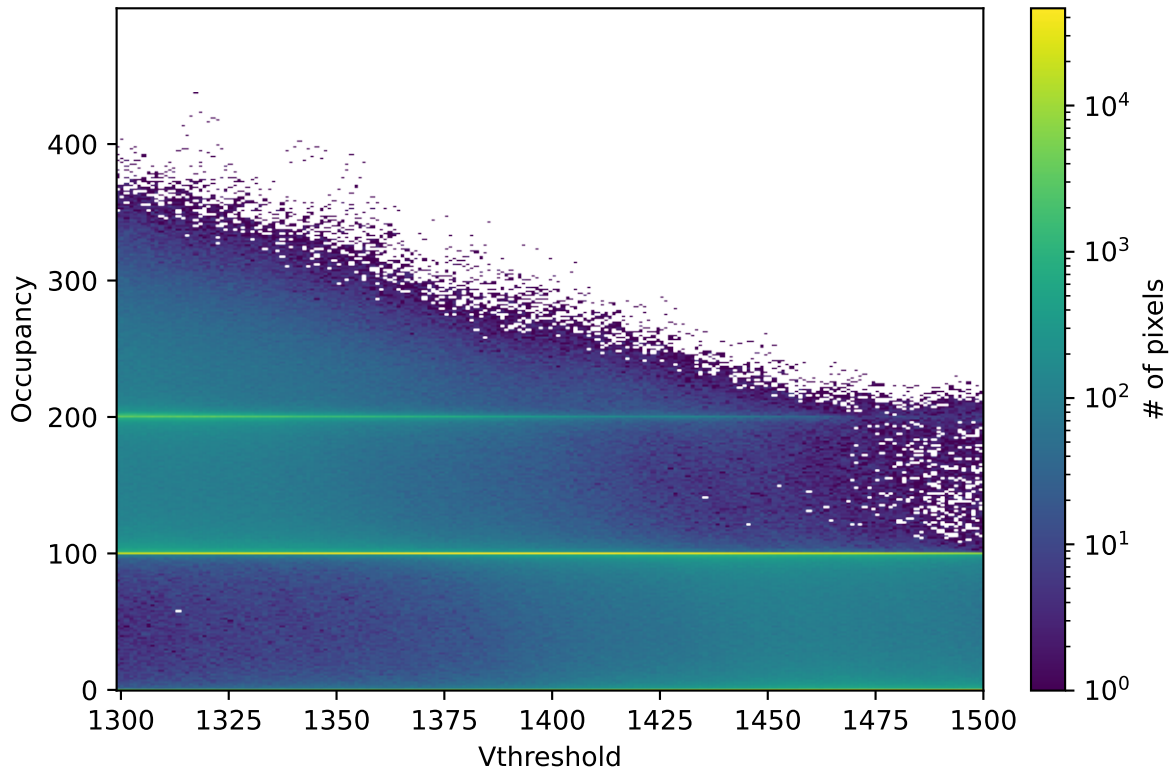
## Threshold distribution



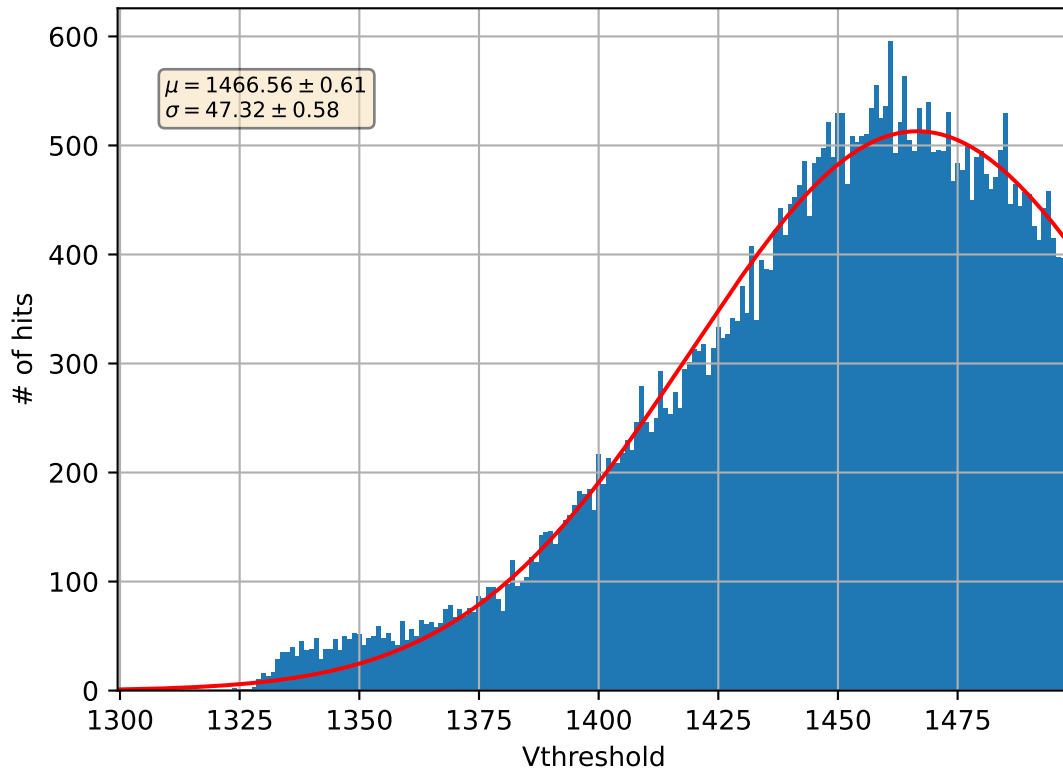
Timepix3 draft

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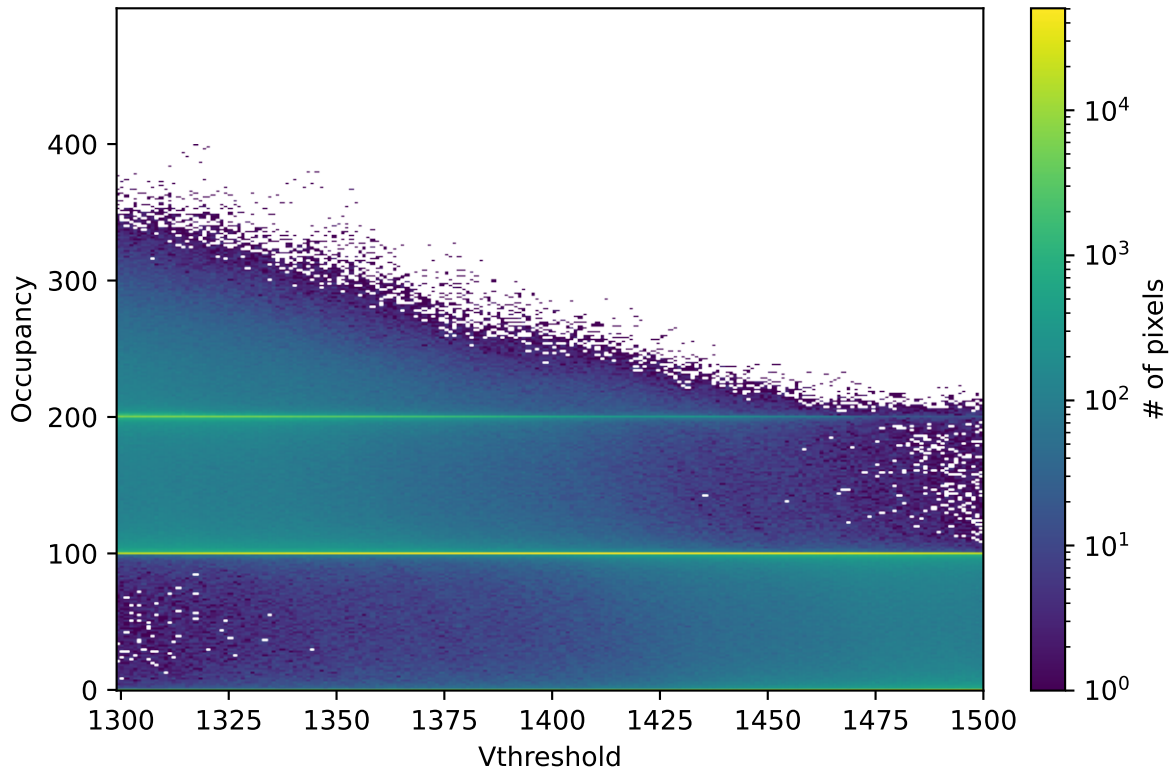
## Threshold distribution



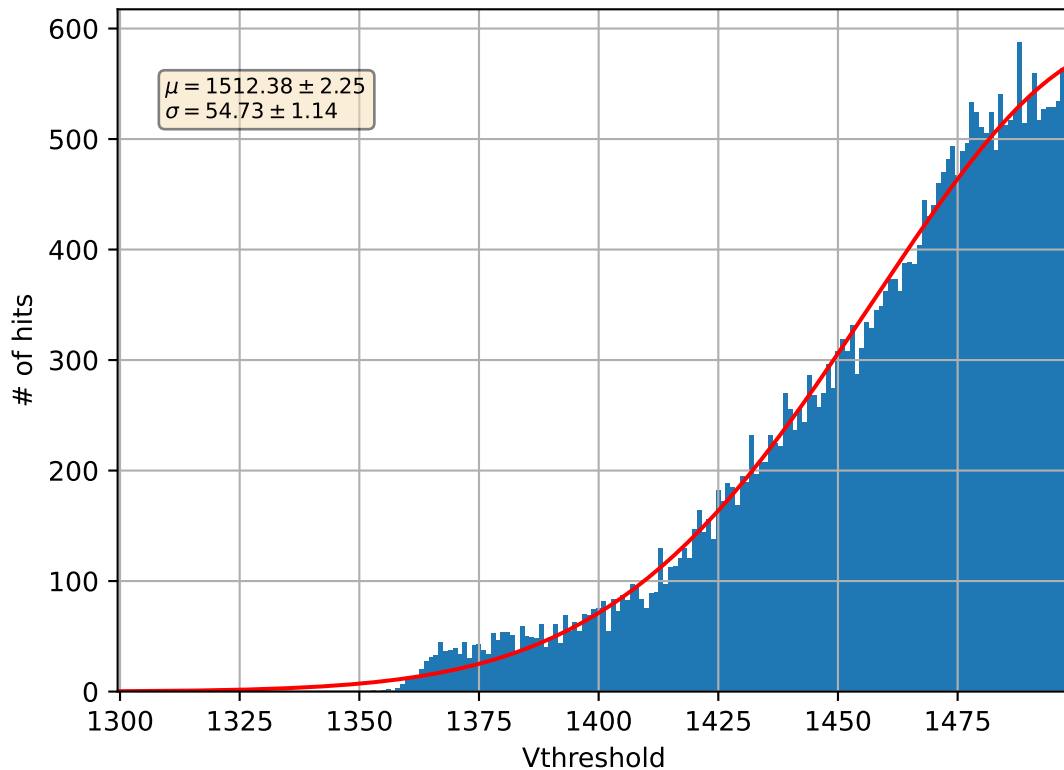
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## Threshold distribution

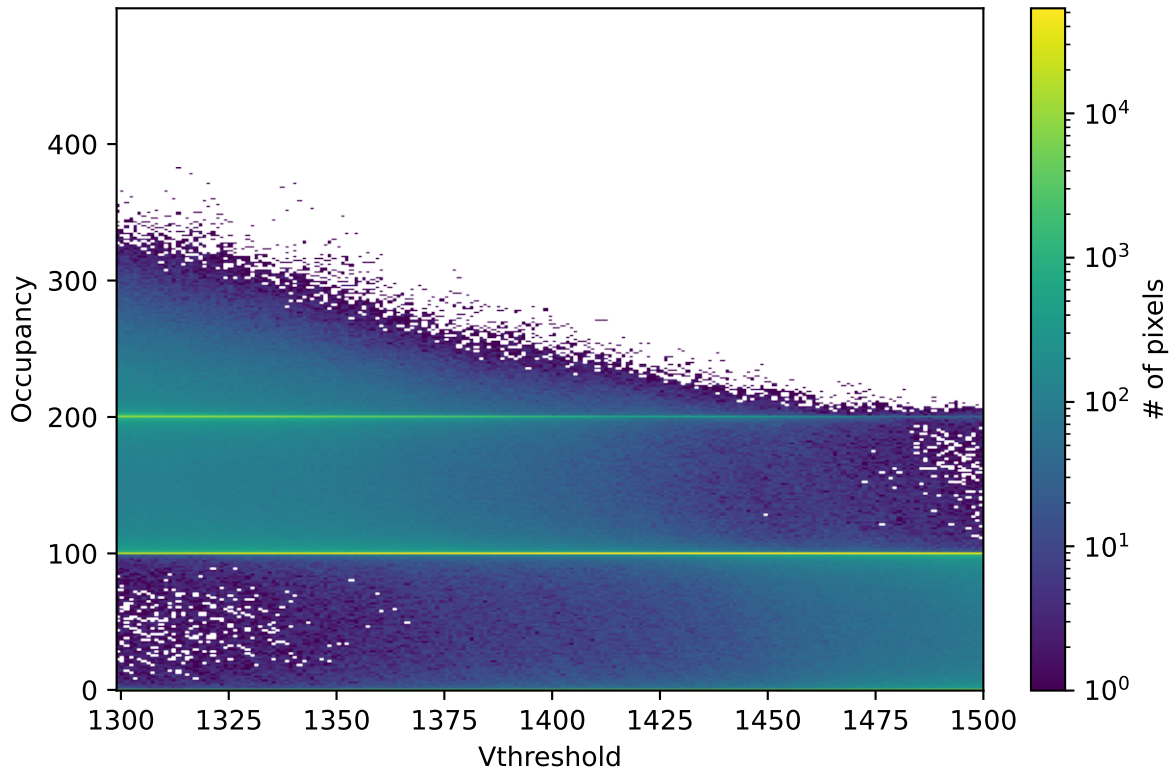




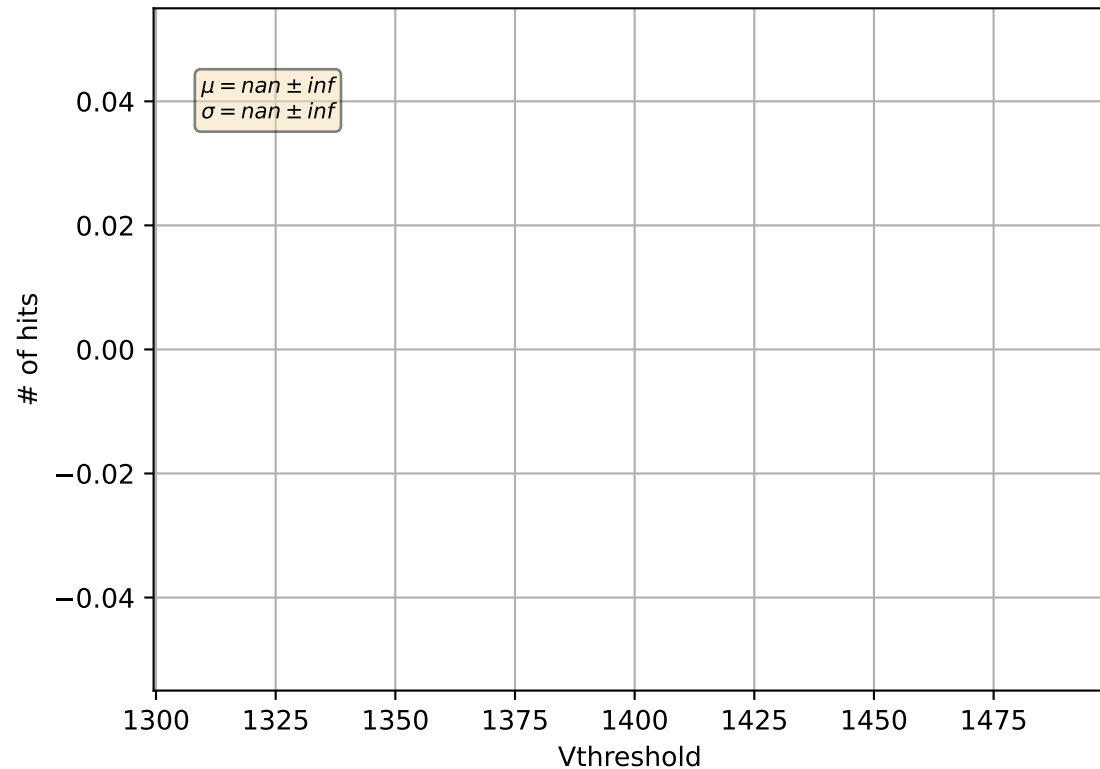
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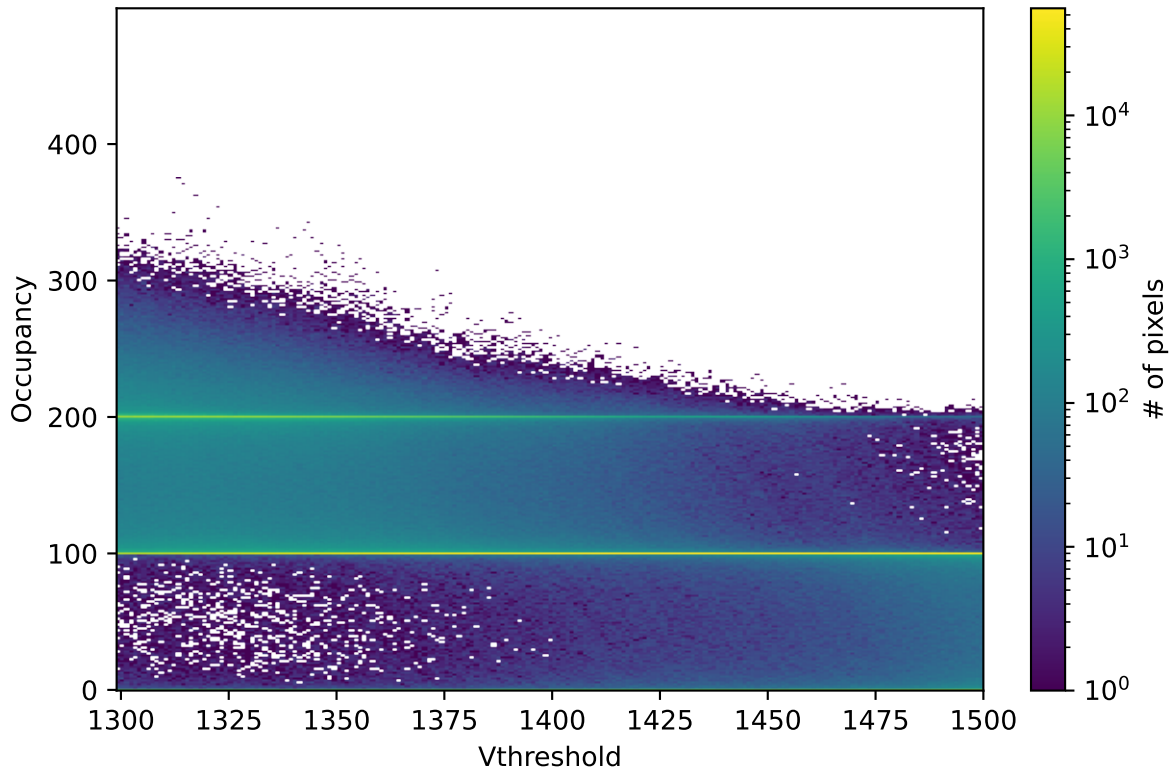
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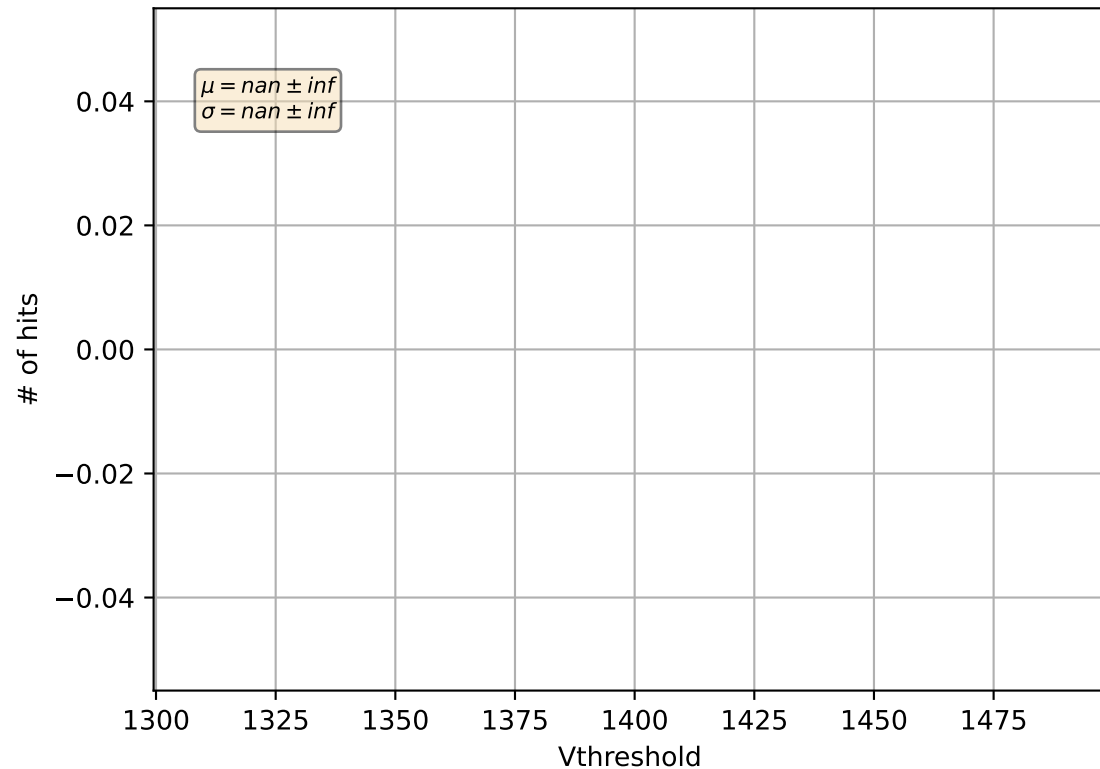
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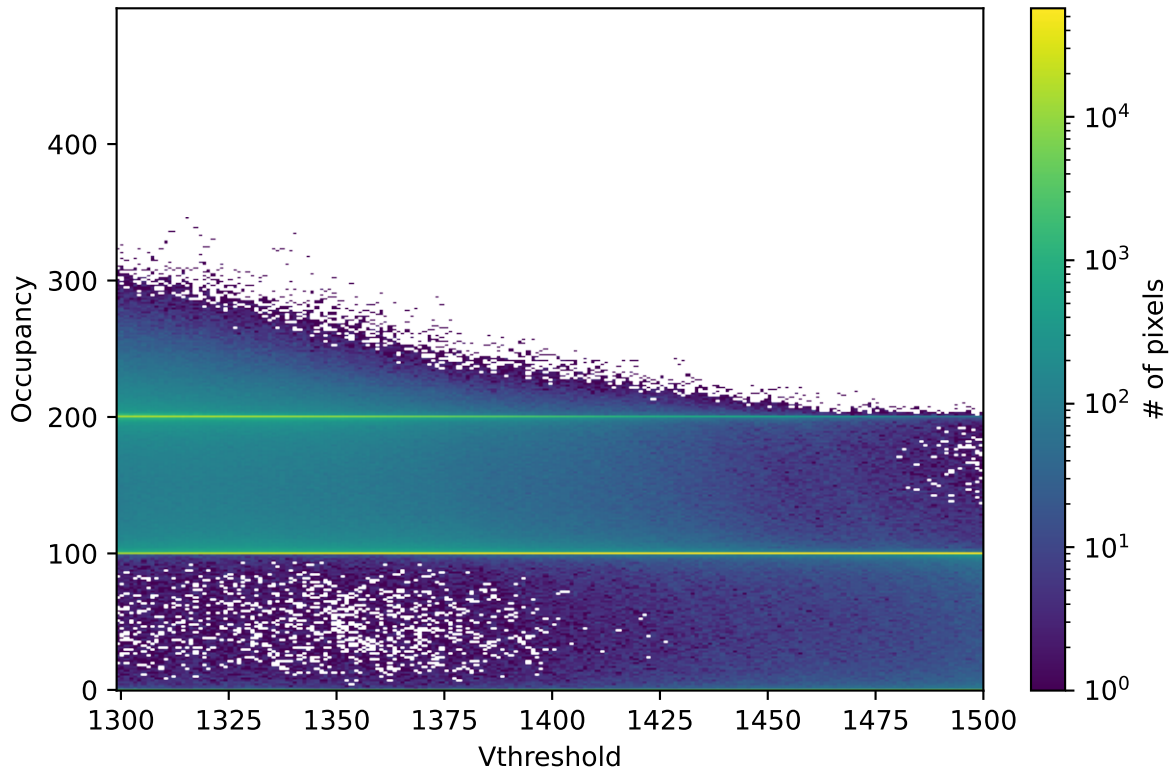
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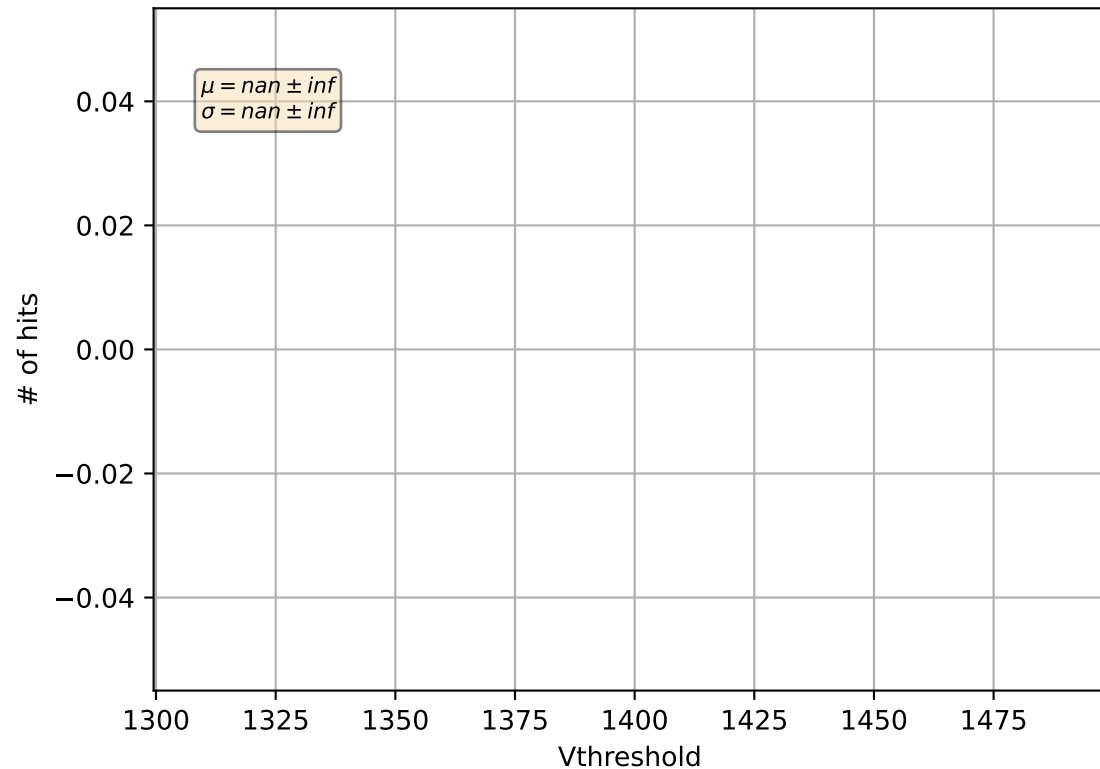
Timepix3 draft

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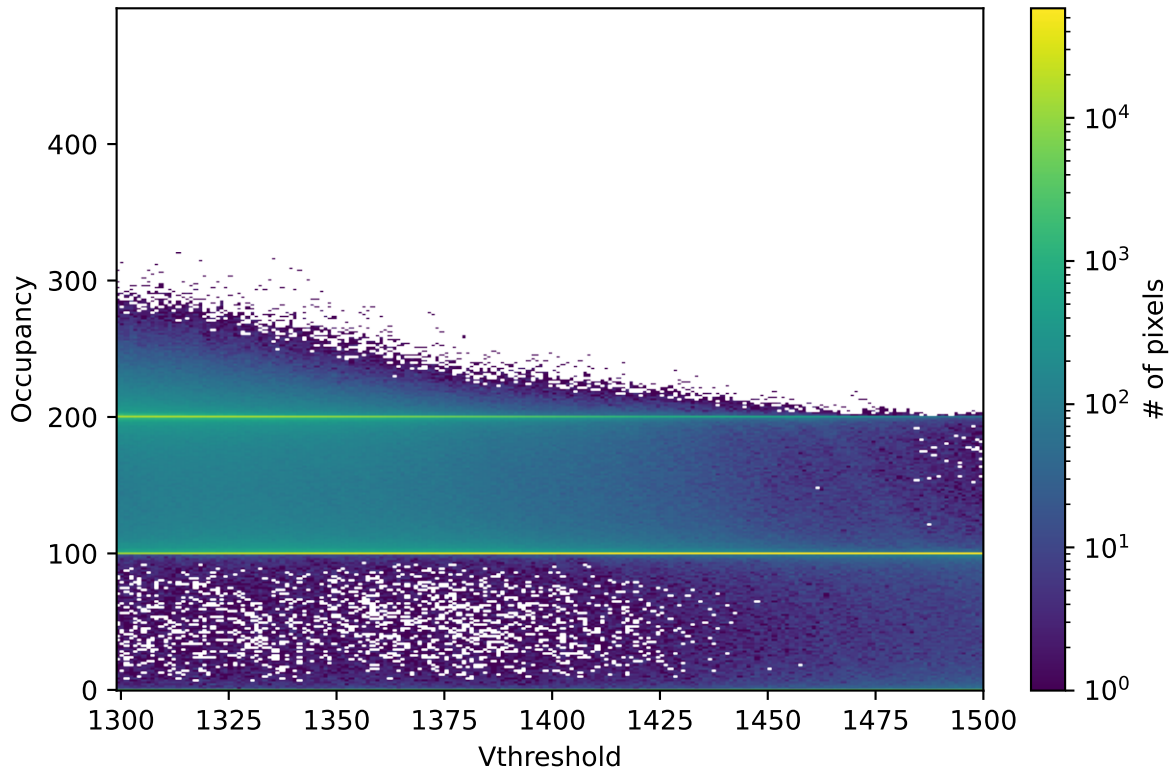
## Threshold distribution



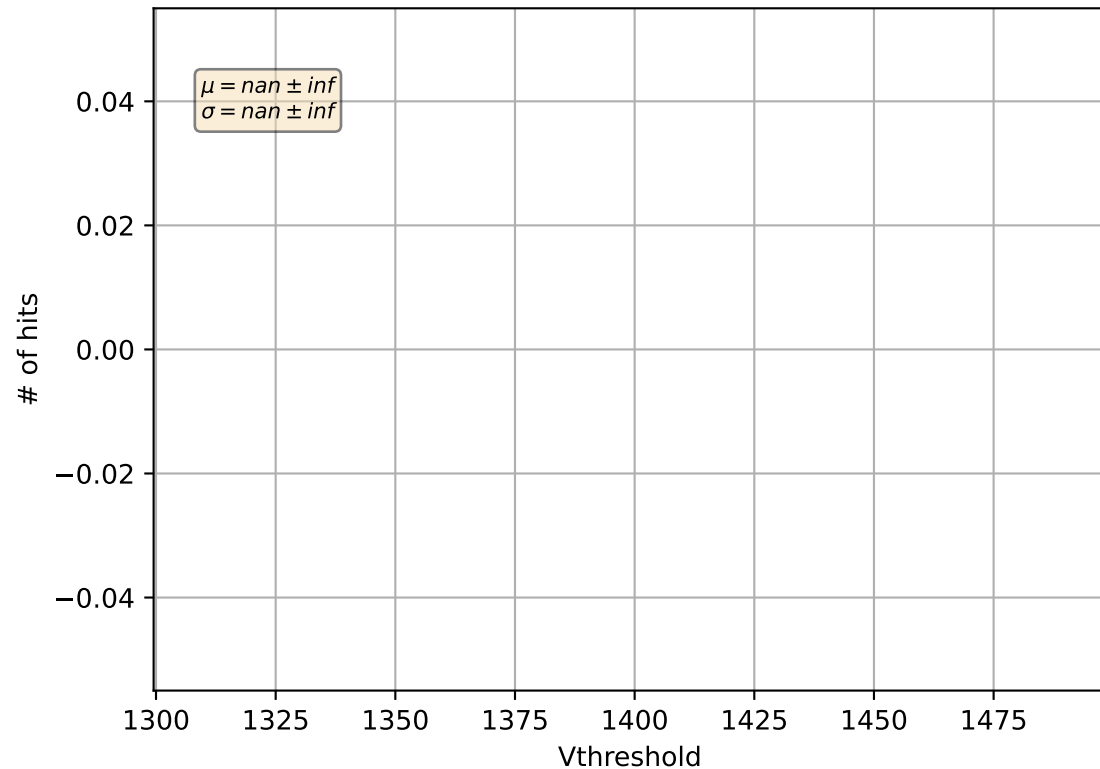
Timepix3 draft

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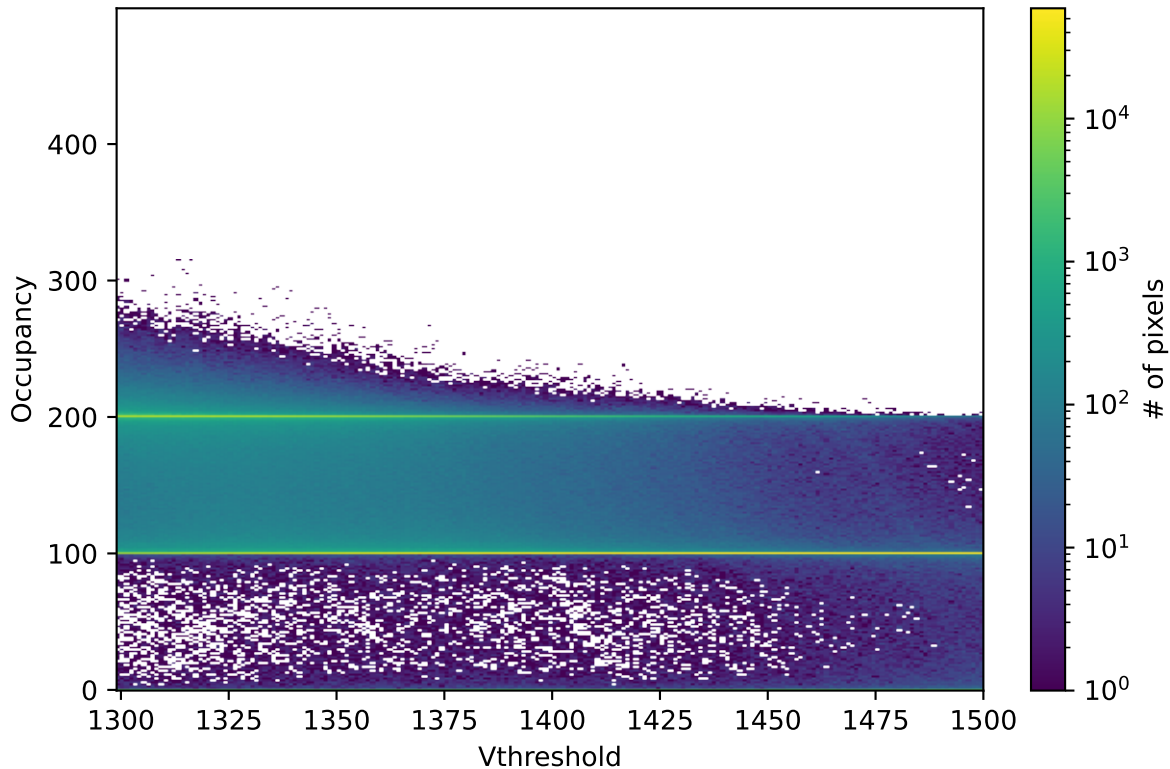




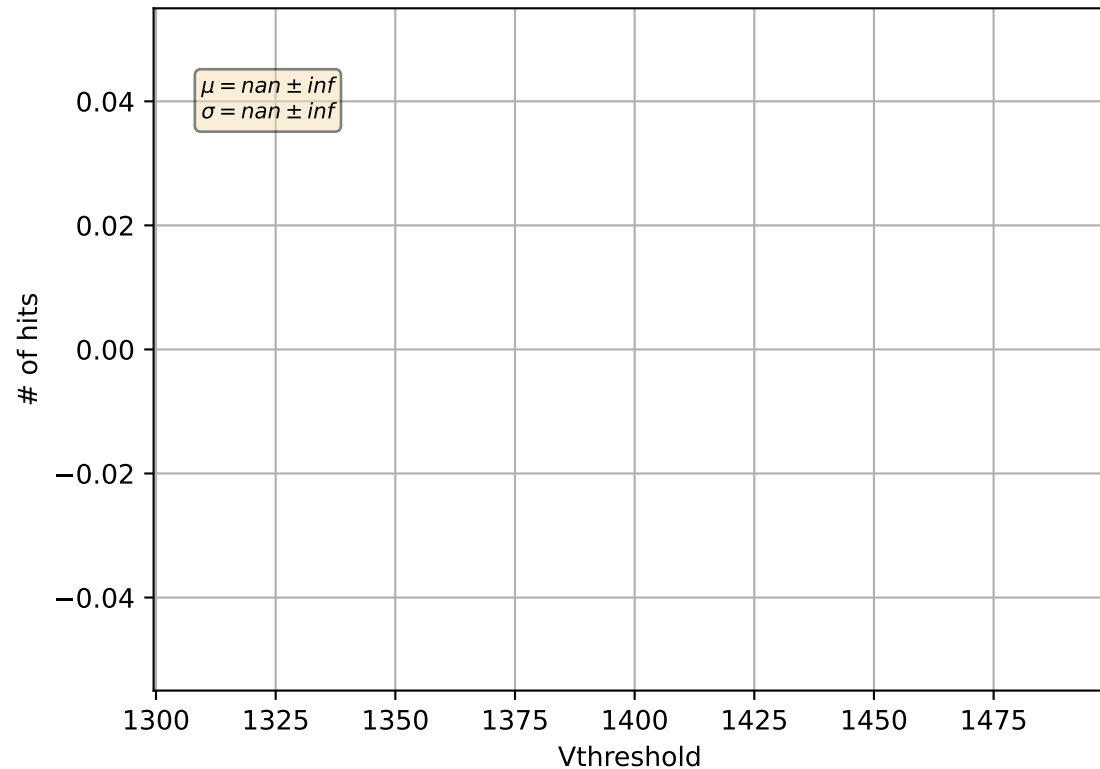
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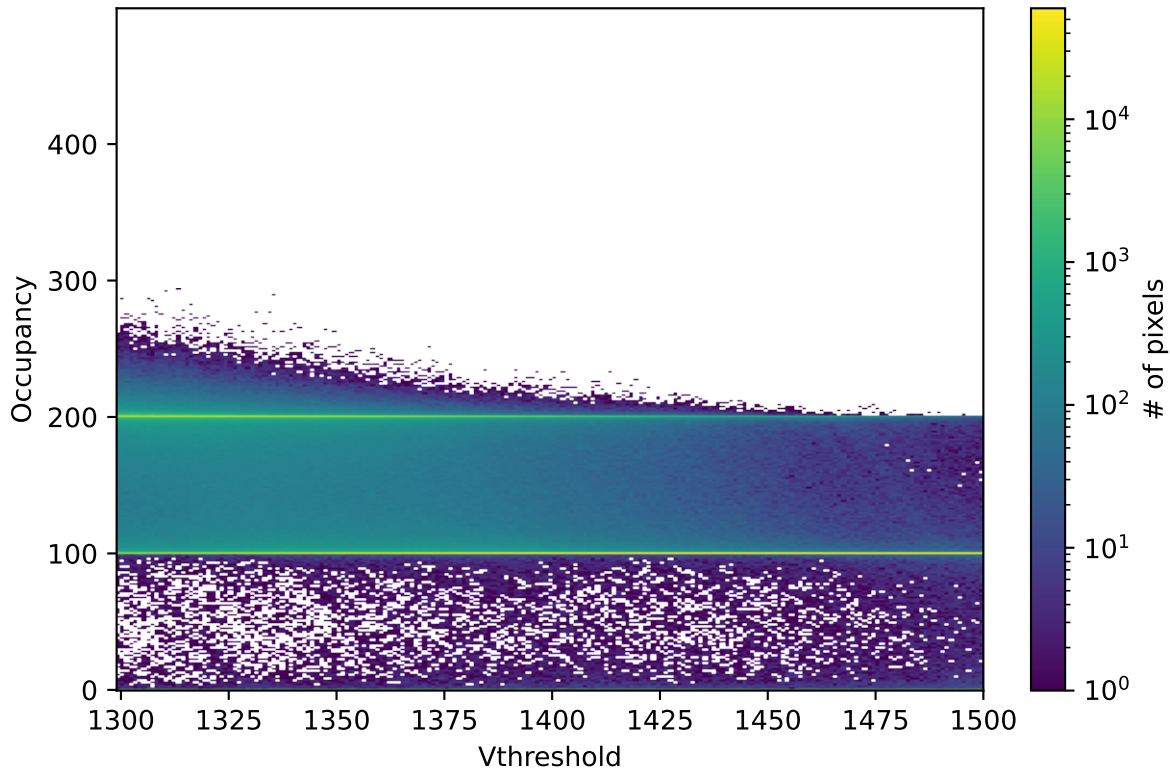
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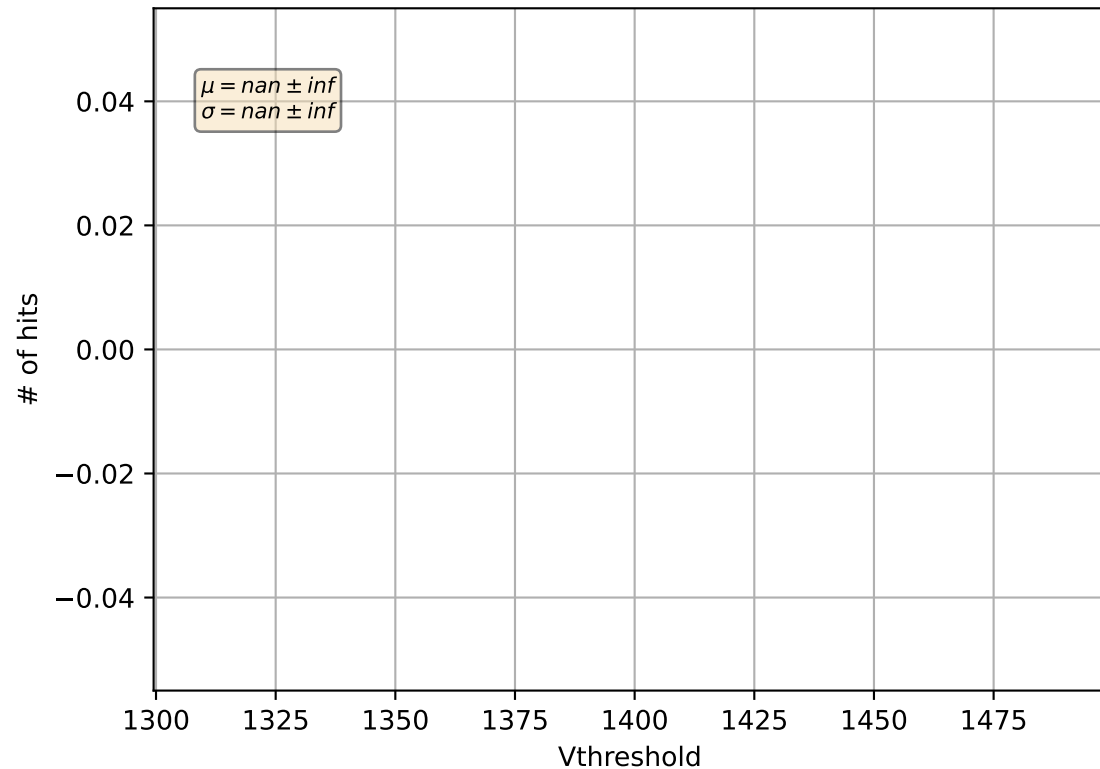
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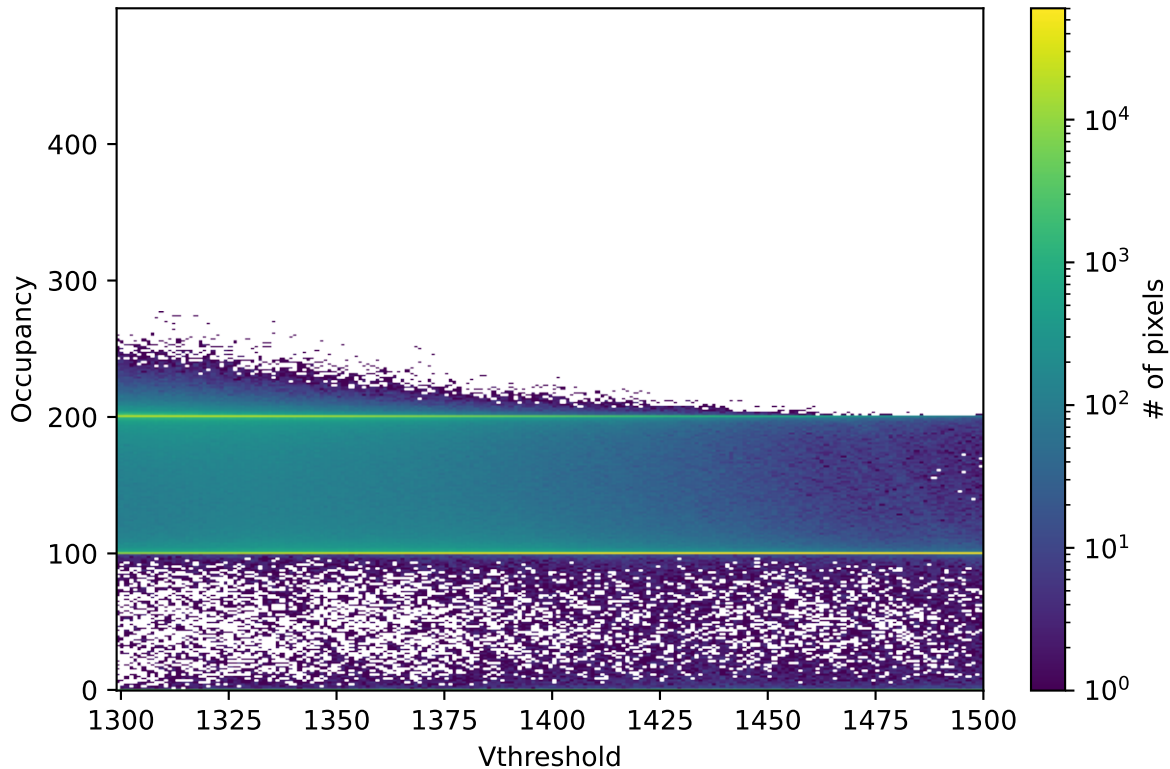
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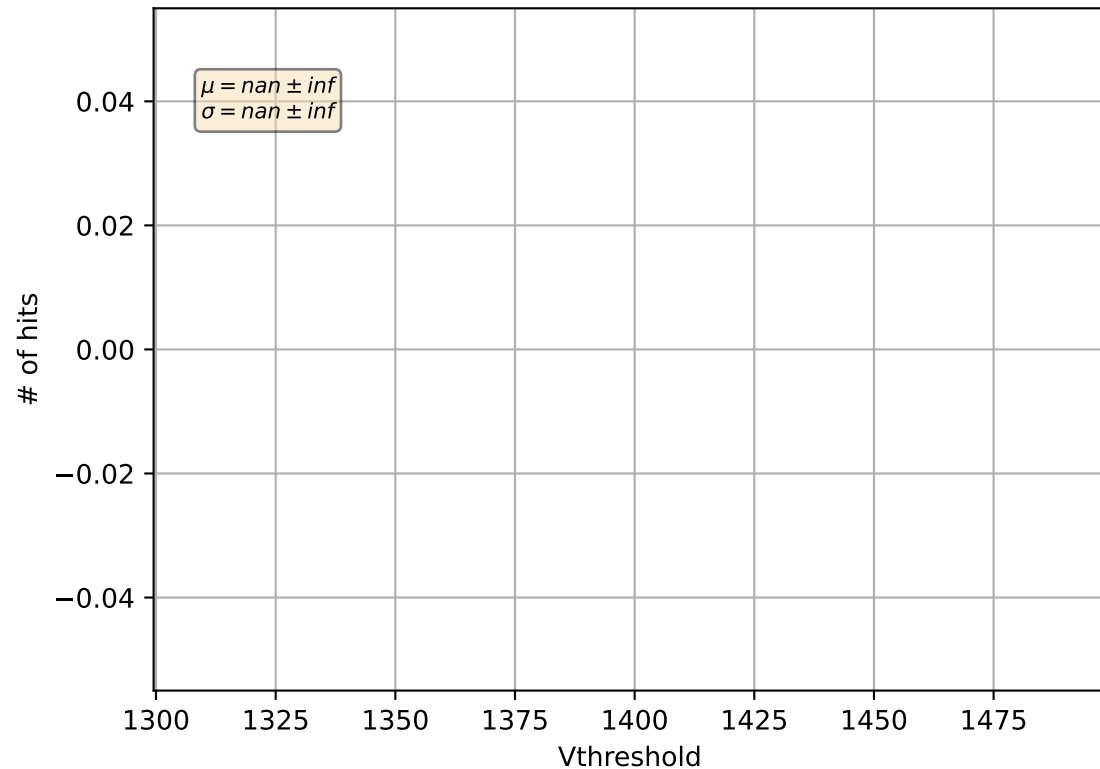
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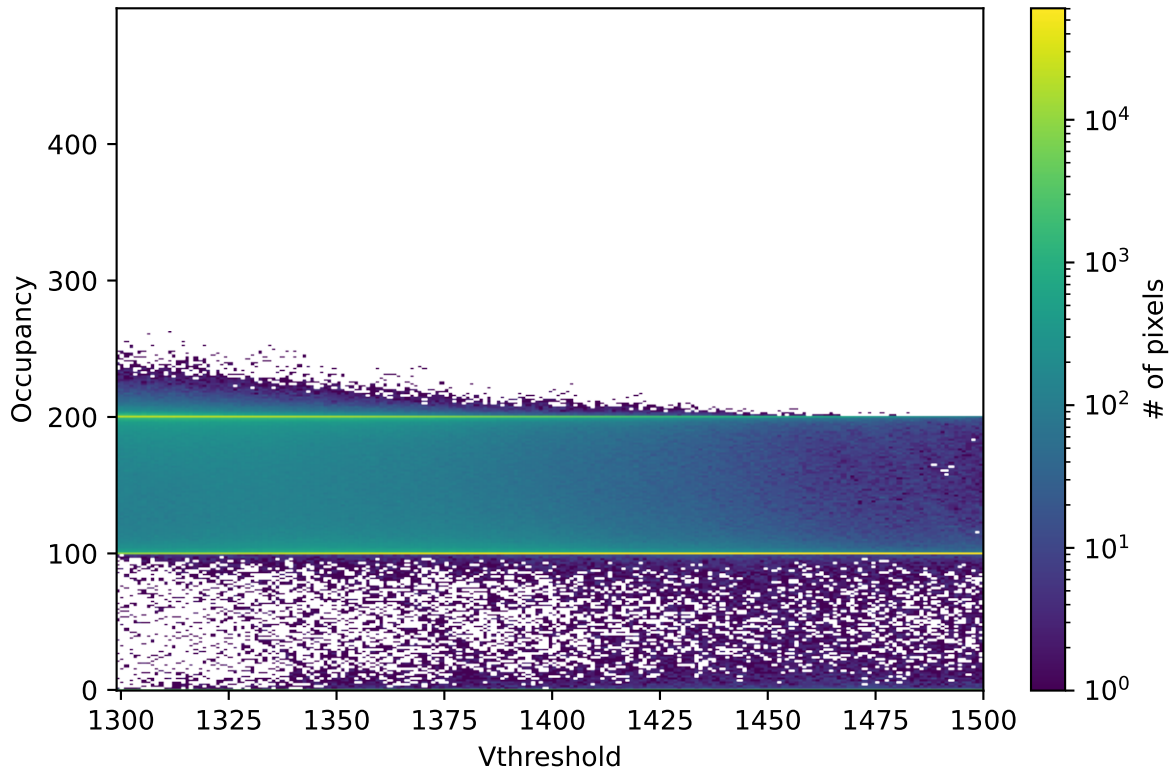
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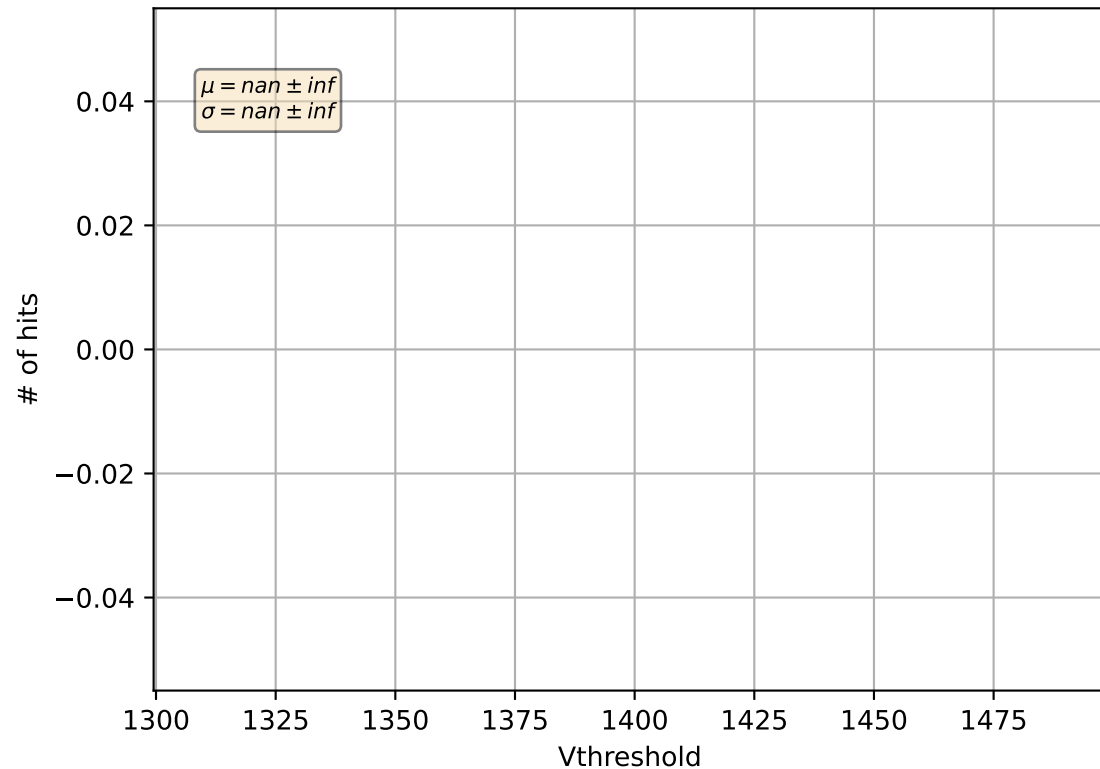
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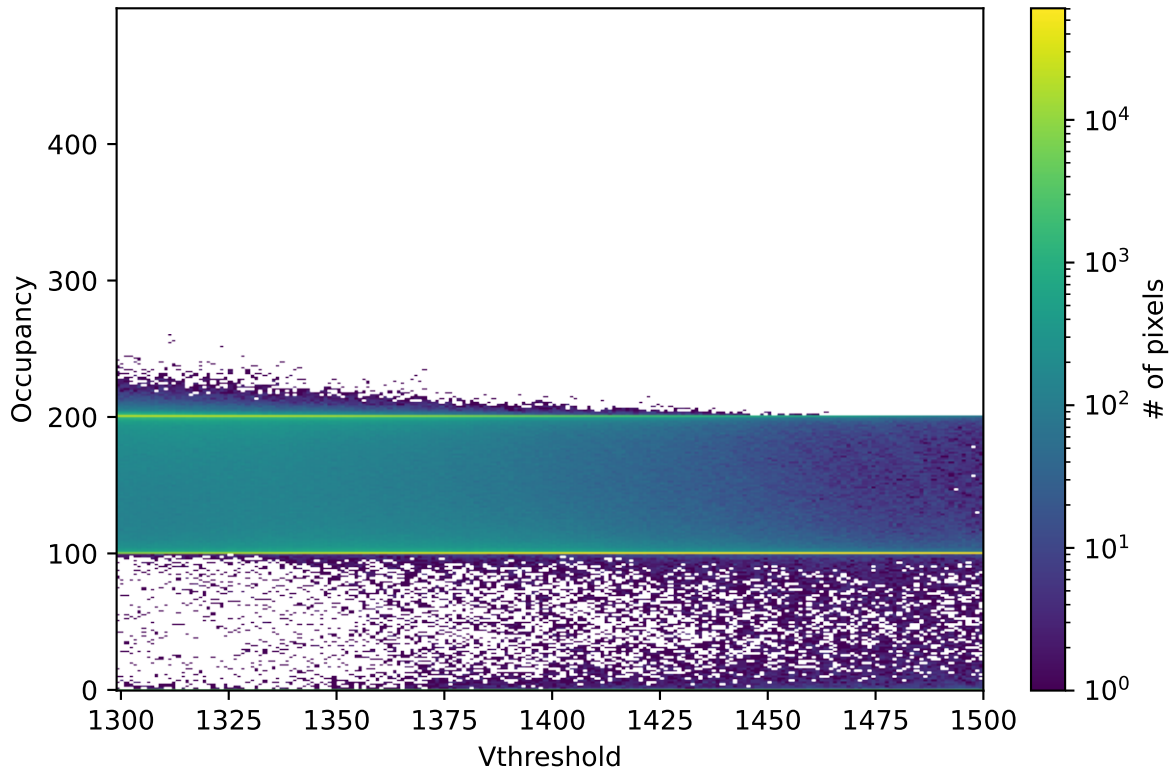




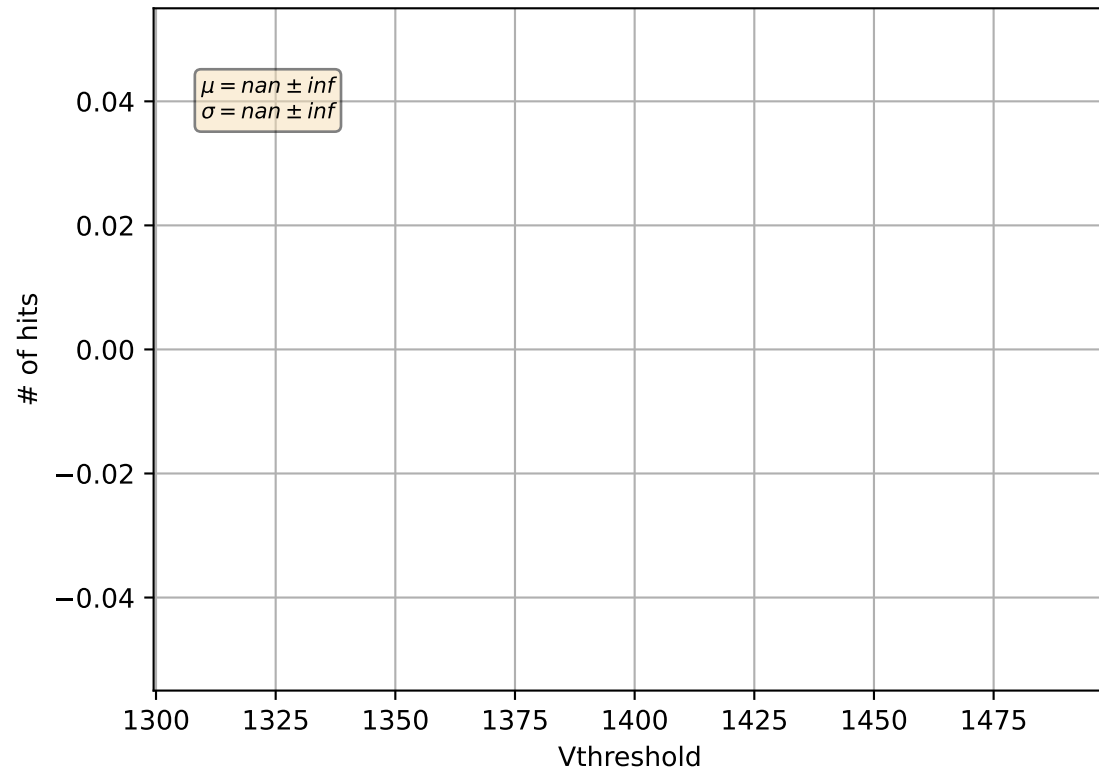
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## Threshold distribution



## Threshold calibration

