

Multistage Amplifiers

Introduction:-

Amplifiers:-

- *) An electronic amplifiers ckt is one, which modifies the characteristics of the input signal, when delivered the output side.
- *) The modification in the characteristics of the input signal can be w.r.t Voltage, current, power or phase.

Classification of Amplifiers:-

- *) Based on type of signal
 - 1) Small signal
 - 2) Large signal
- *) Based on type of Configuration
 - 1) Common Emitter
 - 2) Common Base
 - 3) Common Collector
- *) Based on class of Configuration
 - 1) class A amplifier
 - 2) class B amplifier
 - 3) class AB amplifier
 - 4) class C amplifier

*) Based on frequency of operation,

1) Direct Current (DC)

2) Audio Frequencies (AF)

3) Radio Frequencies (RF)

4) VHF, UHF and SHF frequencies

*) Based on type of coupling:

1) RC Coupled amplifiers

2) Inductive coupled amplifiers

3) Transformer coupled amplifiers

4) Direct Coupled amplifiers

*) Based on Number of stages

1) Single stage Amplifiers

2) Two stage Amplifiers

3) Multistage Amplifiers

4) The number of stages = N .

*) Based on the output

a) Voltage amplifier

b) Current amplifier

*) Based on the frequency response

a) Audio frequency (AF)

b) Intermediate frequency (IF)

c) Radio Frequency (RF)

*) Based on the bandwidth

a) Narrow band amplifier

b) Wide band amplifier

Multistage Amplifiers:-

- * If a voltage or power gain obtained from a single stage small signal amplifier is not sufficient for a practical application, one have to use more than one stage of amplification to achieve necessary voltage and power gain.
- * Such an amplifier is called multistage amplifier.
- * In multistage amplifier, the output of one stage is fed as the input to the next stage.
- * Such a connection is commonly referred to as cascading.
- * A multistage amplifier using two or more single stage common emitter amplifier is called as cascade amplifier.
- * If a multistage amplifier with common emitter amplifier as the first stage and common base as the second stage is called as cascode amplifier.

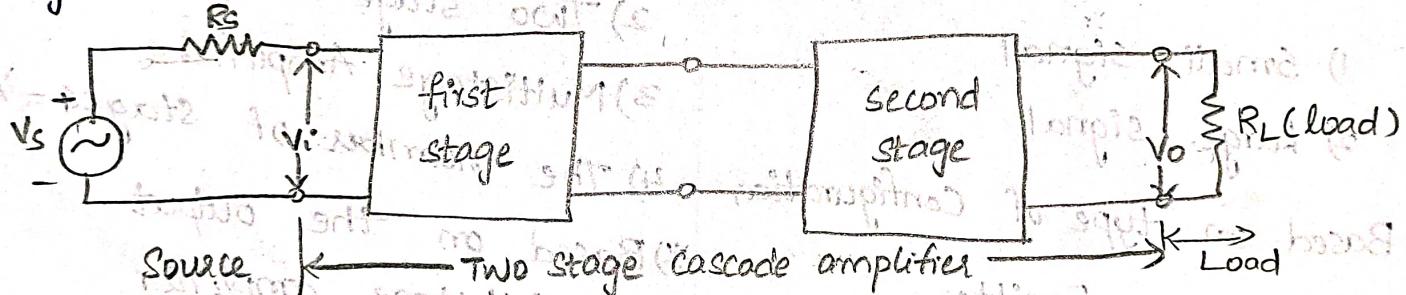


fig: Multistage Amplifier

Different Coupling Schemes Used in Amplifiers:-

- * When amplifiers are cascaded, it is necessary to use a coupling network between the output of one amplifier and the input of the following amplifier.
- * This type of coupling is called inter stage coupling.
- * This type of coupling networks serve the following two purposes:
 - 1) It transfer the a.c output of one stage to

the input of the next stage.

2) It isolates the d.c. conditions of one stage to the next.

1) Resistance - capacitance (RC) coupling

- * It is most commonly used discrete device amplifiers as it is least expensive and has satisfactory frequency response.
- * In this method, the signal developed across the collector resistor R_C of each stage is coupled through capacitor C_C into the next stage.
- * The coupling capacitor C_C isolates the dc conditions of one stage from the following stage.

* The amplifiers using this coupling scheme are called RC coupled amplifiers.

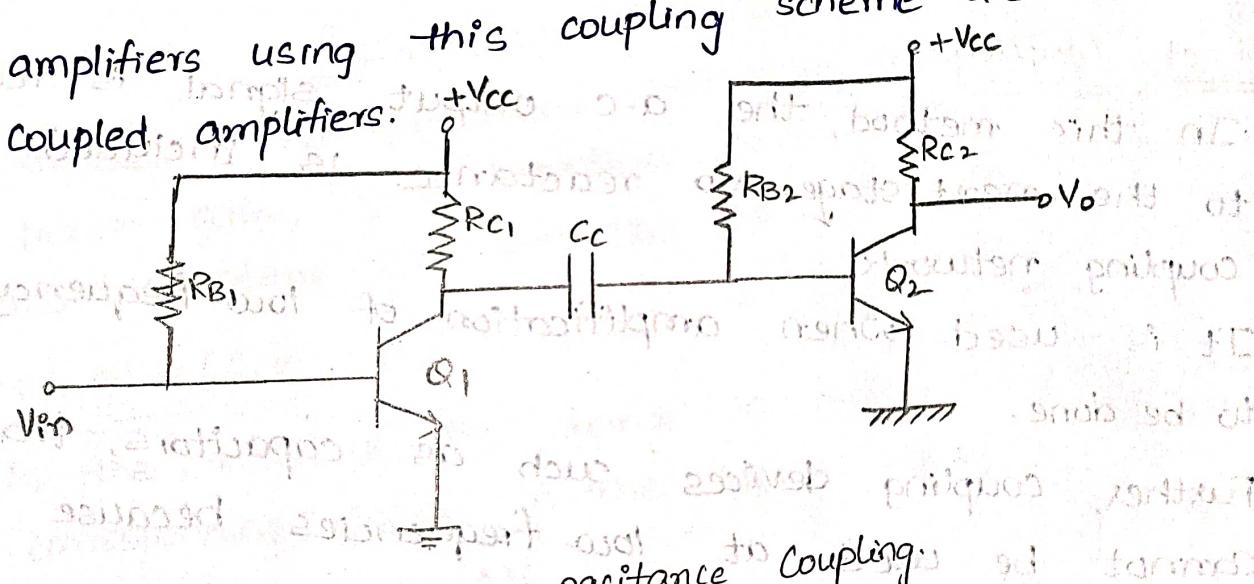


fig:- Resistance - capacitance Coupling is formed

2) Transformer Coupling: This type of coupling uses transformer.

- * In this method, the primary winding of the transformer acts as a collector load and the secondary winding transfers the a.c. output signal directly to the base of the next stage.

* Such a coupling increases the overall circuit gain and the level of inter stage impedance matching.

* However, transformers with broad frequency response are very expensive and hence, this type of coupling is restricted mostly to power amplifiers where efficient

Impedance matching, is a critical requirement for maximum power transfer and efficiency.

- * The amplifiers using this coupling scheme are called "Transformer-Coupling amplifiers."

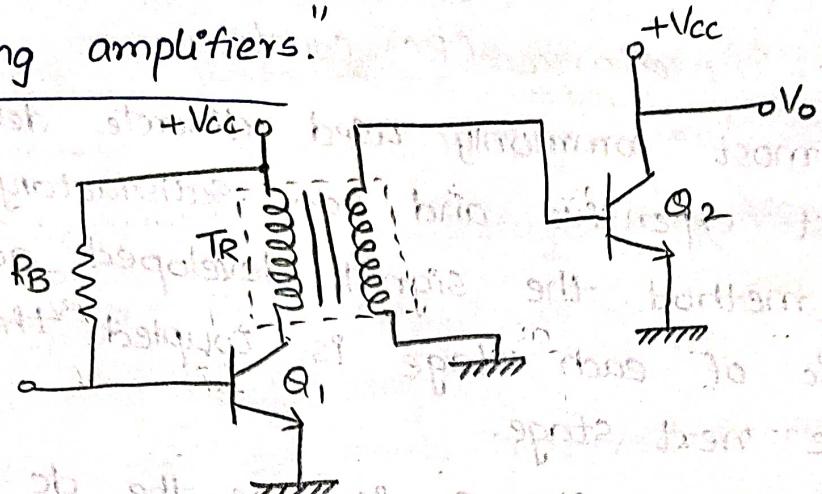


Fig: Transformer Coupling.

3) Direct Coupling:-

- * In this method, the a.c output signal is fed directly to the next stage. No reactance is included in the coupling network.
- * It is used when amplification of low frequency signals is to be done.
- * Further, coupling devices such as capacitors, transformers cannot be used at low frequencies because their size becomes very large.
- * The amplifiers using this coupling scheme are called "Direct-Coupled Amplifiers".

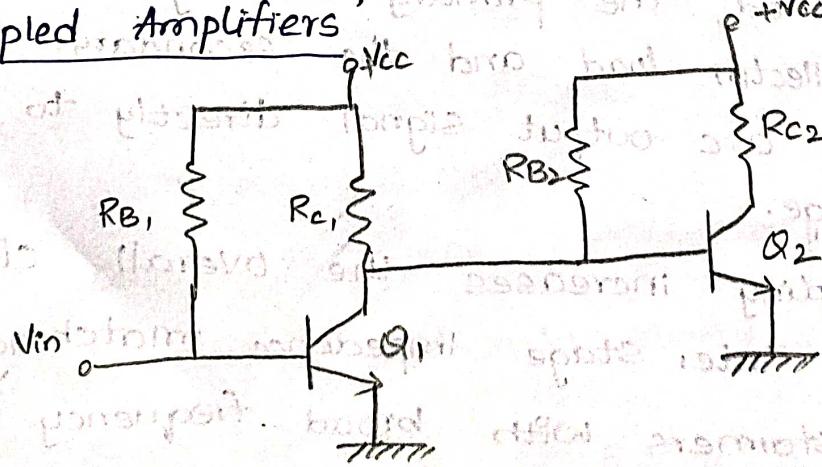


Fig: Direct Coupling.

General Analysis of Cascade Amplifiers:-

- * The most popular cascade amplifier is formed by cascading several CE amplifier stages.
- * Considering the analysis of general 'n' stages CE amplifier as shown in fig:

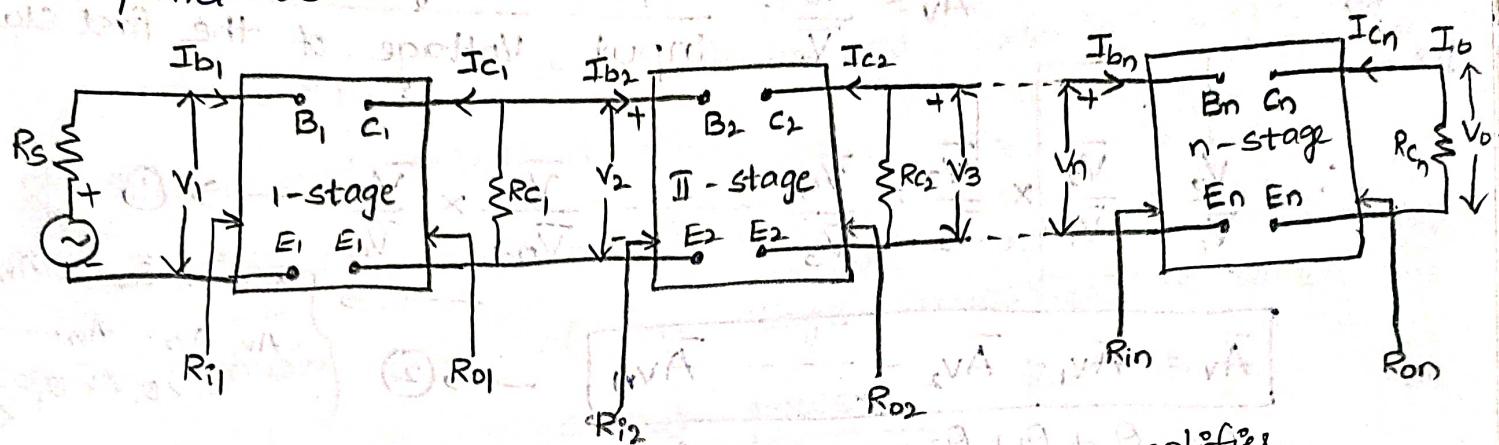


fig: n-stage CE cascade amplifier

- * The expressions for quantities such as voltage gain, current gain, power gain, input impedance and output impedance of this 'n' stage CE amplifier are derived.

(a) Voltage gain (A_v):-

* It is the ratio of the change in output voltage to the corresponding change in the input voltage

$$A_v = \frac{\Delta V_o}{\Delta V_i}$$

- * In a multistage amplifier, the output voltage of first stage acts as the input voltage to next stage, and so on.
- * The voltage gain of the complete cascade amplifier is equal to the product of the voltage gain of the individual stages.

* The voltage gain of the first stage,

$$\bar{A}v_1 = \frac{\bar{V}_2}{\bar{V}_1} = \frac{\text{output Voltage of first stage}}{\text{input Voltage of first stage}}$$

- * Similarly expressions can be written for all the 'n' stages of the cascade amplifier.
- * The resultant voltage gain,

$$\bar{A}_v = A_v < \theta$$

$$\bar{A}_v = \frac{\bar{V}_o}{\bar{V}_i} = \frac{\text{output Voltage of the } n^{\text{th}} \text{ stage}}{\text{input Voltage of the first stage}}$$

$$\therefore \frac{\bar{V}_o}{\bar{V}_i} = \frac{\bar{V}_2}{\bar{V}_1} \times \frac{\bar{V}_3}{\bar{V}_2} \times \frac{\bar{V}_4}{\bar{V}_3} \cdots \frac{\bar{V}_n}{\bar{V}_{n-1}} \times \frac{\bar{V}_o}{\bar{V}_n} \rightarrow ①$$

$$\boxed{\bar{A}_v = \bar{A}_{v_1} \cdot \bar{A}_{v_2} \cdots \bar{A}_{v_n}}$$

$$\theta = \theta_1 + \theta_2 + \theta_3 \cdots \theta_n$$

$$\left. \begin{aligned} \bar{A}_v &= \bar{A}_{v_1} \cdot \bar{A}_{v_2} \cdots \bar{A}_{v_n} \\ &= A_{v_1} \cdot A_{v_2} \cdots A_{v_n} \\ &\quad - \theta_1 + \theta_2 + \cdots + \theta_n \end{aligned} \right\}$$

(b) Current Gain (A_I):-

In order to find the resultant voltage gain, the voltage gain of the individual stages can be found out and the product of these gains gives the resultant voltage gain.

Alternatively, the resultant voltage gain can be found directly by the relation:

$$\boxed{\bar{A}_v = \frac{\bar{A}_I R_{Cn}}{R_{i1}}} \rightarrow ③$$

Where, \bar{A}_I is the current gain of the complete n-stage amplifier.

Now, \bar{A}_I is given by,

$$\bar{A}_I = \frac{\bar{I}_o}{\bar{I}_{b_1}} = - \frac{\bar{I}_{c_n}}{\bar{I}_{b_1}} \rightarrow ④$$

$$\text{Now, } - \frac{\bar{I}_{c_n}}{\bar{I}_{b_1}} = - \frac{\bar{I}_{c_1}}{\bar{I}_{b_1}} \cdot \frac{\bar{I}_{c_2}}{\bar{I}_{c_1}} \cdots \frac{\bar{I}_{c_n}}{\bar{I}_{c_{n-1}}} \frac{\bar{I}_{c_n}}{\bar{I}_{c_{n-1}}} = n \bar{A}$$

$$\text{or, } \bar{A}_I = \bar{A}_{I_1} \cdot \bar{A}'_{I_2} \cdot \bar{A}'_{I_3} \cdots \bar{A}'_{I_n} \rightarrow ⑥$$

Here, \bar{A}_{I_1} is the base to collector gain of the first stage

and equals $-\frac{\bar{I}_{C1}}{\bar{I}_{B1}}$.

While, $\bar{A}'_{I_2}, \bar{A}'_{I_3}$ are the collector to collector current gains of second and third stages.

c) Power Gain (\bar{A}_P):-

* The power gain of n -stage amplifier is given by,

$$\bar{A}_P = \frac{\text{Output power of last stage}}{\text{Input power of first stage.}}$$

$$= \frac{\bar{V}_o \bar{I}_o}{\bar{V}_i \bar{I}_b} = \frac{-\bar{V}_o \bar{I}_{Cn}}{\bar{V}_i \bar{I}_{B1}}$$

$$\boxed{\bar{A}_P = \bar{A}_v \cdot \bar{A}_I} \rightarrow ⑦$$

Substituting,

$$\bar{A}_v = (\bar{A}_I^*) \frac{R_{Cn}}{R_{i1}} \quad \text{in eqn } ⑦, \text{ we get}$$

$$\boxed{\bar{A}_P = (\bar{A}_I)^2 \cdot \frac{R_{Cn}}{R_{i1}}} \rightarrow ⑧$$

Input Impedance:

* By starting from last stage and proceeding towards the first stage, the input impedance can be found out as

Final i) $\bar{A}_{In} = \frac{-h_{fe}}{1 + h_{re} R_{Ln}}$

ii) $R_{in} = h_{ie} + h_{re} \bar{A}_{In} R_{Ln}$

iii) $\bar{R}_{L(n-1)} = R_{C(n-1)} // R_{i(n)}$

$R_{L(n-1)} - (n-1)^{th}$ stage load impedance

iv) calculate $\bar{A}_{I(n-1)}, R_{i(n-1)} & \bar{R}_{L(n-2)}$

v) Proceed in this manner to find the effective i/p impedance of first stage.

Output Impedance:-

* The output impedance of each transistor amplifier stage and that of the complete multistage amplifiers may be calculated from the first stage.

$$Y_{o1} = h_{oe} - \frac{h_{fe} h_{re}}{h_{ie} + R_s}$$

$R_{o1} = \frac{1}{Y_{o1}}$ gives the output impedance of the first transistor.

* Parallel combination of R_{o1} with R_{C1} forms the output impedance of the first stage.

$$R_{ot1} = \frac{R_{o1} R_{C1}}{R_{o1} + R_{C1}}$$

* This R_{ot1} forms the source impedance of the second stage.

$$\text{find } R_{ot2} = R_{o2} \parallel R_{C2}, \text{ where } R_{o2} = \frac{1}{Y_{o2}}$$

Similarly, proceed to find output impedance of the last stage.

(Problems)

Choice of transistor Configuration in a Cascade Amplifier

- *) Cascading of amplifier stages is usually done to increase the total gain of the amplifier. However, sometimes cascading is done to get the desired output and input impedance for specific applications.
- *) Transistor amplifier may be connected in any of the three configurations namely Common Emitter (CE), Common base (CB) and common collector (CC).
- *) However, in cascade amplifier meant for providing high gain, only CE amplifier stage are connected in cascade, CB and CC configurations can not be used for this purpose.
- *) Choice of transistor configuration to be used for intermediate stages in a multistage amplifier depends on the maximum voltage gain provided by the configuration.
- *) Hence, CC amplifiers gives voltage gain less than unity. A single stage CB amplifier, gives voltage gain more than unity but intermediate stages can't use even CB configuration since the overall voltage gain of multistage amplifier using CB configuration is low.
- *) Hence, CE stage are popularly used for intermediate stages since its overall gain is much greater than unity.

Two-Stage RC Coupled Amplifier

- *) The resistance-capacitance coupling is, in short term ed as RC coupling. This is, mostly used coupling technique in amplifiers.

Construction of a Two-stage RC coupled Amplifier:-

- * The two stage amplifier circuit has two transistors connected in CE configuration and a common power supply V_{cc} is used.
- * The potential (Voltage) divider network R_1 & R_2 and the resistor R_E form the biasing and stabilization network.
- * The emitter by-pass capacitor C_E offers a low reactance path to the signal.
- * The resistor R_L is used as a load impedance. The input capacitor C_{in} present at the initial stage of the amplifier couples AC signal to the base of the transistor.

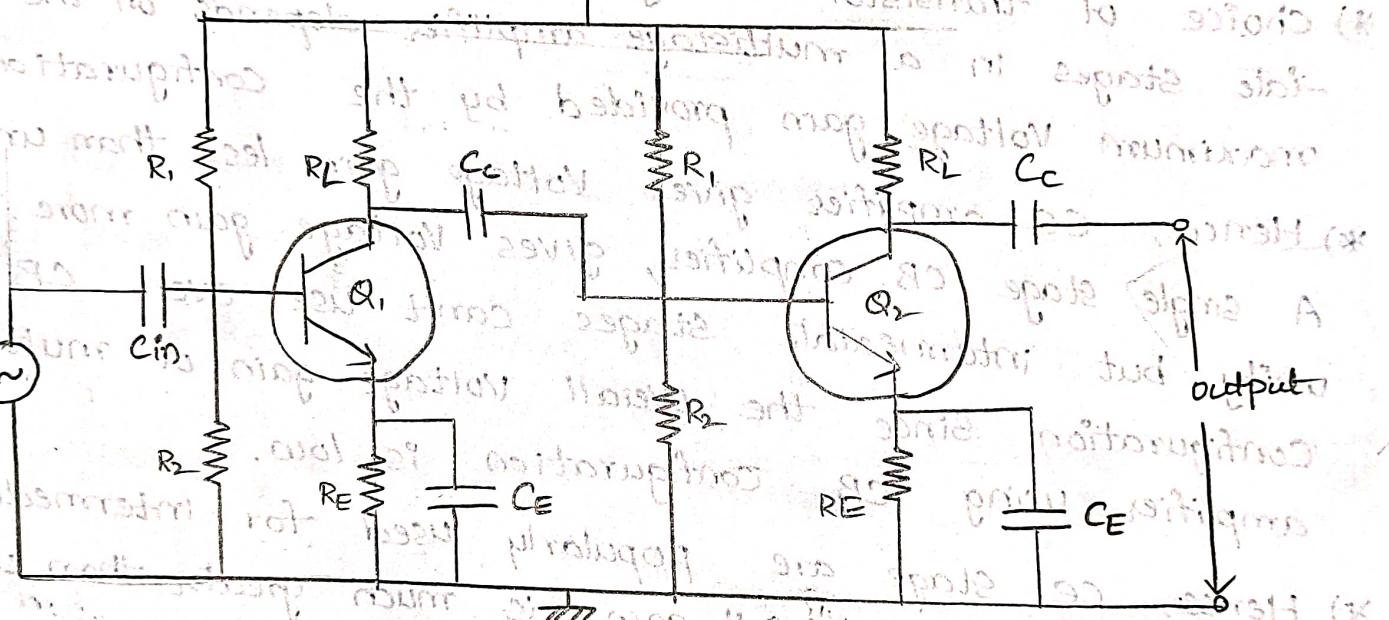


fig: RC Coupled CE Amplifier

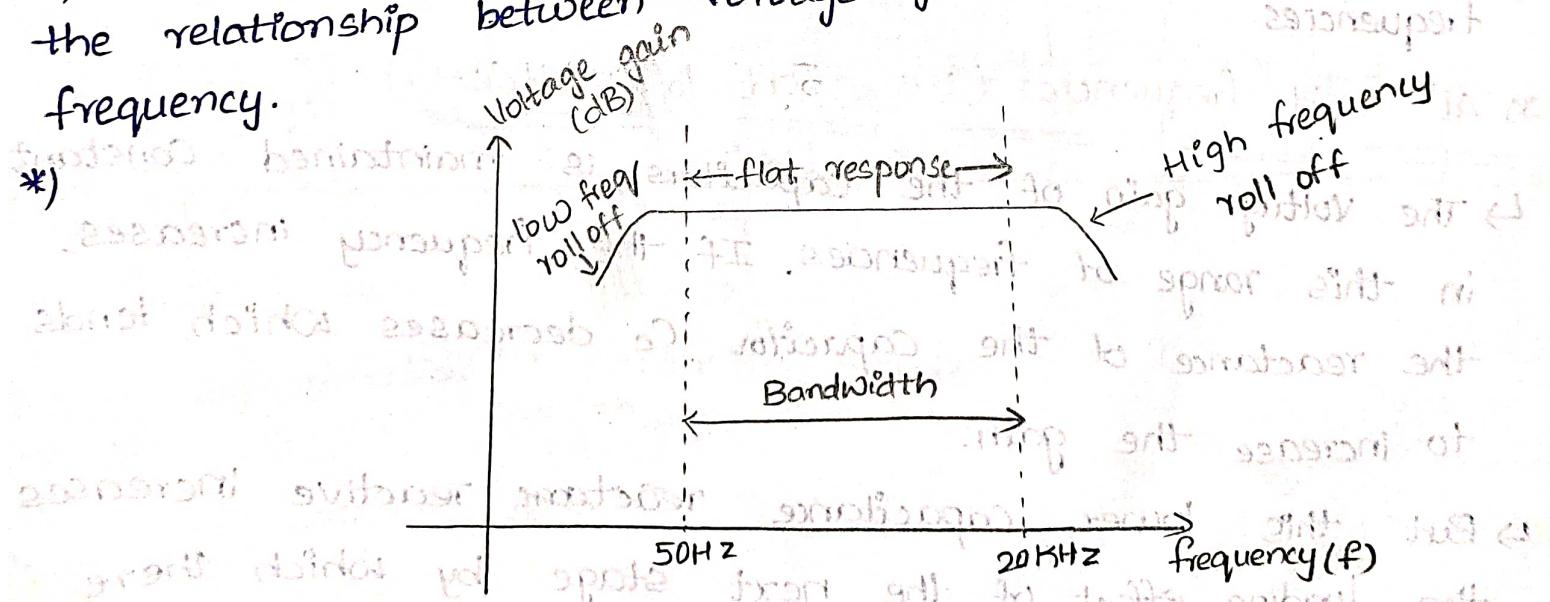
- * The capacitor C_c is the coupling capacitor that connects two stages and prevent DC interference between the stages and control the shift of operating point.

Operation of RC Coupled Amplifiers:-

- * When an AC input signal is applied to the base of first transistor, it gets amplified and appears at the collector load R_L which is then passed through the coupling capacitor C_c to the next stage.
- * This becomes the input to the next stage, whose amplified output again appears across its collector load. Thus the signal is amplified in stage by stage action.
- * The important point that has to be noted here is that the total gain is less than the product of the gains of individual stages.
- * This is because when a second stage is made to follow the first stage, the effective load resistance of the first stage is reduced due to the shunting effect of the input resistance of the second stage.

Hence, in a multi-stage amplifier, only the gain of the last stage remains unchanged.

Frequency of RC Coupled Amplifier:- A graph that indicates the relationship between voltage gain and frequency.



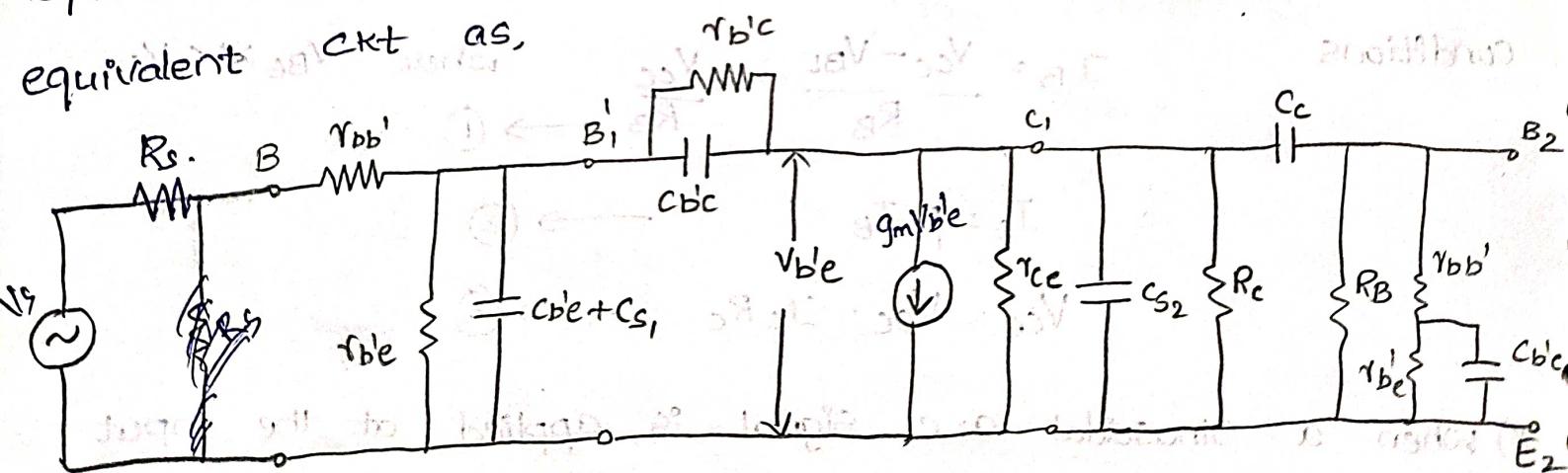
Voltages of CE amplifier

Analysis & frequency response of

RC coupled amplifier:

For finding the response of the RC coupled amplifier, in the three frequency ranges, the transistor Q_1 is replaced by its high frequency π -model yielding the

equivalent ckt as,



Here, C_{S1} , C_{S2} represent stray capacitances caused by wiring. $R_B = R_1 \parallel R_2$ is the biasing resistance of

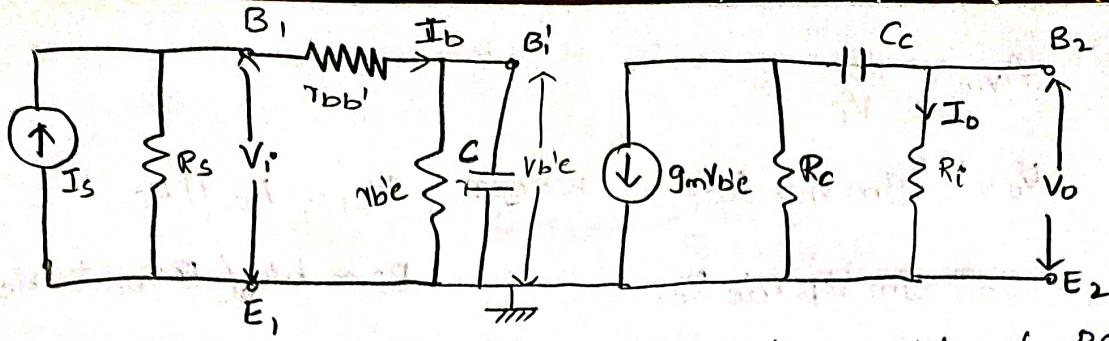
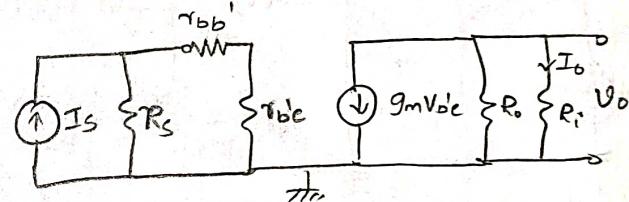


fig: Simplified equivalent ckt of RC coupled Amplifier ~~at mid frequency.~~

* Mid-frequency range:-

- In mid-frequency range, the reactance offered by C_c is small enough so that it can be omitted.
- the frequency is small enough to make shunt capacitance-reactance ($X_C = \frac{1}{\omega C}$) extremely large.
- ⇒ Hence, 'C' can be omitted in the equivalent circuit.
- ⇒ Let I_o be the current through the resistance R_i . This current I_o is useful output current from the first stage and forms the input current for the next stage.

i) current gain - $A_{Im} = \frac{I_o}{I_b}$



$$I_o = -g_m V_{be} \cdot \frac{R_i}{R_i + R_o}$$

fig: Simplified equivalent ckt of RC coupled amplifier at mid freq

$$A_{Im} = -g_m V_{be} \cdot \frac{R_i}{R_i + R_o}$$

$$\text{But, } V_{be} = I_b \cdot r_{be}$$

Hence,

$$A_{Im} = -g_m r_{be} \cdot \frac{R_i}{R_i + R_o}$$

$g_m r_{be} \approx h_{fe}$ [simplified using approximate model]

$$\therefore A_{Im} = -h_{fe} \cdot \frac{R_i}{R_i + R_o}$$

a) Voltage gain:- $A_{Vm} = \frac{V_o}{V_i}$

$$V_o = -g_m V_{be} R_{ci} \quad \text{where, } R_{ci} = R_c // R_i$$

$$= -g_m I_b r_{bb}' R_{ci} \quad R_i \approx h_{ie} (R_r r_{bb}' + r_{be})$$

$$V_i = I_b (r_{bb}' + r_{be})$$

$$V_i = I_b h_{ie}$$

Hence,

$$A_{Vm} = \frac{V_o}{V_i} = \frac{-g_m V_{be} R_{ci}}{I_b h_{ie}} = \frac{-g_m I_b r_{bb}' R_{ci}}{I_b h_{ie}}$$

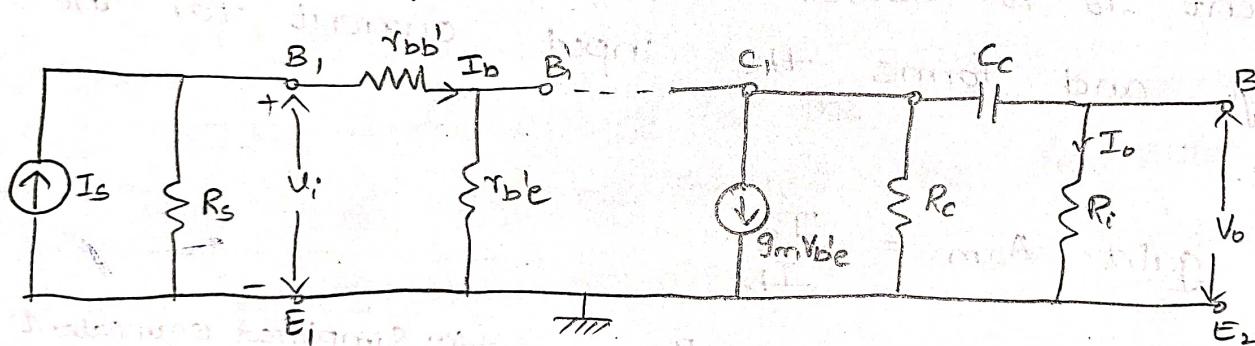
Substituting $g_m r_{be} \approx h_{fe}$ then,

$$A_{Vm} = \frac{-h_{fe} \cdot R_{ci}}{h_{ie}}$$

* Low frequency range:-

In low frequency range capacitor 'C' is omitted since its reactance is extremely large as $X_C // r_{be} \approx r_{be}$.

→ However, C_C capacitor cannot be neglected.



i) Current gain:- $A_{II} = \frac{I_o}{I_b}$

From fig:-

$$I_o = -g_m V_{be} \cdot \frac{R_C}{R_C + (R_i - jX_C)}$$

$$= -g_m V_{be} \cdot \frac{R_C}{R_C + R_i + \frac{1}{j\omega_C}}$$

$$= -g_m V_{be} \cdot \frac{R_C}{R_C + R_i + \frac{1}{j\omega_C}}$$

$$-g_m V_{be} \cdot R_C$$

$$A_{II} =$$

$$A_{IL} = -\frac{g_m \gamma_b e}{J_b} \cdot \frac{R_c}{R_c + R_i + \frac{1}{j\omega C_c}}$$

$$A_{IL} = -g_m \gamma_b e \cdot \frac{R_c}{R_c + R_i + \frac{1}{j\omega C_c}}$$

$$= -h_{fe} \cdot \frac{R_c}{R_c + R_i + \frac{1}{j\omega C_c}}$$

{ dividing n & d with
\$R_c + R_i\$}

$$= -h_{fe} R_c \cdot \frac{R_c + R_i^o}{R_c + R_i + \frac{1}{j\omega C_c}}$$

$$= A_{Im} \cdot \frac{R_c + R_i^o}{R_c + R_i + \frac{1}{j\omega C_c}}$$

$$= A_{Im} \cdot \frac{R_c + R_i}{R_c + R_i} \left[\frac{1}{1 + \frac{1}{j\omega C_c (R_c + R_i)}} \right]$$

$$= A_{Im} \left[\frac{1}{1 - \frac{1}{j\omega C_c (R_c + R_i)}} \right]$$

$$= A_{Im} \left[\frac{1}{1 - \frac{1}{j2\pi f C_c (R_c + R_i)}} \right]$$

$$A_{IL} = \frac{A_{Im}}{1 - \frac{jf_L}{f}} \quad \text{where, } f_L = \frac{1}{2\pi C_c (R_c + R_i)}$$

$$|A_{IL}| = \frac{|A_{Im}|}{\sqrt{1 + (f_L/f)^2}}$$

*) The phase angle of current gain at any freq 'f' is given by,

$$\phi_{L^o} = \text{phase angle of } A_{Im} + \tan^{-1}(f_L/f)$$

$$= 180^\circ + \tan^{-1}(f_L/f)$$

$$\text{At, } f_L = f \text{ then, } |A_{IL}| = \frac{|A_{Im}|}{\sqrt{1+(1)^2}} = \frac{|A_{Im}|}{\sqrt{2}} = 0.707 |A_{Im}|$$

Thus, f_L forms the lower 3 dB frequency for the current gain.

i) Voltage gain:-

$$A_{V_L} = \frac{V_o}{V_i}$$

$$V_o = I_o \cdot R_i$$

$$= -g_m r_{b'e} \cdot I_b \cdot \frac{R_c R_i}{R_{ct} + R_i + \frac{1}{j\omega C_c}}$$

$$V_i = I_{bb} (r_{bb'} + r_{b'e}) \approx I_{bb} \cdot h_{ie}$$

$$A_{V_L} = \frac{V_o}{V_i} = -\frac{g_m r_{b'e} \cdot R_i}{I_{bb} \cdot h_{ie}} \left(\frac{R_c R_i}{R_{ct} + R_i + \frac{1}{j\omega C_c}} \right)$$

Substituting $g_m r_{b'e} = h_{ie}$ we get,

$$A_{V_L} = -\frac{h_{fe}}{h_{ie}} \cdot \frac{R_c R_i}{R_{ct} + R_i + \frac{1}{j\omega C_c}} \\ = -\frac{h_{fe}}{h_{ie}} \left(\frac{R_c R_i}{R_{ct} + R_i} \right) \left[\frac{1}{1 + \frac{1}{j\omega C_c (R_{ct} + R_i)}} \right]$$

$$R_{ct} \parallel R_i = R_{ci},$$

$$= -\frac{h_{fe}}{h_{ie}} \cdot R_{ci} \left[\frac{1}{1 - \frac{j}{\omega C_c (R_{ct} + R_i)}} \right]$$

$$= -\frac{h_{fe} R_{ci}}{h_{ie}} \left[\frac{1}{1 - \frac{j}{2\pi f C_c (R_{ct} + R_i)}} \right]$$

where,

$$f_L = \frac{1}{2\pi C_c (R_{ct} + R_i)}$$

$$A_{V_L} = A_{Vm} \left(\frac{1}{1 - \frac{j f_L}{f}} \right)$$

$$|A_{rel}| = \frac{|A_{Vm}|}{\sqrt{1 + (f_L/f)^2}}$$

* The phase angle of voltage gain

$$\begin{aligned}\phi_{V_L} &= \text{phase angle of } A_{Vm} + \tan^{-1}(-f_L/f) \\ &= 180^\circ + \tan^{-1}(-f_L/f)\end{aligned}$$

At $f = f_L$,

$$|A_{rel}| = \frac{|A_{Vm}|}{\sqrt{2}} = 0.707 |A_{Vm}|$$

Thus, f_L forms the lower 3dB frequency for the voltage gain. Since in both derivations ' f_L ' ~~res~~ are same.

* High frequency range:-

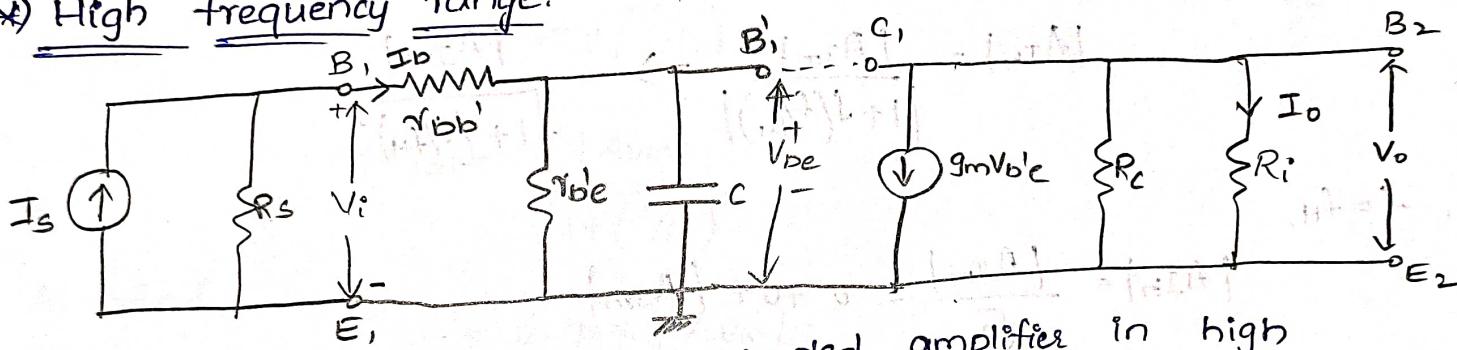


fig: Equivalent circuit of RC coupled amplifier in high frequency range

* In this frequency range coupling capacitance 'C' is neglected since its reactance is small, whereas the shunt capacitance 'C' cannot be neglected to the equivalent ckt.

i) Current Gain:-

$$I_o = -g_m V_{be} \cdot \frac{R_c}{R_c + R_i}$$

where,

$$V_{be} = \frac{I_b}{\frac{1}{r_{be}} + j\omega C} = \frac{I_b \cdot r_{be}}{1 + j\omega C r_{be}}$$

$$\therefore I_o = -g_m \frac{I_b \cdot r_{be}}{1 + j\omega C r_{be}} \cdot \frac{R_c}{R_c + R_i}$$

* Hence, current gain in high frequency range,

$$\begin{aligned} A_{Ih} &= \frac{I_o}{I_b} = -g_m r_{be} \frac{i(R_{ci})}{R_c + R_i} \cdot \frac{1}{1+j\omega C_{rbe}} \\ &= -h_{fe} \cdot \underbrace{\frac{R_c}{R_c + R_i}}_{A_{Im}} \cdot \frac{1}{1+j\omega C_{rbe}} \\ &= A_{Im} \cdot \frac{1}{1+j\omega C_{rbe}} \end{aligned}$$

let,

$$f_H = \frac{1}{2\pi C_{rbe}} \quad \text{then,}$$

$$A_{Ih} = \frac{A_{Im}}{1+j(f/f_H)}$$

$$|A_{Ih}| = \frac{|A_{Im}|}{|1+j(f/f_H)|} \Rightarrow \frac{|A_{Im}|}{\sqrt{1+(f/f_H)^2}}$$

If, $f = f_H$,

$$|A_{Ih}| = \frac{|A_{Im}|}{\sqrt{2}} = 0.707 |A_{Im}|$$

Hence,

' f_H ' forms the upper 3 dB frequency.

* Phase angle of current gain at any frequency f is

given by,

$$\begin{aligned} \phi_{Ih} &= \text{phase angle of } A_{Im} - \tan^{-1}(f/f_H) \\ &= 180^\circ + \tan^{-1}(f/f_H) \end{aligned}$$

ii) Voltage gain:

$$V_o = -g_m V_{be} \cdot R_{ci} \quad R_{ci} (R_c || R_i)$$

$$V_o = -g_m R_{ci} \cdot \frac{I_b}{\frac{1}{r_{be}} + j\omega C_{rbe}}$$

$$= -g_m R_{ci} \frac{r_{be} I_b}{1 + j\omega C_{rbe}}$$

Substituting $g_m r_{be} = h_{fe}$

$$V_o = -h_{fe} \frac{R_{ci} I_b}{1 + j\omega C r_{be}}$$

$$V_i = I_b h_{ie}$$

$$h_{ie} = r_{bb'} + r_{be}$$

$$A_{Vi} = \frac{V_o}{V_i} = -\frac{h_{fe}}{h_{ie}} \cdot \frac{R_{ci}}{1 + j\omega C r_{be}}$$

Since, $A_{Vm} = -\frac{h_{fe} R_{ci}}{h_{ie}}$ then, $A_{Vi} = \frac{A_{Vm}}{1 + j\omega C r_{be}}$

$$= \frac{A_{Vm}}{1 + j2\pi f C r_{be}}$$

$$= \frac{A_{Vm}}{1 + j(f/f_H)}$$

$$\text{where, } f_H = \frac{1}{2\pi C r_{be}}$$

$$|A_{Vi}| = \frac{|A_{Vm}|}{\sqrt{1 + (f/f_H)^2}}$$

$$\text{At, } f = f_H$$

$$= 0.707 |A_{Vm}|$$

Thus, f_H forms the upper 3 dB frequency.
*) Phase angle of Voltage gain at any frequency 'f'

$$\phi_{Vi} = \text{phase angle of } A_{Vm} = +\tan^{-1}(f/f_H)$$

$$= 180^\circ - \tan^{-1}(2\pi f C r_{be})$$

since, $f_H = \frac{1}{2\pi C r_{be}}$ in both cases, upper 3 dB frequencies of A_{Im} and A_{Vi} are the same. (freq response graph)

*) Gain-Bandwidth product :-

It is for the current gain,

$$|A_{Im} \cdot f_H| = h_{fe} \cdot \frac{R_c}{R_c + R_i} \cdot \frac{1}{2\pi C r_{be}}$$

$$= \frac{g_m r_{be}}{2\pi C r_{be}} \cdot \frac{R_c}{R_c + R_i}$$

$$= \frac{g_m}{2\pi C} \cdot \frac{R_c}{R_c + R_i}$$

for Voltage gain,

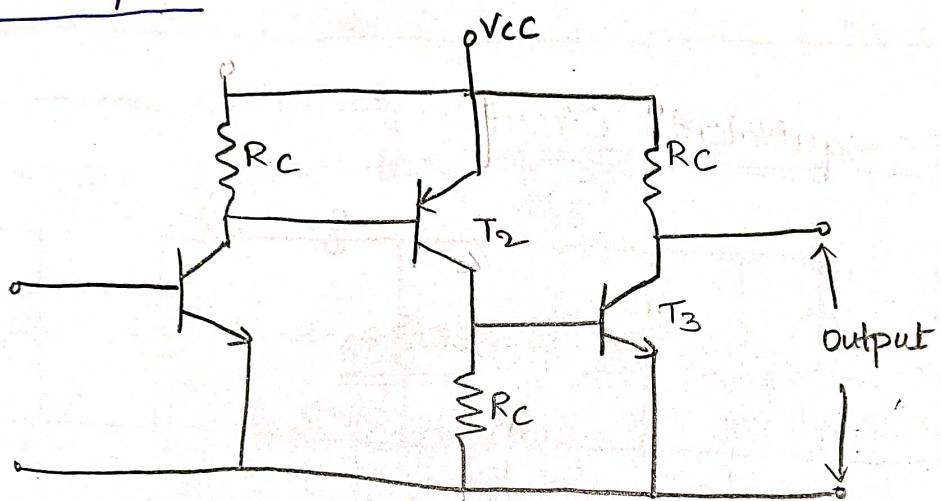
$$|A_{Vm-f_H}| = \frac{h_{fe}}{h_{ie}} \cdot R_{ci} \cdot \frac{1}{2\pi C r_{be}}$$

$$= \frac{g_m}{2\pi C} \cdot \frac{R_{ci}}{h_{ie}}$$

Direct-Coupled Amplifier:-

* As no coupling devices are used, the coupling of the amplifier stages is done directly and hence called as "Direct-Coupled Amplifier".

Construction:-



- * The figure below indicates the three stage direct coupled transistor amplifier.
- * The output of first stage T₁ is connected to the input of second stage transistor T₂.

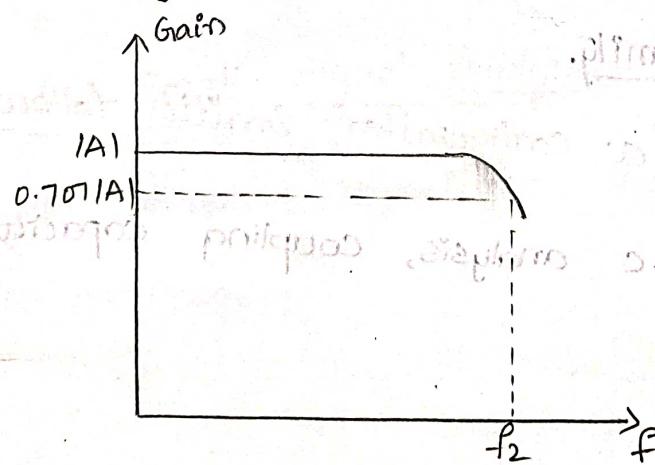
- * The transistor in the first stage is NPN transistor, while the transistor in the next stage is PNP and so on.
- * This is because, the variations in one transistor tend to cancel the variations in the other. The rise in the collector current and the variation in B of one transistor gets cancelled by the decrease in the other.

Operation:-

- * The input signal when applied at the base of transistor T_1 , it gets amplified due to the action and the amplified output appears at the collector resistor R_c of the transistor T_1 .
- * This output is applied to the base of transistor T_2 which further amplifies the signal. In this way, a signal is amplified in a direct coupled amplifier circuit.

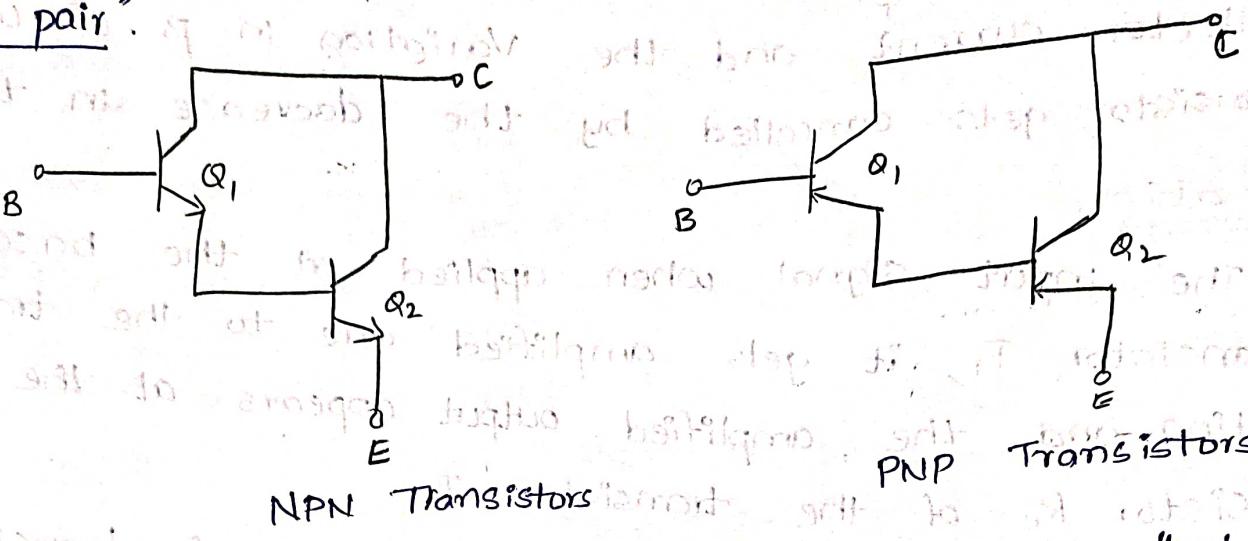
Frequency Response:-

- * The frequency response curve of a directly coupled amplifier is flat up to an upper cut-off frequency f_2 . After this cut-off frequency, stray wiring capacitances and internal transistor capacitances play their role.
- * As a result the gain falls.



Principle of Darlington Amplifier

* In order to increase the overall values of circuit current gain and input impedance, two transistors are connected in series in CC Configuration. Such circuit is called as "Darlington pair".



- * The Darlington transistor acts like a single transistor that has high current gain and high input impedance.
- * Note that emitter of first transistor is connected to the base of the second transistor and the collector terminals of the two transistors are connected together.
- * The result is that emitter current of the first transistor is the base current of the second transistor.
- * Therefore the current gain of the pair is equal to the product of individual current gains i.e., $B = B_1 B_2$.
- * It can be shown that the voltage gain of the Darlington pair is less than unity.

Darlington Amplifier:- (CC configuration, Emitter follower)

- 1) DC Analysis:- In D.C analysis, coupling capacitors acts as open circuited.

where,

$$Z_b \approx \beta_D \left[\frac{1}{g_m} + R_E \right]$$

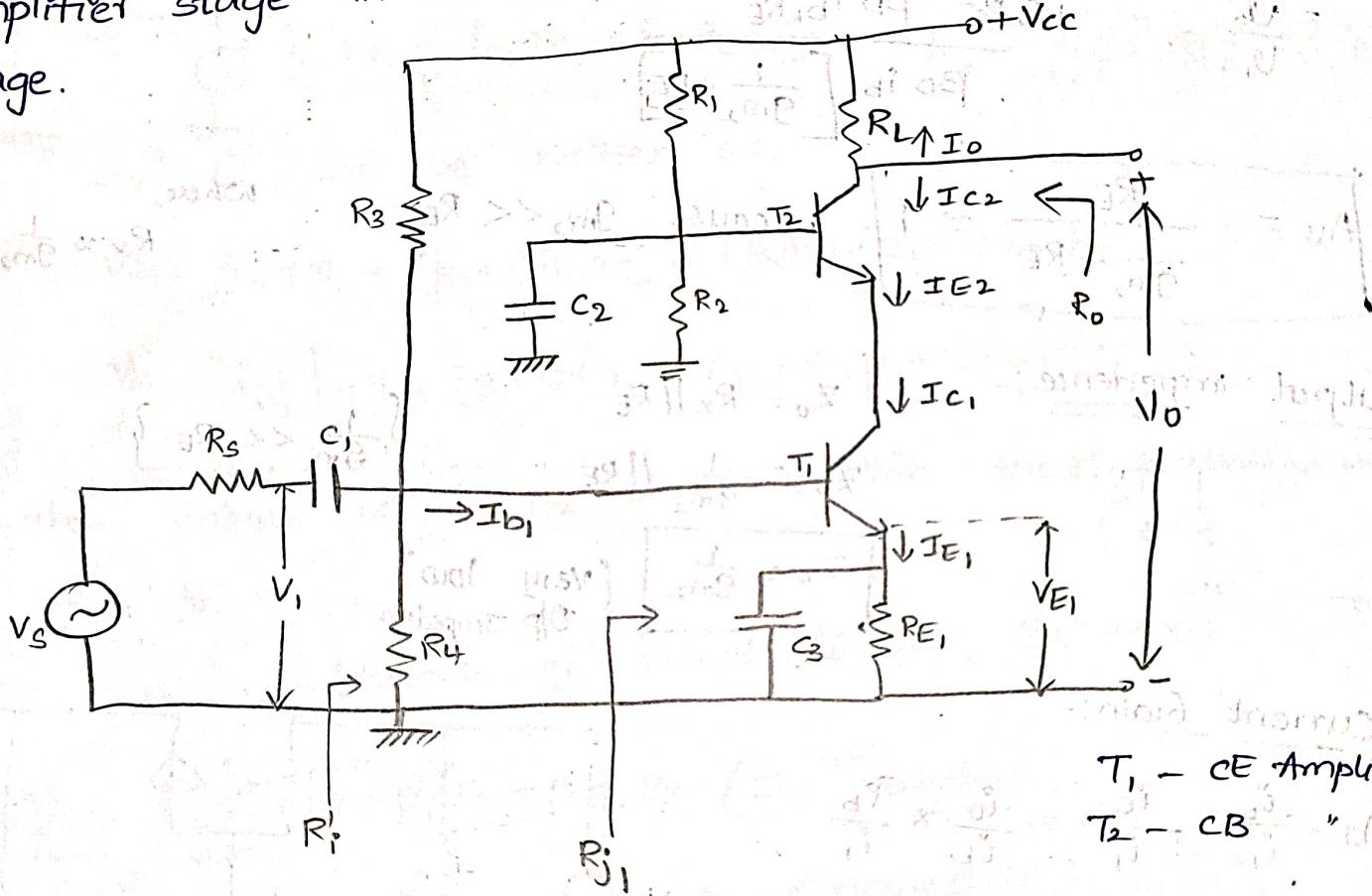
$$A_i = \frac{i_o}{i_b} \times \frac{i_b}{i_{in}}$$

$$A_i = \beta_D \times \frac{R_B}{R_B + Z_b} \Rightarrow \text{if } R_B = Z_b \text{ then, } A_i = \frac{\beta_D}{2}$$

* A_i is high, A_v is low.

Cascode Amplifier:-

* The cascode amplifier consists of a common emitter amplifier stage in series with a common base amplifier stage.



T₁ - CE Amplifier

T₂ - CB " "

* It is one approach to solve loco impedance problem of a common base circuit.

* It gives high input impedance of a common emitter amplifier, as well as the good voltage gain and high frequency performance of a common base circuit.

Dc Analysis:- (o.c. capacitors & AC supply)

- * For the dc bias conditions of the circuit, it is seen that the emitter current for T_1 is set by V_E , and R_E .
- * Collector current I_C , approximately equals I_{E1} , and I_{E2} is same as I_{C2} .
- * Therefore, I_{C2} approximately equals I_{E1} . This current remains constant regardless of the level of V_{B2} , as long as V_{CE} remains large enough for current operation of T_1 .

Ac Analysis:-

- * For AC equivalent circuit, short circuit the dc supply and capacitors.

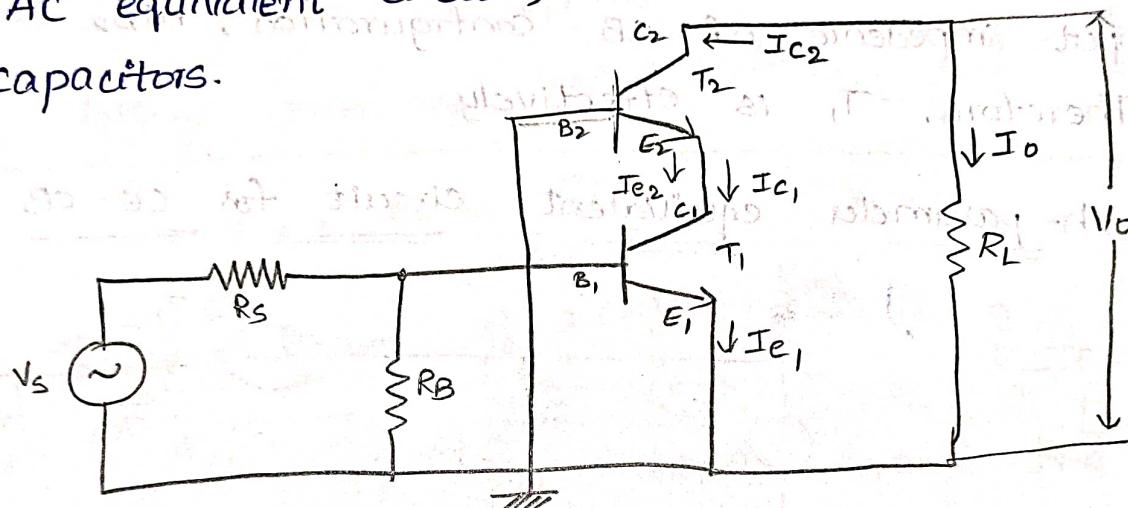
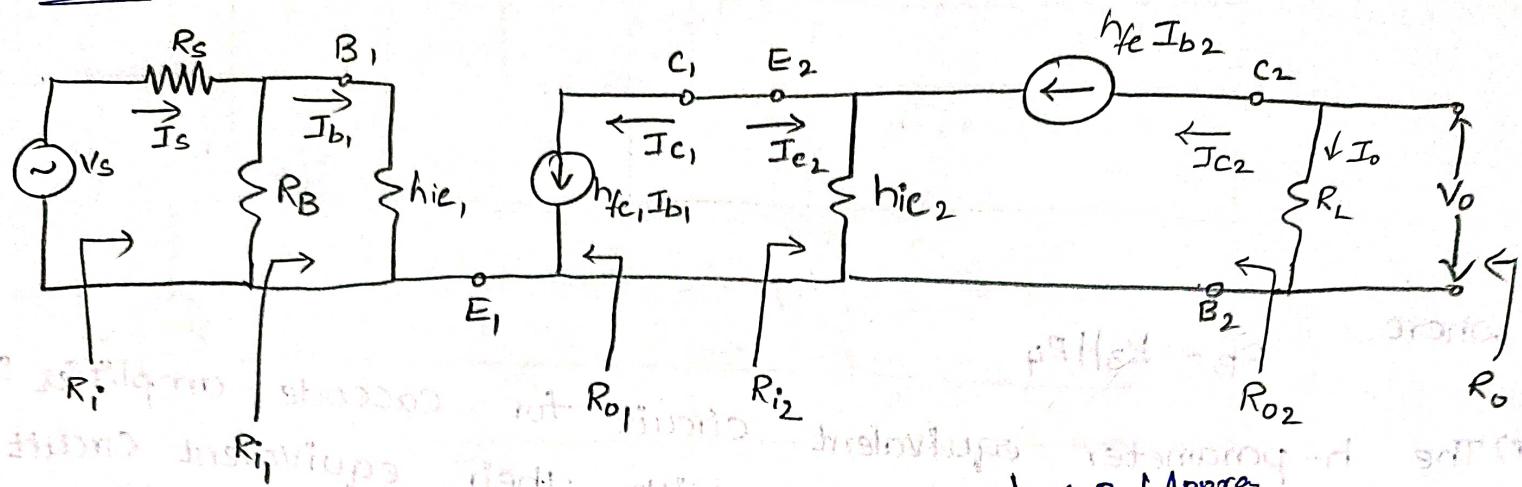


Fig: AC equivalent

Where,

$$R_B = R_3 \parallel R_4$$

Simplified h-parameter equivalent circuit for CE-CB amplifier



Analysis of Second stage (CB amplifier)

$$\Rightarrow \text{Current gain: } A_{I2} = \frac{h_{fe}}{1+h_{fe}}$$

$$\text{Input resistance: } R_{i2} = \frac{h_{ie}}{1+h_{fe}}$$

$$\text{Voltage gain: } A_{V2} = \frac{A_{I2} R_{L2}}{R_{i2}}$$

LB (Appra

$$A_I = -h_{fb} = \frac{h_{fe}}{1+h_{fe}}$$

$$R_i = h_{ib} = \frac{h_{ie}}{1+h_{fe}}$$

$$A_V = \frac{A_I R_L}{R_i} = \frac{h_{fe} R_L}{h_{ie}}$$

$$R_o = \infty$$

Analysis of first stage (ce Amplifier)

a) Current gain (A_{I_1}) = $-h_{fe}$

b) Input resistance (R_{i_1}) = h_{ie}

c) Voltage gain (A_{V_1}) = $\frac{A_{I_1} R_L}{R_{i_1}}$

CE (Apprx)

$$A_I = -h_{fe}$$

$$R_i \approx h_{ie}$$

$$A_v = \frac{h_{fe} R_L}{h_{ie}}$$

$$R_o = \infty$$

*) Overall Voltage gain, $(\frac{V_o}{V_i})$

$$A_v = A_{V_1} \times A_{V_2}$$

*) Overall Input resistance,

$$R_i = R_{i_1} \parallel R_B$$

*) Overall Voltage gain of entire S/m, $(\frac{V_o}{V_s}) = \frac{V_o}{V_i} \times \frac{V_i}{V_s}$

$$= A_{Vs} = A_v \times \frac{R_i}{R_i + R_s}$$

S/m,

*) Overall current gain of entire

$$A_{Is} = \frac{I_o}{I_s} = \frac{I_o}{I_{C_2}} \times \frac{I_{C_2}}{I_{e_2}} \times \frac{I_{e_2}}{I_{C_1}} \times \frac{I_{C_1}}{I_{b_1}} \times \frac{I_{b_1}}{I_s}$$

where,

$$I_o = -I_{C_2}; \quad \frac{I_{C_2}}{I_{e_2}} = -A_{I_2}; \quad \frac{I_{e_2}}{I_{C_1}} = -I_{C_1}; \quad \frac{I_{C_1}}{I_{b_1}} = -A_{I_1}$$

$$\frac{I_{b_1}}{I_s} = \frac{R_B}{R_B + R_{i_1}}$$

*) O/P resistance,

$$R_{o_1} = \infty$$

$$R_{o_2} = \infty$$

$$R_o = R_{o_2} \parallel R_L \approx R_L$$