

GATE-EC2021,31

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1. The propagation delays of the XOR gate, AND gate and multiplexer (*MUX*) in the circuit shown in the figure are $4ns$, $2ns$ and $1ns$, respectively. If all the inputs P, Q, R, S and T are applied simultaneously and held constant, the maximum propagation delay of the circuit is

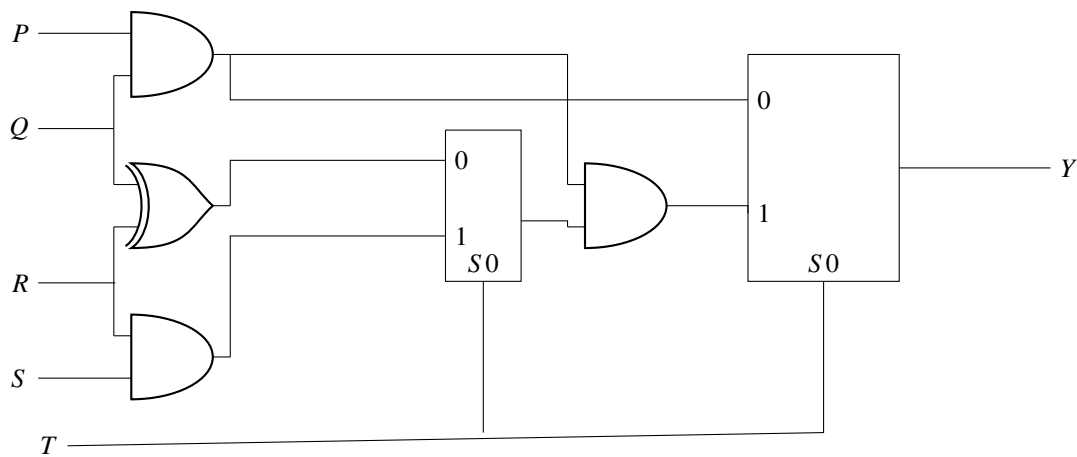


Figure 1: Circuit Diagram

1. $3ns$
2. $5ns$
3. $6ns$
4. $7ns$