CDAC

Centre for Development of Advanced Computing
(Scientific Society of the Ministry of Communications and Information Technology, Government of India)

CERTIFICATE

This is to certify that

Rohan Shrikant Kulkarni

has completed 24 weeks full time Post Graduate Diploma course offered at

C-DAC's Advanced Computing Training School, Pune

This course has the following compulsory modules:

- *Advanced Digital Design*
- *System Architecture*
- *CMOS VLSI Design*
- *High Level Design Methodology (HDL, VHDL, Synthesis)*
- *Verilog (In accordance with IEEE 1364-2005 and 2005)*
- *Programming Fundamentals & Linux Shell Scripting*
- *Aptitude & General English and Business Communication*
- *Project*

This course was conducted during the period February 17, 2015 to August 03, 2015. Rohan Shrikant Kulkarni has been examined and found proficient in the above modules and has been conferred

PG Diploma in VLSI Design

with Grade **A**.

Grade Scale : A+ >=80%, A >=70% to <80%, B >=60% to <70%, C >=50% to <60%, D >=40% to <50%, F <40%

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Date: August 03, 2015

Place : Pune