Appendix A. Project Timeplan

	Timeplan										
Project:	The Unlimited Well										
Project group:	Company 10		Approved:								
Orderer: Course:	Central Operative & Independent Retailers Acquirer (Coira)									
Course:	EDA382										
Date:	2010-02-02										
Version:	1										
Responsible:	Sebastian Rehnby										
	ACTIVTIES AND MILESTONES	TIME	WHO	٧	VHI	ΞN	(we	ek	nur	nbe	ers)
# Description			Initials	3	4	5	6	7	8	9	
 Organise project te Assign project management 		28	All	12					\vdash		(
1.2 Implement group			All	12					П		12
1.3 Detail project org	anisational structure	4		4					П		4
	rganisation defined and approved ckground and goals	48	1	_					\vdash		(
2.1 Create backgrou			VS/JM/HN	16					П		16
2.2 Describe aims ar	nd goals of the project	20	VS/JM/HN	20							20
2.3 Identify decision2.4 Identify project decision	gates and deadlines		SR SR	6					\vdash		6
	Directives verified and approved	0	SK	- 0	1				П		6
3. Project planning		60							口		(
	e and time allocation		SR/VS SR/VS	\perp	14		<u> </u>		Ы	4	14
3.2 Define meeting a 3.3 Define project ac	and coordination plan tivities		SR/VS SR/VS	+	12 6		<u> </u>	H	\dashv	\dashv	12
M3: Project a	ctivities defined								二		(
	esponsibilites throughout project		SR SBA/S	4		Ĺ		H	屽	_]	8
3.5 Define and verify M4: Project p	milestones lan verified and approved	20	SR/VS	+	20	\vdash	<u> </u>	H	\dashv	\dashv	20
4. Analyse system rec	quirements	178									(
4.1 Analyse project of			MR/HS/JM	20	24	00			Ш		44
	system requirements subsystem requirements		MR/HS/JM MR/HS/JM	-		30 16			\vdash		38 24
	ional system requirements		MR/HS/JM		8						24
4.5 Define use cases			HN		8				П		24
4.6 Prioritize requirer	ments ed Software Requirements Specification	24	MR/HS/Client	+	4	20			\vdash		24
5. Development plann		64									(
5.1 Define developm			SR/MR		4				П		8
5.2 Define developm	ent activities ment process defined and approved	20	SR/MR	-		4	16		\vdash		20
	each development activity	14	SR/MR			4	10				14
	mate time resources for tasks		SR				8		П		8
	at o phases according to requirements P0-Px ent timeline and deliverables		SR/MR SR/MR	-	<u> </u>		10		\vdash		10
	ment Plan verified and approved		Orthurt								(
6. System design		82				L.,			П		(
	key design choices. Platforms, languages etc		MR/HS MR/HS	-	<u> </u>	10	10 20		\vdash		20
	ntational phases according to system requirements		MR/HS				10				10
	pecification completed and approved		MD#IO				_		Ш		(
	ependencies within the design nplementation for system/subsystem components		MR/HS MR/HS	-	<u> </u>		6		\vdash		6
	ntation proposal completed	Ť					Ŭ				(
	s based on implementation proposal		VS VC/MP/US				8		H	\exists	8
	ses according to implementational phases in design specification an completed and approved	12	VS/MR/HS	+	_	\vdash	12	H	\dashv	\dashv	12
7. System implementa	ation & testing	202							二		(
7.1 Analyse impleme			MR/HS/JM/HN	-		<u> </u>		16			16
	m components according to proposal and phases defined, Phase X tests for phase X		MR/HS/JM/HN/SR VS	+					60 10	_	140
7.4 Execute Phase X		8	VS					4	4		-8
7.5 Analyse test resu			VS		L	igsqcut		2	2	\Box	4
	orts and feedback to developers testing for Phase X		VS VS	+	<u> </u>	1	_	6 2	4	\dashv	10
	implementation completed			ᆂ	L	L		Ľ	力		(
	testing and verification successfully completed				L	igsqcut		\Box	ДŢ	\Box	(
 System demonstati Deploy system in 	on demo environment	52 12	MR/HS/JM/HN	+	_	\vdash	<u> </u>	H	\dashv	12	12
8.2 Prepare promotion	onal material & documentation	32	SR/MR	1					12		32
	client and additional stakeholders	8	SR	+	L	Ĺ		H	屽	8	8
9. Documentation	demonstation completed	46		+	1				\dashv	-	(
9.1 Collect and pack	age system documentation	4	HN/VS/JM	上					4		4
9.2 Produce final Pro			All		L	igsqcut		\Box	22		22
	documentation to client and stakeholders documentation finalized and approved	4	MR/HS	+	1		-		\dashv	4	(
	tem project analysis and evaluation	16	All	士	L	Ħ	L		一	16	10
M15: Project	completed, entering maintenance mode								أت		(
WITS. FTOJECE		760							120		760