ADC’s Power: 35 mW per channels

 LTC2141-12

TDC

SPI IP Core

Microcontroller

FPGA’s Power Estimate: <5mW per channel

~40MHz

Some Logics

Discriminator

Some Logics 2

Header FIFO

DataFIFO

FPGA (iCE40LP1K)

ADC1[11:0]

ADC2[11:0]

ADC

Pulse Shaper

PMT